<table>
<thead>
<tr>
<th>REV</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>A4</td>
<td>* Changed PHY to DP83867IRRGZ</td>
</tr>
<tr>
<td></td>
<td>* Changed RJ45 to 0845-2D1T-E4</td>
</tr>
<tr>
<td></td>
<td>* Changed micro SD Connector</td>
</tr>
</tbody>
</table>

**Date:** 07/17/2018

**By:** TI
* BOOT0=0: SMPS3 = 1.35V (DDR3L)
  BOOT0=1: SMPS3 = 1.50V (DDR3)

* BOOT1=0: Single PORz release at startup, no PORz during warm reset
  BOOT1=1: Double PORz release at startup, PORz assertion during warm reset

Insert wire here to enable use of the JTAG without boot image present.
Preliminary Information - Subject to change
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