LOW INPUT VOLTAGE SYNCHRONOUS BOOST CONVERTER
WITH LOW QUIESCENT CURRENT

Check for Samples: TPS61097

FEATURES
- Up to 95% Efficiency at Typical Operating Conditions
- Connection from Battery to Load via Bypass Switch in Shutdown Mode
- Typical Shutdown Current Less Than 5 nA
- Typical Quiescent Current Less Than 5 μA
- Operating Input Voltage Range
  From 0.9 V to 5.5 V
- Power-Save Mode for Improved Efficiency at Low Output Power
- Overtemperature Protection
- Small 2.8-mm x 2.9-mm 5-Pin SOT-23 Package (6-Pin for Adjustable)

APPLICATIONS
- MSP430 Applications
- All Single-Cell, Two-Cell, and Three-Cell Alkaline, NiCd, NiMH, or Single-Cell Li-Battery Powered Products
- Personal Medical Products
- Fuel Cell and Solar Cell Powered Products
- PDAs
- Mobile Applications
- White LEDs

DESCRIPTION
The TPS61097 provide a power supply solution for products powered by either a single-cell, two-cell, or three-cell alkaline, NiCd, or NiMH, or one-cell Li-Ion or Li-polymer battery. They can also be used in fuel cell or solar cell powered devices where the capability of handling low input voltages is essential. Possible output currents depend on the input-to-output voltage ratio. The devices provide output currents up to 100 mA at a 3.3-V output while using a single-cell Li-Ion or Li-Polymer battery. The boost converter is based on a current-mode controller using synchronous rectification to obtain maximum efficiency. The maximum average input current is limited to a value of 350 mA. The output voltage can be programmed by an external resistor divider, or it is fixed internally on the chip. The converter can be disabled to minimize battery drain. During shutdown, the battery is connected to the load to enable battery backup of critical functions on the load. The fixed output device is packaged in a 5-pin SOT-23 package (DBV) measuring 2.8 mm × 2.9 mm.
**ORDERING INFORMATION**

<table>
<thead>
<tr>
<th>TA</th>
<th>PACKAGE</th>
<th>ORDERABLE PART NUMBER</th>
<th>TOP-SIDE MARKING</th>
</tr>
</thead>
<tbody>
<tr>
<td>–40°C to 85°C</td>
<td>5-pin SOT-23 – DBV</td>
<td>Reel of 3000</td>
<td>TPS61097-33DBVR</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Reel of 250</td>
<td>TPS61097-33DBVT</td>
</tr>
</tbody>
</table>

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.
(2) Contact the factory for availability of other fixed output voltage versions.
(3) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.

**ABSOLUTE MAXIMUM RATINGS**

over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Description</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>V&lt;sub&gt;I&lt;/sub&gt;</td>
<td>Input voltage range VIN, L, VOUT, EN, FB</td>
<td>–0.3 V</td>
<td>7 V</td>
<td></td>
</tr>
<tr>
<td>I&lt;sub&gt;sc&lt;/sub&gt;</td>
<td>Short-circuit current</td>
<td>400 mA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>T&lt;sub&gt;J&lt;/sub&gt;</td>
<td>Junction temperature range</td>
<td>–40°C</td>
<td>150°C</td>
<td></td>
</tr>
<tr>
<td>T&lt;sub&gt;stg&lt;/sub&gt;</td>
<td>Storage temperature range</td>
<td>–65°C</td>
<td>150°C</td>
<td></td>
</tr>
<tr>
<td>ESD</td>
<td>Electrostatic discharge rating Human-Body Model (HBM)</td>
<td></td>
<td>2000 V</td>
<td></td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
(2) ESD testing is performed according to the respective JESD22 JEDEC standard.

**DISSIPATION RATINGS TABLE**

<table>
<thead>
<tr>
<th>PACKAGE</th>
<th>THERMAL RESISTANCE θ&lt;sub&gt;JA&lt;/sub&gt;</th>
<th>POWER RATING T&lt;sub&gt;A&lt;/sub&gt; ≤ 25°C</th>
<th>DERATING FACTOR ABOVE T&lt;sub&gt;A&lt;/sub&gt; = 25°C</th>
</tr>
</thead>
<tbody>
<tr>
<td>DBV</td>
<td>255°C/W</td>
<td>390 mW</td>
<td>-3.92 mW/°C</td>
</tr>
</tbody>
</table>

**RECOMMENDED OPERATING CONDITIONS**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Description</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>V&lt;sub&gt;IN&lt;/sub&gt;</td>
<td>Supply voltage at VIN</td>
<td>0.9 V</td>
<td>5.5 V</td>
<td></td>
</tr>
<tr>
<td>V&lt;sub&gt;OUT&lt;/sub&gt;</td>
<td>Adjustable output voltage</td>
<td>1.8 V</td>
<td>5.5 V</td>
<td></td>
</tr>
<tr>
<td>T&lt;sub&gt;A&lt;/sub&gt;</td>
<td>Operating free air temperature range</td>
<td>–40 °C</td>
<td>85 °C</td>
<td></td>
</tr>
<tr>
<td>T&lt;sub&gt;J&lt;/sub&gt;</td>
<td>Operating junction temperature range</td>
<td>–40 °C</td>
<td>125 °C</td>
<td></td>
</tr>
</tbody>
</table>
### ELECTRICAL CHARACTERISTICS

over recommended free-air temperature range and over recommended input voltage range (typical at an ambient temperature range of 25°C) (unless otherwise noted)

#### DC/DC STAGE

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{IN} ) Input voltage</td>
<td></td>
<td>0.9</td>
<td>5.5</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>( V_{OUT} ) TPS61097-33</td>
<td>( V_{IN} = 1.2 , V , , I_{OUT} = 10 , mA )</td>
<td>3.20</td>
<td>3.30</td>
<td>3.40</td>
<td>V</td>
</tr>
<tr>
<td>( I_{SW} ) Switch current limit</td>
<td>( V_{OUT} = 3.3 , V )</td>
<td>200</td>
<td>350</td>
<td>475</td>
<td>mA</td>
</tr>
<tr>
<td>Rectifying switch on resistance</td>
<td>( V_{OUT} = 3.3 , V )</td>
<td>1.0</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Main switch on resistance</td>
<td>( V_{OUT} = 3.3 , V )</td>
<td>1.0</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Bypass switch on resistance</td>
<td>( V_{IN} = 1.2 , I_{OUT} = 100 , mA )</td>
<td>3.4</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>Line regulation</td>
<td>( V_{IN} &lt; V_{OUT} , , V_{IN} = 1.2 , V , \text{to} , 1.8 , V , , I_{OUT} = 10 , mA )</td>
<td>0.5%</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Load regulation</td>
<td>( V_{IN} &lt; V_{OUT} , , I_{OUT} = 10 , mA , \text{to} , 50 , mA , , V_{IN} = 1.8 , V )</td>
<td>0.5%</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( I_{Q} ) Quiescent current</td>
<td>( V_{IN} \quad V_{OUT} )</td>
<td>( I_{Q} = 0 , mA , , V_{EN} = V_{IN} = 1.2 , V , , V_{OUT} = 3.4V )</td>
<td>1</td>
<td>2.5</td>
<td>μA</td>
</tr>
<tr>
<td>( I_{SD} ) Shutdown current</td>
<td>( V_{IN} )</td>
<td>( V_{EN} = 0 , V , , V_{IN} = 1.2 , V , , I_{OUT} = 0 , mA )</td>
<td>0.005</td>
<td>0.15</td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{EN} = 0 , V , , V_{EN} = 3 , V , , I_{OUT} = 0 , mA )</td>
<td>0.005</td>
<td>0.15</td>
<td>μA</td>
</tr>
<tr>
<td>Leakage current into ( L )</td>
<td>( V_{EN} = 0 , V , , V_{IN} = 1.2 , V , , V_{L} = 1.2 , V )</td>
<td>0.01</td>
<td>1</td>
<td></td>
<td>μA</td>
</tr>
</tbody>
</table>

#### CONTROL STAGE

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{IL} ) Logic low level, EN falling edge</td>
<td>( EN = 0 , V , \text{or} , EN = V_{IN} )</td>
<td>0.01</td>
<td>0.1</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>( V_{IH} ) Logic high level, EN rising edge</td>
<td>( V_{IN} )</td>
<td>( 0.78 , V_{IN} + 1.0 , V )</td>
<td>150</td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td></td>
<td>20</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td>( \text{VUVLO} ) Undervoltage lock-out threshold for turn off</td>
<td>( V_{IN} ) decreasing</td>
<td>0.5</td>
<td>0.7</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
## PIN ASSIGNMENTS

**FIXED OUTPUT VOLTAGE DBV PACKAGE (TOP VIEW)**

<table>
<thead>
<tr>
<th>Terminal</th>
<th>No.</th>
<th>I/O</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>VIN</td>
<td>1</td>
<td>I</td>
<td>Boost converter input voltage</td>
</tr>
<tr>
<td>GND</td>
<td>2</td>
<td></td>
<td>Control / logic ground</td>
</tr>
<tr>
<td>EN</td>
<td>3</td>
<td>I</td>
<td>Enable input (1 = enabled, 0 = disabled). EN must be actively terminated high or low.</td>
</tr>
<tr>
<td>VOUT</td>
<td>4</td>
<td>O</td>
<td>Boost converter output</td>
</tr>
<tr>
<td>L</td>
<td>5</td>
<td>I</td>
<td>Connection for inductor</td>
</tr>
<tr>
<td>FB</td>
<td>–</td>
<td>I</td>
<td>Voltage feedback</td>
</tr>
</tbody>
</table>
PARAMETER MEASUREMENT INFORMATION

![Diagram of TPS61097-33 with components labeled]

<table>
<thead>
<tr>
<th>REFERENCE</th>
<th>MANUFACTURER</th>
<th>PART NO.</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1</td>
<td>Murata</td>
<td>GRM319R61A106KE19 10μF 10V X5R 1206 20%</td>
</tr>
<tr>
<td>C2</td>
<td>Murata</td>
<td>GRM319R61A106KE19 10μF 10V X5R 1206 20%</td>
</tr>
<tr>
<td>L1</td>
<td>Coilcraft</td>
<td>DO3314-103MLC</td>
</tr>
</tbody>
</table>

Table 1. List of Components
### TYPICAL CHARACTERISTICS

#### Table 2. Table of Graphs

<table>
<thead>
<tr>
<th>Parameter</th>
<th>FIGURE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum Output Current</td>
<td>1</td>
</tr>
<tr>
<td>Efficiency</td>
<td>2</td>
</tr>
<tr>
<td>Input Current</td>
<td>3</td>
</tr>
<tr>
<td>Startup Voltage</td>
<td>4</td>
</tr>
<tr>
<td>Output Voltage</td>
<td>5</td>
</tr>
<tr>
<td>Waveforms</td>
<td>6</td>
</tr>
<tr>
<td>Waveforms</td>
<td>7</td>
</tr>
<tr>
<td>Waveforms</td>
<td>8</td>
</tr>
<tr>
<td>Waveforms</td>
<td>9</td>
</tr>
<tr>
<td>Waveforms</td>
<td>10</td>
</tr>
<tr>
<td>Waveforms</td>
<td>11</td>
</tr>
<tr>
<td>Waveforms</td>
<td>12</td>
</tr>
<tr>
<td>Waveforms</td>
<td>13</td>
</tr>
<tr>
<td>Waveforms</td>
<td>14</td>
</tr>
<tr>
<td>Waveforms</td>
<td>15</td>
</tr>
<tr>
<td>Waveforms</td>
<td>16</td>
</tr>
</tbody>
</table>

#### Maximum Output Current vs Input Voltage

![Graph 1](image1.png)

**Figure 1.**

#### Efficiency vs Output Current

![Graph 2](image2.png)

**Figure 2.**
**Efficiency**

**vs**

**Input Voltage**

- $I_{out} = 10$ mA
- $I_{out} = 5$ mA
- $I_{out} = 100$ µA
- $I_{out} = 50$ mA

$C_{out} = 10$ µF, ceramic

$L = 10$ µH

**Input Current**

**vs**

**Input Voltage**

- Device Enabled
- No Output Load
- $V_{out} = 3.3$ V

**Startup Voltage**

**vs**

**Temperature**

- Device Disabled
- No Output Load
- $V_{in} = 1.8$ V
- No Load

**Figure 3.**

**Figure 4.**

**Figure 5.**

**Figure 6.**
STARTUP VOLTAGE vs OUTPUT CURRENT

**Figure 7.**

**OUTPUT VOLTAGE vs OUTPUT CURRENT**

**Figure 8.**

**OUTPUT VOLTAGE vs INPUT VOLTAGE**

**Figure 9.**
Inductor Current

\[ V_{\text{in}} = 1.8 \text{ V} \]
\[ I_{\text{out}} = 50 \text{ mA} \]
\[ C_{\text{out}} = 10 \mu\text{F}, \text{ceramic} \]
\[ L = 10 \mu\text{H} \]

\[ V_{\text{out}} \]

Figure 10.

Load Transient Response

\[ V_{\text{in}} = 1.2 \text{ V} \]
\[ I_{\text{out}} = 6 \text{ mA to 50 mA} \]

\[ V_{\text{out}} \]

Figure 11.
\[ V_{\text{IN}} = 1.8 \text{ V to } 2.4 \text{ V} \]
\[ R_{\text{IN}} = 100 \Omega \]
\[ V_{\text{OUT}} = \text{Offset } 1.8 \text{ V} \]
Figure 14. SWITCHING WAVEFORM, DISCONTINUOUS MODE

Figure 15. STARTUP AFTER ENABLE
Figure 16. STARTUP AFTER ENABLE

\[ V_{\text{EN}} = 1.8 \, \text{V} \]
\[ I_{\text{OUT}} = 10 \, \text{mA} \]
DETAILED DESCRIPTION

Operation
The TPS61097 is a high performance, high efficient family of switching boost converters. To achieve high efficiency the power stage is realized as a synchronous boost topology. For the power switching two actively controlled low $R_{DS(on)}$ power MOSFETs are implemented.

Controller Circuit
The device is controlled by a hysteretic current mode controller. This controller regulates the output voltage by keeping the inductor ripple current constant in the range of 200 mA and adjusting the offset of this inductor current depending on the output load. If the required average input current is lower than the average inductor current defined by this constant ripple the inductor current goes discontinuous to keep the efficiency high at low load conditions.

The output voltage $V_{OUT}$ is monitored via the feedback network which is connected to the voltage error amplifier. To regulate the output voltage, the voltage error amplifier compares this feedback voltage to the internal voltage reference and adjusts the required offset of the inductor current accordingly. For fixed output voltage versions, the feedback function is connected internally. A resistive divider network is required to set the output voltage with the adjustable option.

The self oscillating hysteretic current mode architecture is inherently stable and allows fast response to load variations. It also allows using inductors and capacitors over a wide value range.

Device Enable and Shutdown Mode
The device is enabled when EN is set high and shut down when EN is low. During shutdown, the converter stops switching and all internal control circuitry is turned off.

Bypass Switch
The TPS61097 contains a P-channel MOSFET (Bypass Switch) in parallel with the synchronous rectifying MOSFET. When the IC is enabled (EN = $V_{IH}$), the Bypass Switch is turned off to allow the IC to work as a standard boost converter. When the IC is disabled (EN = $V_{IL}$) the Bypass Switch is turned on to provide a direct, low impedance connection from the input voltage (at the L pin) to the load ($V_{OUT}$). The Bypass Switch is not impacted by Undervoltage lockout, Overvoltage or Thermal shutdown.

Startup
After the EN pin is tied high, the device starts to operate. If the input voltage is not high enough to supply the control circuit properly a startup oscillator starts to operate the switches. During this phase the switching frequency is controlled by the oscillator and the maximum switch current is limited. As soon as the device has built up the output voltage to about 1.8 V, high enough for supplying the control circuit, the device switches to its normal hysteretic current mode operation. The startup time depends on input voltage and load current.

Operation at Output Overload
If in normal boost operation the inductor current reaches the internal switch current limit threshold the main switch is turned off to stop further increase of the input current.
In this case the output voltage will decrease since the device can not provide sufficient power to maintain the set output voltage.
If the output voltage drops below the input voltage the backgate diode of the rectifying switch gets forward biased and current starts flow through it. Because this diode cannot be turned off, the load current is only limited by the remaining DC resistances. As soon as the overload condition is removed, the converter automatically resumes normal operation and enters the appropriate soft start mode depending on the operating conditions.

**Undervoltage Lockout**

An undervoltage lockout function stops the operation of the converter if the input voltage drops below the typical undervoltage lockout threshold. This function is implemented in order to prevent malfunctioning of the converter. The undervoltage lockout function has no control of the Bypass Switch. If the Bypass Switch is enabled (EN = V_IL) there is no impact during an undervoltage condition, the Bypass Switch remains on.

**Overtemperature Protection**

The device has a built-in temperature sensor which monitors the internal IC temperature. If the temperature exceeds the programmed threshold (150 °C typical), the device stops operating. As soon as the IC temperature has decreased below the programmed threshold, it starts operating again. There is a built-in hysteresis to avoid unstable operation at IC temperatures at the overtemperature threshold.
APPLICATION INFORMATION

Design Procedure

The TPS61097 DC/DC converters are intended for systems powered by a single up to triple cell Alkaline, NiCd, NiMH battery with a typical terminal voltage between 0.9 V and 5.5 V. They can also be used in systems powered by one-cell Li-ion or Li-Polymer with a typical voltage between 2.5 V and 4.2 V. Additionally, any other voltage source like solar cells or fuel cells with a typical output voltage between 0.9 V and 5.5 V can power systems where the TPS61097 is used. The TPS61097 does not down-regulate VIN; therefore, if VIN is greater than VOUT, VOUT tracks VIN.

Adjustable Bypass Switching

The EN pin can be set up as a low voltage control for the bypass switch. By setting the desired ratio of R1 and R2, the TPS61097 can be set to switch on the bypass at a defined voltage level on VIN. For example, setting R1 and R2 to 200K Ω would set VEN to half of VIN. The voltage level of VIN engaging the bypass switch is based on the VIL level of EN (0.65 V). If VIN is less than 1.30 V then the bypass switch will be enabled. For VIN values above 1.50 V (50% of VIH) the bypass switch is disabled.

Inductor Selection

To make sure that the TPS61097 devices can operate, a suitable inductor must be connected between pin VIN and pin L. Inductor values of 4.7 μH show good performance over the whole input and output voltage range. Choosing other inductance values affects the switching frequency f proportional to 1/L as shown in Equation 1.

\[
L = \frac{1}{f \times 200 \text{ mA}} \times \frac{V_{\text{IN}} \times (V_{\text{OUT}} - V_{\text{IN}})}{V_{\text{OUT}}}
\]  

(1)

Choosing inductor values higher than 4.7 μH can improve efficiency due to reduced switching frequency and therefore with reduced switching losses. Using inductor values below 2.2 μH is not recommended.

Having selected an inductance value, the peak current for the inductor in steady state operation can be calculated. Equation 2 gives the peak current estimate.

\[
I_{\text{L,MAX}} = \begin{cases} 
V_{\text{OUT}} \times I_{\text{OUT}} + 100 \text{ mA} & \text{continuous current operation} \\
0.8 \times V_{\text{IN}} & \text{200 mA; discontinuous current operation}
\end{cases}
\]  

(2)

I_{\text{L,MAX}} is the inductor's required minimum current rating. Note that load transient or over current conditions may require an even higher current rating.
Equation 3 provides an easy way to estimate whether the device is operating in continuous or discontinuous operation. As long as the equation is true, continuous operation is typically established. If the equation becomes false, discontinuous operation is typically established.

\[
\frac{V_{OUT} \times I_{OUT}}{V_{IN}} > 0.8 \times 100 \text{ mA}
\]  

(3)

Due to the use of current hysteretic control in the TPS61097, the series resistance of the inductor can impact the operation of the main switch. There is a simple calculation that can ensure proper operation of the TPS61097 boost converter. The relationship between the series resistance \( R_{\text{IN}} \), the input voltage \( V_{\text{IN}} \) and the switch current limit \( I_{\text{SW}} \) is shown in Equation 4.

\[
R_{\text{IN}} < \frac{V_{\text{IN}}}{I_{\text{SW}}}
\]  

(4)

Examples:

\( I_{\text{SW}} = 400 \text{ mA}, V_{\text{IN}} = 2.5 \text{ V} \)  

(5)

In Equation 5, \( R_{\text{IN}} < 2.5 \text{ V} / 400 \text{ mA} \); therefore, \( R_{\text{IN}} \) must be less than 6.25 \( \Omega \).

\( I_{\text{SW}} = 400 \text{ mA}, V_{\text{IN}} = 1.8 \text{ V} \)  

(6)

In Equation 6, \( R_{\text{IN}} < 1.8 \text{ V} / 400 \text{ mA} \); therefore, \( R_{\text{IN}} \) must be less than 4.5 \( \Omega \).

The following inductor series from different suppliers have been used with TPS61097 converters:

<table>
<thead>
<tr>
<th>VENDOR</th>
<th>INDUCTOR SERIES</th>
</tr>
</thead>
<tbody>
<tr>
<td>Coilcraft</td>
<td>DO3314</td>
</tr>
<tr>
<td>TDK</td>
<td>NLC565050T</td>
</tr>
<tr>
<td>Taiyo Yuden</td>
<td>CBC2012T</td>
</tr>
</tbody>
</table>

**Capacitor Selection**

*Input Capacitor*

The input capacitor should be at least 10-\( \mu \)F to improve transient behavior of the regulator and EMI behavior of the total power supply circuit. The input capacitor should be a ceramic capacitor and be placed as close as possible to the VIN and GND pins of the IC.

*Output Capacitor*

For the output capacitor \( C_2 \), it is recommended to use small ceramic capacitors placed as close as possible to the VOUT and GND pins of the IC. If, for any reason, the application requires the use of large capacitors which cannot be placed close to the IC, the use of a small ceramic capacitor with a capacitance value of around \( 2.2 \mu \text{F} \) in parallel to the large one is recommended. This small capacitor should be placed as close as possible to the VOUT and GND pins of the IC.

A minimum capacitance value of 4.7 \( \mu \)F should be used, 10 \( \mu \)F are recommended. If the inductor value exceeds 4.7 \( \mu \text{H} \), the value of the output capacitance value needs to be half the inductance value or higher for stability reasons, see Equation 7.

\[
C_2 \geq \frac{L}{2} \times \frac{\mu \text{F}}{\mu \text{H}}
\]  

(7)

The TPS61097 is not sensitive to the ESR in terms of stability. Using low ESR capacitors, such as ceramic capacitors, is recommended to minimize output voltage ripple. If heavy load changes are expected, the output capacitor value should be increased to avoid output voltage drops during fast load transients.
Table 4. Recommended Output Capacitors

<table>
<thead>
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<th>VENDOR</th>
<th>CAPACITOR SERIES</th>
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<tr>
<td>Murata</td>
<td>GRM188R60J106M47D 10μF 6.3V X5R 0603</td>
</tr>
<tr>
<td>Murata</td>
<td>GRM319R61A106KE19 10μF 10V X5R 1206</td>
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</table>
Layout Considerations

As for all switching power supplies, the layout is an important step in the design, especially at high peak currents and high switching frequencies. If the layout is not carefully done, the regulator could show stability problems as well as EMI problems. Therefore, use wide and short traces for the main current path and for the power ground tracks. The input and output capacitor, as well as the inductor should be placed as close as possible to the IC. Use a common ground node for power ground and a different one for control ground to minimize the effects of ground noise. Connect these ground nodes at any place close to one of the ground pins of the IC.

The feedback divider should be placed as close as possible to the control ground pin of the IC. To lay out the control ground, it is recommended to use short traces as well, separated from the power ground traces. This avoids ground shift problems, which can occur due to superimposition of power ground current and control ground current.

Figure 19. Layout Schematic
Thermal Information

Implementation of integrated circuits in low-profile and fine-pitch surface-mount packages typically requires special attention to power dissipation. Many system-dependent issues such as thermal coupling, airflow, added heat sinks and convection surfaces, and the presence of other heat-generating components affect the power-dissipation limits of a given component.

Three basic approaches for enhancing thermal performance are listed below.

- Improving the power dissipation capability of the PCB design
- Improving the thermal coupling of the component to the PCB
- Introducing airflow in the system

The maximum recommended junction temperature ($T_{J}$) of the TPS61097 devices is 125°C. Specified regulator operation is assured to a maximum ambient temperature $T_A$ of 85°C. Therefore, the maximum power dissipation is about TBD mW. More power can be dissipated if the maximum ambient temperature of the application is lower.
### REVISION HISTORY

**Changes from Revision B (December 2009) to Revision C**

<table>
<thead>
<tr>
<th>Change Description</th>
<th>Page</th>
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<tr>
<td>Deleted Fixed Output Voltage Options from 1.8V to 5.0V</td>
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</tr>
<tr>
<td>Deleted adjustable output feature from DESCRIPTION.</td>
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</tr>
<tr>
<td>Deleted adjustable output feature listed in the ORDERING INFORMATION table.</td>
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</tr>
<tr>
<td>Deleted $V_{OUT}$ parameters for the TPS61097-18, TPS61097-27, TPS61097-30, and TPS61097-50 from the ELECTRICAL CHARACTERISTICS table.</td>
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<tr>
<td>Deleted Overvoltage protection threshold parameter.</td>
<td>3</td>
</tr>
<tr>
<td>Deleted the adjustable output voltage pinout package.</td>
<td>4</td>
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<tr>
<td>Deleted the adjustable output voltage features from the Terminal Functions table.</td>
<td>4</td>
</tr>
<tr>
<td>Deleted the Functional Block Diagram for the adjustable output version.</td>
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</tr>
<tr>
<td>Deleted &quot;Overvoltage Protection&quot; and &quot;Programming the Output Voltage&quot; sections.</td>
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## PACKAGING INFORMATION

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<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
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<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
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<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
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<tr>
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(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

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**TAPE AND REEL INFORMATION**

*All dimensions are nominal*

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<td>3000</td>
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<td>201.0</td>
<td>28.0</td>
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</table>
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
D. Falls within JEDEC MO-178 Variation AA.
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
D. Publication IPC-7351 is recommended for alternate designs.
E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.
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## TAPE AND REEL INFORMATION

### TAPE DIMENSIONS

- **A0**: Dimension designed to accommodate the component width
- **B0**: Dimension designed to accommodate the component length
- **K0**: Dimension designed to accommodate the component thickness
- **W**: Overall width of the carrier tape
- **P1**: Pitch between successive cavity centers

### PACKAGE MATERIALS INFORMATION

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Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.
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NOTES: (continued)

4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

7. Board assembly site may have different recommendations for stencil design.
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