1 Features

- Accurate, Continuous Heart-Rate Monitoring:
  - Up to 100-dB Dynamic Range for Accurate Heart-Rate Detection
  - Low Current for Continuous Operation on a Wearable Device With a Typical Value:
    - 30 µA for an LED, 25 µA for the Receiver
- Transmitter:
  - 4 LEDs in Common Anode Configurations
  - 8-Bit Programmable LED Current to 200 mA
  - Mode to Fire Two LEDs in Parallel
  - Programmable LED On-Time
  - Simultaneous Support of 3 LEDs for Optimized \( \text{SpO}_2 \), HRM, or Multiwavelength HRM
  - Average Current of 30 µA Adequate for a Typical Heart-Rate Monitoring Scenario:
    - 20-mA Setting, 60-µs Pulse Duration, 25-Hz Sampling Rate
- Receiver:
  - Supports 3 Time-Multiplexed PD Inputs
  - 24-Bit Representation of Current Input From PD in Two’s-Complement Format
  - Individual DC Offset Subtraction DAC (Up to ±127-µA Range) at TIA Input for Each LED, Ambient
  - Transimpedance Gain: 10 kΩ to 2 MΩ
  - Noise Filtering With Programmable Bandwidth
  - Receiver Operates at Approximately 1-µA/Hz Sampling Rate (Example, 25 µA at 25 Hz)
  - Hardware Power-Down Mode: Approximately 0-µA Current
- Flexible Pulse Sequencing and Timing Control
- Clocking Via External Clock or Internal Oscillator
- FIFO With 128-Sample Depth:
  - Programmable Partitioning Across Phases
- Pin-Selectable \( \text{I}^2\text{C}, \text{SPI} \) Interface
- Operating Temperature Range: −20°C to +70°C
- 2.6-mm × 2.1-mm, 0.4-mm Pitch DSBGA Package
- Supplies:
  - Tx: 3 V to 5.25 V
  - Rx: 1.8 V to 1.9 V (LDO Bypass), 2.0 V to 3.6 V (LDO Enabled)
  - IO: 1.7 V to Rx_SUP

2 Applications

- Optical Heart-Rate Monitoring (HRM) for Wearables, Hearables
- Heart-Rate Variability (HRV)
- Pulse Oximetry (\( \text{SpO}_2 \)) Measurements
- Maximum Oxygen Consumption (\( \text{VO}_2 \) Max)

3 Description

The AFE4410 is an analog front-end for optical biosensing applications, such as heart-rate monitoring (HRM). The device supports up to four switching light-emitting diodes (LEDs) and up to three photodiodes (PDs). The current from the photodiode is converted into voltage by the transimpedance amplifier (TIA) and digitized using an analog-to-digital converter (ADC). The ADC code is stored in a 128-sample first in, first-out block (FIFO) with programmable depth. The FIFO can be read out using either an \( \text{I}^2\text{C} \) interface or a serial peripheral interface (SPI). The AFE also has a fully integrated LED driver with 8-bit current control. The device has high dynamic range transmit-and-receive circuitry offering a dynamic range of up to 100 dB that enables accurate heart-rate sensing. The AFE achieves extremely low current levels by operating an ultralow power (ULP) mode set by using the ENABLE_ULP register bit.

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>AFE4410</td>
<td>DSBGA (30)</td>
<td>2.60 mm × 2.10 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the package option addendum at the end of the datasheet.

- **Simplified Block Diagram**
# Revision History

<table>
<thead>
<tr>
<th>Changes from Original (May 2017) to Revision A</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>• Changed ±126-µA Range to ±127-µA Range</td>
<td>1</td>
</tr>
</tbody>
</table>

Product Folder Links: [AFE4410](https://www.ti.com)
5 Device and Documentation Support

5.1 Trademarks
All trademarks are the property of their respective owners.

5.2 Electrostatic Discharge Caution
This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

5.3 Glossary
SLYZ022 — Ti Glossary.
This glossary lists and explains terms, acronyms, and definitions.

6 Mechanical, Packaging, and Orderable Information
The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. See Texas Instruments Literature No. SNVA009 (www.ti.com/lit/snva009).
NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.
## Packaging Information

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>AFE4410YZR</td>
<td>ACTIVE</td>
<td>DSBGA</td>
<td>YZ</td>
<td>30</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SNAGCU</td>
<td>Level-1-260C-UNLIM</td>
<td>-20 to 70</td>
<td>AFE4410</td>
<td></td>
</tr>
<tr>
<td>AFE4410YZT</td>
<td>ACTIVE</td>
<td>DSBGA</td>
<td>YZ</td>
<td>30</td>
<td>250</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SNAGCU</td>
<td>Level-1-260C-UNLIM</td>
<td>-20 to 70</td>
<td>AFE4410</td>
<td></td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
- **RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
- **Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) **Lead/Ball Finish** - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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### TAPE AND REEL INFORMATION

*All dimensions are nominal*

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
</tr>
</thead>
<tbody>
<tr>
<td>AFE4410YZR</td>
<td>DSBGA</td>
<td>YZ</td>
<td>30</td>
<td>3000</td>
<td>180.0</td>
<td>8.4</td>
<td>2.16</td>
<td>2.66</td>
<td>0.6</td>
<td>4.0</td>
<td>8.0</td>
<td>Q1</td>
</tr>
<tr>
<td>AFE4410YZT</td>
<td>DSBGA</td>
<td>YZ</td>
<td>30</td>
<td>250</td>
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<td>0.6</td>
<td>4.0</td>
<td>8.0</td>
<td>Q1</td>
</tr>
</tbody>
</table>

**Device**
- **AFE4410YZR**: DSBGA, YZ, 30, 3000
- **AFE4410YZT**: DSBGA, YZ, 30, 250

**Package Type**
- DSBGA

**Package Drawing**
- YZ

**Pins**
- 30

**SPQ**
- 3000

**Reel Diameter**
- 180.0 mm

**Reel Width W1**
- 8.4 mm

**A0**
- 2.16 mm

**B0**
- 2.66 mm

**K0**
- 0.6 mm

**P1**
- 4.0 mm

**W**
- 8.0 mm

**Pin1 Quadrant**
- Q1

---

**TAPE DIMENSIONS**

- K0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- A0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P1: Pitch between successive cavity centers

**QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE**

- Pocket Quadrants
- Sprocket Holes
- User Direction of Feed
### TAPE AND REEL BOX DIMENSIONS

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
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</thead>
<tbody>
<tr>
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<td>DSBGA</td>
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<td>182.0</td>
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<tr>
<td>AFE4410YZT</td>
<td>DSBGA</td>
<td>YZ</td>
<td>30</td>
<td>250</td>
<td>182.0</td>
<td>182.0</td>
<td>20.0</td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
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