FEATUERES

- Improved Specifications include:
  - Offset Voltage 3 mV Maximum Over Temperature (LM101A/LM201A)
  - Input Current 100 nA Maximum Over Temperature (LM101A/LM201A)
  - Offset Current 20 nA Maximum Over Temperature (LM101A/LM201A)
  - Specified Drift Characteristics
  - Offsets Specified Over Entire Common Mode and Supply Voltage Ranges
  - Slew Rate of 10V/μs as a Summing Amplifier

DESCRIPTION

The LM101A series are general purpose operational amplifiers which feature improved performance over industry standards like the LM709. Advanced processing techniques make possible an order of magnitude reduction in input currents, and a redesign of the biasing circuitry reduces the temperature drift of input current.

This amplifier offers many features which make its application nearly foolproof: Overload protection on the input and output, no latch-up when the common mode range is exceeded, and freedom from oscillations and compensation with a single 30 pF Capacitor. It has advantages over internally compensated amplifiers in that the frequency compensation can be tailored to the particular application. For example, in low frequency circuits it can be overcompensated for increased stability margin or the compensation can be optimized to give more than a factor of ten improvement in high frequency performance for most applications.

In Addition, the device provides better accuracy and lower noise in high impedance circuitry. The low input currents also make it particularly well suited for long interval integrators or timers, sample and hold circuits and low frequency waveform generators. Further, replacing circuits where matched transistor pairs buffer the inputs of conventional IC op amps, It can give lower offset voltage and a drift at a lower cost.

The LM101A is ensured over a temperature range of −55°C to +125°C, the LM201A from −25°C to +85°C, and the LM301A from 0°C to +70°C.

Fast AC-DC Converter

Feedforward compensation can be used to make a fast full wave rectifier without a filter.

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>LM101A/LM201A</th>
<th>LM301A</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>±22V</td>
<td>±18V</td>
</tr>
<tr>
<td>Differential Input Voltage</td>
<td>±30V</td>
<td>±30V</td>
</tr>
<tr>
<td>Input Voltage(3)</td>
<td>±15V</td>
<td>±15V</td>
</tr>
<tr>
<td>Output Short Circuit Duration(4)</td>
<td>Continuous</td>
<td>Continuous</td>
</tr>
<tr>
<td>Operating Ambient Temp. Range</td>
<td>−55°C to +125°C (LM101A)</td>
<td>0°C to +70°C</td>
</tr>
<tr>
<td></td>
<td>−25°C to +85°C (LM201A)</td>
<td></td>
</tr>
<tr>
<td>( T_J ) Max</td>
<td>LMC0008C Package</td>
<td>150°C</td>
</tr>
<tr>
<td></td>
<td>P0008E Package</td>
<td>150°C</td>
</tr>
<tr>
<td></td>
<td>NAB0008A, J0014A Package</td>
<td>150°C</td>
</tr>
<tr>
<td>Power Dissipation at ( T_A = 25°C )</td>
<td>LMC0008C-Package (Still Air)</td>
<td>500 mW</td>
</tr>
<tr>
<td></td>
<td>(400 LF/Min Air Flow)</td>
<td>1200 mW</td>
</tr>
<tr>
<td></td>
<td>P0008E Package</td>
<td>900 mW</td>
</tr>
<tr>
<td></td>
<td>NAB0008A, J0014A Package</td>
<td>1000 mW</td>
</tr>
<tr>
<td>Thermal Resistance (Typical) ( \theta_{JA} )</td>
<td>LMC0008C Package (Still Air)</td>
<td>165°C/W</td>
</tr>
<tr>
<td></td>
<td>(400 LF/Min Air Flow)</td>
<td>67°C/W</td>
</tr>
<tr>
<td></td>
<td>P0008E Package</td>
<td>135°C/W</td>
</tr>
<tr>
<td></td>
<td>NAB0008A, J0014A Package</td>
<td>110°C/W</td>
</tr>
<tr>
<td>(Typical) ( \theta_{JC} )</td>
<td>LMC0008C Package</td>
<td>25°C/W</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>−65°C to +150°C</td>
<td>−65°C to +150°C</td>
</tr>
<tr>
<td>Lead Temperature (Soldering, 10 sec.)</td>
<td>LMC0008C or NAB0008A, J0014A, NAD0010A</td>
<td>300°C</td>
</tr>
<tr>
<td></td>
<td>P0008E</td>
<td>260°C</td>
</tr>
<tr>
<td>ESD Tolerance(5)</td>
<td></td>
<td>2000V</td>
</tr>
</tbody>
</table>

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating ratings indicate for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

(3) For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

(4) Continuous short circuit is allowed for case temperatures to 125°C and ambient temperatures to 75°C for LM101A/LM201A, and 70°C and 55°C respectively for LM301A.

(5) Human body model, 100 pF discharged through 1.5 kΩ.
## Electrical Characteristics

\( T_A = T_J \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>LM101A/LM201A</th>
<th>LM301A</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Input Offset Voltage</td>
<td>( T_A = 25^\circ C, R_S \leq 50 , k\Omega )</td>
<td>0.7</td>
<td>2.0</td>
<td>2.0</td>
</tr>
<tr>
<td>Input Offset Current</td>
<td>( T_A = 25^\circ C )</td>
<td>1.5</td>
<td>10</td>
<td>3.0</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>( T_A = 25^\circ C )</td>
<td>30</td>
<td>75</td>
<td>70</td>
</tr>
<tr>
<td>Input Resistance</td>
<td>( T_A = 25^\circ C )</td>
<td>1.5</td>
<td>4.0</td>
<td>0.5</td>
</tr>
<tr>
<td>Supply Current</td>
<td>( T_A = 25^\circ C )</td>
<td>( V_S = \pm 20V )</td>
<td>1.8</td>
<td>3.0</td>
</tr>
<tr>
<td></td>
<td>( V_S = \pm 15V )</td>
<td>1.8</td>
<td>3.0</td>
<td>mA</td>
</tr>
<tr>
<td>Large Signal Voltage Gain</td>
<td>( T_A = 25^\circ C, V_S = \pm 15V )</td>
<td>( V_{OUT} = \pm 10V, R_L \geq 2 , k\Omega )</td>
<td>50</td>
<td>160</td>
</tr>
<tr>
<td>Input Offset Voltage</td>
<td>( R_S \leq 50 , k\Omega )</td>
<td>3.0</td>
<td>15</td>
<td>6.0</td>
</tr>
<tr>
<td>Average Temperature Coefficient of</td>
<td>( R_S \leq 50 , k\Omega )</td>
<td>20</td>
<td>70</td>
<td></td>
</tr>
<tr>
<td>Input Offset Voltage</td>
<td>( 25^\circ C \leq T_A \leq T_{MAX} )</td>
<td>0.01</td>
<td>0.1</td>
<td>0.01</td>
</tr>
<tr>
<td>Average Temperature Coefficient of</td>
<td>( T_{MIN} \leq T_A \leq 25^\circ C )</td>
<td>0.02</td>
<td>0.2</td>
<td>0.02</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>( 25^\circ C \leq T_A \leq T_{MAX} )</td>
<td>0.1</td>
<td></td>
<td>0.3</td>
</tr>
<tr>
<td>Supply Current</td>
<td>( T_A = T_{MAX}, V_S = \pm 20V )</td>
<td>1.2</td>
<td>2.5</td>
<td></td>
</tr>
<tr>
<td>Large Signal Voltage Gain</td>
<td>( V_S = \pm 15V, V_{OUT} = \pm 10V )</td>
<td>( R_L \geq 2k )</td>
<td>25</td>
<td></td>
</tr>
<tr>
<td>Output Voltage Swing</td>
<td>( V_S = \pm 15V )</td>
<td>( R_L = 10 , k\Omega )</td>
<td>12</td>
<td>14</td>
</tr>
<tr>
<td></td>
<td>( R_L = 2 , k\Omega )</td>
<td>10</td>
<td>13</td>
<td>10</td>
</tr>
<tr>
<td>Input Voltage Range</td>
<td>( V_S = \pm 20V )</td>
<td>15</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>( V_S = \pm 15V )</td>
<td>+15, -13</td>
<td>12</td>
<td>+15, -13</td>
</tr>
<tr>
<td>Common-Mode Rejection Ratio</td>
<td>( R_S \leq 50 , k\Omega )</td>
<td>80</td>
<td>96</td>
<td>70</td>
</tr>
<tr>
<td>Supply Voltage Rejection Ratio</td>
<td>( R_S \leq 50 , k\Omega )</td>
<td>80</td>
<td>96</td>
<td>70</td>
</tr>
</tbody>
</table>

(1) Unless otherwise specified, these specifications apply for \( C1 = 30 \, pF, \pm 5V \leq V_S \leq \pm 20V \) and \( -55^\circ C \leq T_A \leq +125^\circ C \) (LM101A), \( \pm 5V \leq V_S \leq \pm 20V \) and \( -25^\circ C \leq T_A \leq +85^\circ C \) (LM201A), \( \pm 5V \leq V_S \leq \pm 15V \) and \( 0^\circ C \leq T_A \leq +70^\circ C \) (LM301A).
Typical Performance Characteristics

LM101A/LM201A

Input Voltage Range

Output Swing

Figure 1.

Figure 2.

Voltage Gain

Figure 3.

Performance Characteristics

LM301A

Input Voltage Range

Output Swing

Figure 4.

Figure 5.
Performance Characteristics (continued)

LM301A

![Voltage Gain Diagram](image)

**Figure 6.**

Voltage Gain

VOLTAGE GAIN (dB)

0°C ≤ T<sub>A</sub> ≤ 70°C

SUPPLY VOLTAGE (±V)
Typical Performance Characteristics

Supply Current

Voltage Gain

Maximum Power Dissipation

Input Current, LM101A/LM201A/LM301A

Current Limiting

Input Noise Voltage

Figure 7.

Figure 8.

Figure 9.

Figure 10.

Figure 11.

Figure 12.
Typical Performance Characteristics (continued)

Input Noise Current

Common Mode Rejection

Power Supply Rejection

Closed Loop Output Impedance

Figure 13.

Figure 14.

Figure 15.

Figure 16.
Typical Performance Characteristics for Various Compensation Circuits

Pin connections shown are for 8-pin packages.

\[ C_1 = \frac{R_1 \cdot C_S}{R_1 + R_2} \]

\[ C_S = 30 \text{ pF} \]

\[ C_2 = 10 \cdot C_1 \]

\[ f_o = 3 \text{ MHz} \]

\[ C_2 = \frac{1}{2\pi f_o \cdot R_2} \]

**Figure 17. Single Pole Compensation**

**Figure 18. Two Pole Compensation**

**Figure 19. Feedforward Compensation**

**Figure 20.**

**Figure 21.**

**Figure 22.**
Typical Performance Characteristics for Various Compensation Circuits (continued)

Large Signal Frequency Response

Figure 23.

Figure 24.

Large Signal Frequency Response

Figure 25.

Figure 26.

Voltage Follower Pulse Response

Figure 27.

Figure 28.

Inverter Pulse Response
TYPICAL APPLICATIONS

Pin connections shown are for 8-pin packages

\[
C = \frac{R_b}{R_b + C_1}
\]

Figure 29. Variable Capacitance Multiplier

Figure 30. Simulated Inductor

\[
L = R_1 \cdot R_2 \cdot C_1
\]

\[
R_s = R_2
\]

\[
R_p = R_1
\]

Figure 31. Fast Inverting Amplifier with High Input Impedance

\[
f_o = 10 \text{ kHz}
\]

Figure 32. Inverting Amplifier with Balancing Circuit

† May be zero or equal to parallel combination of \(R_1\) and \(R_2\) for minimum offset.

Figure 33. Sine Wave Oscillator

* Adjust for zero integrator drift. Current drift typically 0.1 nA/°C over –55°C to +125°C temperature range.

Figure 34. Integrator with Bias Current Compensation
Application Hints

Pin connections shown are for 8-pin packages.

*Protects input
†Protects output
‡Protects output—not needed when R4 is used.

Figure 35. Protecting Against Gross Fault Conditions

Figure 36. Compensating for Stray Input Capacitances or Large Feedback Resistor

Figure 37. Isolating Large Capacitive Loads
Although the LM101A is designed for trouble-free operation, experience has indicated that it is wise to observe certain precautions given below to protect the devices from abnormal operating conditions. It might be pointed out that the advice given here is applicable to practically any IC op amp, although the exact reason why may differ with different devices.

When driving either input from a low-impedance source, a limiting resistor should be placed in series with the input lead to limit the peak instantaneous output current of the source to something less than 100 mA. This is especially important when the inputs go outside a piece of equipment where they could accidentally be connected to high-voltage sources. Large capacitors on the input (greater than 0.1 \( \mu \)F) should be treated as a low source impedance and isolated with a resistor. Low impedance sources do not cause a problem unless their output voltage exceeds the supply voltage. However, the supplies go to zero when they are turned off, so the isolation is usually needed.

The output circuitry is protected against damage from shorts to ground. However, when the amplifier output is connected to a test point, it should be isolated by a limiting resistor, as test points frequently get shorted to bad places. Further, when the amplifier drives a load external to the equipment, it is also advisable to use some sort of limiting resistance to preclude mishaps.

Precautions should be taken to insure that the power supplies for the integrated circuit never become reversed—even under transient conditions. With reverse voltages greater than 1V, the IC will conduct excessive current, fusing internal aluminum interconnects. If there is a possibility of this happening, clamp diodes with a high peak current rating should be installed on the supply lines. Reversal of the voltage between \( V^+ \) and \( V^- \) will always cause a problem, although reversals with respect to ground may also give difficulties in many circuits.

The minimum values given for the frequency compensation capacitor are stable only for source resistances less than 10 k\( \Omega \), stray capacitances on the summing junction less than 5 pF and capacitive loads smaller than 100 pF. If any of these conditions are not met, it becomes necessary to overcompensate the amplifier with a larger compensation capacitor. Alternately, lead capacitors can be used in the feedback network to negate the effect of stray capacitance and large feedback resistors or an RC network can be added to isolate capacitive loads.

Although the LM101A is relatively unaffected by supply bypassing, this cannot be ignored altogether. Generally it is necessary to bypass the supplies to ground at least once on every circuit card, and more bypass points may be required if more than five amplifiers are used. When feed-forward compensation is employed, however, it is advisable to bypass the supply leads of each amplifier with low inductance capacitors because of the higher frequencies involved.
Typical Applications

Pin connections shown are for 8-pin packages.

Figure 38. Standard Compensation and Offset Balancing Circuit

Figure 39. Fast Voltage Follower

Figure 40. Fast Summing Amplifier
\[ \frac{V_{IH}}{R1 \times R5} \]

\[ R3 = R4 + R5 \]
\[ R1 = R2 \]

**Figure 41. Bilateral Current Source**

\[ R1^* = R4^*; R2^* = R3^* \]
\[ A_y = 1 + \frac{R1}{R2} \]

\[ * , \dagger \text{ Matching determines CMRR.} \]

**Figure 42. Fast AC/DC Converter**

(1) Feedforward compensation can be used to make a fast full wave rectifier without a filter
*Adjust for zero integrator drift. Current drift typically 0.1 nA/°C over 0°C to +70°C temperature range.

Figure 44. Integrator with Bias Current Compensation

Figure 45. Voltage Comparator for Driving RTL Logic or High Current Driver

Figure 46. Low Frequency Square Wave Generator

*Polycarbonate-dielectric capacitor

Figure 47. Low Drift Sample and Hold
Figure 48. Voltage Comparator for Driving DTL or TTL Integrated Circuits

Schematic

Pin connections shown are for 8-pin packages.
Connection Diagrams

Figure 49. CDIP and PDIP Packages
Package Number NAB0008A or P0008E

Figure 50. CLGA Package
Package Number NAD0010A

Figure 51. TO-99 Package
See Package Number LMC0008C

Figure 52. CDIP Package
See Package Number J0014A,
## REVISION HISTORY

Changes from Revision C (March 2013) to Revision D

<table>
<thead>
<tr>
<th>Change Description</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>Changed layout of National Data Sheet to TI format</td>
<td>17</td>
</tr>
</tbody>
</table>

Submit Documentation Feedback
# PACKAGE OPTION ADDENDUM

## PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish (6)</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM101AH</td>
<td>ACTIVE</td>
<td>TO-99</td>
<td>LMC</td>
<td>8</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>-55 to 125</td>
<td>(LM101AH, LM101AH)</td>
<td>Samples</td>
<td></td>
</tr>
<tr>
<td>LM101AH/NOPB</td>
<td>ACTIVE</td>
<td>TO-99</td>
<td>LMC</td>
<td>8</td>
<td>500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>Call TI</td>
<td>Level-1-NA-UNLIM</td>
<td>-55 to 125</td>
<td>(LM101AH, LM101AH)</td>
<td>Samples</td>
</tr>
<tr>
<td>LM101AJ</td>
<td>ACTIVE</td>
<td>CDIP</td>
<td>NAB</td>
<td>8</td>
<td>40</td>
<td>TBD</td>
<td>Call TI</td>
<td>-40 to 85</td>
<td>LM101AJ</td>
<td>Samples</td>
<td></td>
</tr>
<tr>
<td>LM201AH</td>
<td>ACTIVE</td>
<td>TO-99</td>
<td>LMC</td>
<td>8</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>-40 to 85</td>
<td>(LM201AH, LM201AH)</td>
<td>Samples</td>
<td></td>
</tr>
<tr>
<td>LM201AH/NOPB</td>
<td>ACTIVE</td>
<td>TO-99</td>
<td>LMC</td>
<td>8</td>
<td>500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>Call TI</td>
<td>Level-1-NA-UNLIM</td>
<td>-40 to 85</td>
<td>(LM201AH, LM201AH)</td>
<td>Samples</td>
</tr>
<tr>
<td>LM301AH</td>
<td>ACTIVE</td>
<td>TO-99</td>
<td>LMC</td>
<td>8</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>0 to 70</td>
<td>(LM301AH, LM301AH)</td>
<td>Samples</td>
<td></td>
</tr>
<tr>
<td>LM301AH/NOPB</td>
<td>ACTIVE</td>
<td>TO-99</td>
<td>LMC</td>
<td>8</td>
<td>500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
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<td>Level-1-NA-UNLIM</td>
<td>0 to 70</td>
<td>(LM301AH, LM301AH)</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substances do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.**: The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
NOTES:  
A. All linear dimensions are in inches (millimeters).  
B. This drawing is subject to change without notice.  
C. Leads in true position within 0.010 (0.25) R © MMC at seating plane.  
D. Pin numbers shown for reference only. Numbers may not be marked on package.  
E. Folio within JEDEC MO-002/10-99.