1 Features

- Typical 0.005%/V Line Regulation
- 1% Output Voltage Tolerance
- 1.5-A Output Current
- Adjustable Output Down to 1.25 V
- Current Limit Constant with Temperature
- 80-dB Ripple Rejection
- Short-Circuit Protected Output
- −40°C to 125°C Operating Temperature Range

2 Applications

- Automotive LED Lighting
- Battery Chargers
- Post Regulation for Switching Supplies
- Constant-Current Regulator
- Microprocessor Supplies

Typical Application

\[
V_{\text{OUT}} = 1.25 V \left(1 + \frac{R_2}{R_1}\right) + I_{\text{ADJ}} (R_2)
\]

*Needed if device is more than 6 inches from filter capacitors.
†Optional—improves transient response
††Needed if device is more than 6 inches from filter capacitors.

3 Description

The LM317A adjustable 3-terminal, positive-voltage regulators are capable of supplying current in excess of 1.5 A over a 1.25-V to 37-V output range and provide 1% output-voltage accuracy. The LM317A device is available in an SOT-233 package, which makes it ideal for space-constrained applications that require high-performance regulation. The device is exceptionally easy to use and requires only two external resistors to set the output voltage. Both line regulation and load regulation are better achieved with the LM317A device than with standard fixed regulators.

The LM317A offers full overload protection such as current limit, thermal-overload protection, and safe-area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is disconnected.

Typically, no capacitors are needed unless the device is situated more than 6 inches from the input filter capacitors, in which case an input bypass is needed. An optional output capacitor can be added to improve transient response. The adjustment terminal can be bypassed to achieve very high ripple-rejection ratios that are difficult to achieve with standard 3-terminal regulators.

Because the LM317A regulator is floating and detects only the input-to-output differential voltage, supplies of several hundred volts can be regulated as long as the maximum input-to-output differential is not exceeded. Exceeding the maximum input-to-output differential will result in short-circuiting the output.

By connecting a fixed resistor between the adjustment pin and output, the LM317A can be also used as a precision current regulator. Supplies with electronic shutdown can be achieved by clamping the adjustment terminal to ground, which programs the output to 1.25 V where most loads draw little current.

For applications requiring greater output current, see data sheets for LM150 series (3A), SNVS772, and LM138 series (5A), SNVS771. For the negative complement, see LM137 (SNVS778) series data sheet.

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM317A</td>
<td>TO-220 (3)</td>
<td>14.986 mm x 10.16 mm</td>
</tr>
<tr>
<td></td>
<td>SOT-233 (4)</td>
<td>6.50 mm x 3.50 mm</td>
</tr>
<tr>
<td></td>
<td>TO (3)</td>
<td>8.255 mm x 8.255 mm</td>
</tr>
<tr>
<td></td>
<td>TO-252 (3)</td>
<td>6.58 mm x 6.10 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum at the end of the data sheet.

An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.
Table of Contents

1 Features ................................................................. 1
2 Applications .......................................................... 1
3 Description ............................................................ 1
4 Revision History ...................................................... 2
5 Device Comparison Table ......................................... 3
6 Pin Configuration and Functions ................................. 4
7 Specifications ......................................................... 5
  7.1 Absolute Maximum Ratings ................................. 5
  7.2 ESD Ratings ....................................................... 5
  7.3 Recommended Operating Conditions ..................... 5
  7.4 Thermal Information ............................................ 5
  7.5 Electrical Characteristics .................................... 6
  7.6 Typical Characteristics ....................................... 7
8 Detailed Description ............................................... 10
  8.1 Overview ........................................................ 10
  8.2 Functional Block Diagram .................................. 11
  8.3 Feature Description ........................................... 12
8.4 Device Functional Modes ...................................... 12
9 Application and Implementation ............................... 14
  9.1 Application Information ...................................... 14
  9.2 Typical Applications ......................................... 14
10 Power Supply Recommendations ............................... 25
11 Layout .................................................................. 25
  11.1 Layout Guidelines .............................................. 25
  11.2 Layout Examples ................................................ 25
  11.3 Thermal Considerations ..................................... 26
12 Device and Documentation Support ........................... 32
  12.1 Documentation Support ...................................... 32
  12.2 Community Resources ....................................... 32
  12.3 Trademarks ....................................................... 32
  12.4 Electrostatic Discharge Caution ............................ 32
  12.5 Glossary .......................................................... 32
13 Mechanical, Packaging, and Orderable Information ....... 32

4 Revision History
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

<table>
<thead>
<tr>
<th>DATE</th>
<th>REVISION</th>
<th>NOTES</th>
</tr>
</thead>
<tbody>
<tr>
<td>October 2015</td>
<td></td>
<td>Initial Release</td>
</tr>
</tbody>
</table>

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5 Device Comparison Table

Table 1. LM317 Family Options

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>TEMPERATURE</th>
<th>DESCRIPTION</th>
<th>PRODUCT FOLDER</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM317-N</td>
<td>0°C to 125°C</td>
<td>40-V, 1.5-A Catalog device</td>
<td>Click here</td>
</tr>
<tr>
<td>LM317A</td>
<td>−40°C to 125°C</td>
<td>40-V, 1.5-A Industrial device</td>
<td>Click here</td>
</tr>
<tr>
<td>LM317HV</td>
<td>0°C to 125°C</td>
<td>60-V, 1.5-A Catalog device</td>
<td>Click here</td>
</tr>
<tr>
<td>LM317L-N</td>
<td>−40°C to 125°C</td>
<td>40-V, 0.1-A Industrial device</td>
<td>Click here</td>
</tr>
<tr>
<td>LM117</td>
<td>−55°C to 150°C</td>
<td>40-V, 1.5-A Extended temperature device</td>
<td>Click here</td>
</tr>
<tr>
<td>LM117HV</td>
<td>−55°C to 150°C</td>
<td>60-V, 1.5-A Extended temperature device</td>
<td>Click here</td>
</tr>
<tr>
<td>LM117HVQML</td>
<td>−55°C to 125°C</td>
<td>60-V, 1.5-A Military grade device per spec MIL-PRF-38535</td>
<td>Click here</td>
</tr>
<tr>
<td>LM117HVQML-SP</td>
<td>−55°C to 125°C</td>
<td>60-V, 1.5-A Space grade device</td>
<td>Click here</td>
</tr>
<tr>
<td>LM117JAN</td>
<td>−55°C to 125°C</td>
<td>40-V, 1.5-A Military grade device per spec MIL-PRF-38510</td>
<td>Click here</td>
</tr>
<tr>
<td>LM117QML</td>
<td>−55°C to 125°C</td>
<td>40-V, 1.5-A Military grade device per spec MIL-PRF-38535</td>
<td>Click here</td>
</tr>
<tr>
<td>LM117QML-SP</td>
<td>−55°C to 125°C</td>
<td>40-V, 1.5-A Space grade device</td>
<td>Click here</td>
</tr>
</tbody>
</table>
6 Pin Configuration and Functions

### Pin Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADJ</td>
<td>—</td>
<td>Adjust pin</td>
</tr>
<tr>
<td>VIN</td>
<td>I</td>
<td>Input voltage pin for the regulator</td>
</tr>
<tr>
<td>VOUT</td>
<td>O</td>
<td>Output voltage pin for the regulator</td>
</tr>
</tbody>
</table>
7 Specifications

7.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)(1)(2)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power dissipation</td>
<td>Internally Limited</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input-output voltage differential</td>
<td>−0.3</td>
<td>40</td>
<td>V</td>
</tr>
<tr>
<td>Lead temperature</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td>Metal Package (Soldering, 10 seconds)</td>
<td>300</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Plastic Package (Soldering, 4 seconds)</td>
<td>260</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Storage temperature, $T_{stg}$</td>
<td>−65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

7.2 ESD Ratings

<table>
<thead>
<tr>
<th></th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{(ESD)}$ Electrostatic discharge</td>
<td>±3000</td>
<td>V</td>
</tr>
</tbody>
</table>

(1) Manufacturing with less than 500-V HBM is possible with the necessary precautions. Pins listed as ±3000 V may actually have higher performance.

7.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating temperature</td>
<td>−40</td>
<td>125</td>
<td>°C</td>
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</table>

7.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC(1)(2)</th>
<th>LM317A</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>NDE (TO-220)</td>
</tr>
<tr>
<td></td>
<td>3 PINS</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>23.3</th>
<th>59.6</th>
<th>186(3)</th>
<th>54.0</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{\theta JA}$ Junction-to-ambient thermal resistance</td>
<td>°C/W</td>
<td>°C/W</td>
<td>°C/W</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>16.2</th>
<th>39.3</th>
<th>21</th>
<th>51.3</th>
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</thead>
<tbody>
<tr>
<td>$R_{\theta JC(top)}$ Junction-to-case (top) thermal resistance</td>
<td>°C/W</td>
<td>°C/W</td>
<td>°C/W</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>4.9</th>
<th>8.4</th>
<th>—</th>
<th>28.6</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{\theta JB}$ Junction-to-board thermal resistance</td>
<td>°C/W</td>
<td>°C/W</td>
<td>°C/W</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>2.7</th>
<th>1.8</th>
<th>—</th>
<th>3.9</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\Psi_{JT}$ Junction-to-top characterization parameter</td>
<td>°C/W</td>
<td>°C/W</td>
<td>°C/W</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>4.9</th>
<th>8.3</th>
<th>—</th>
<th>28.1</th>
</tr>
</thead>
<tbody>
<tr>
<td>$\Psi_{JB}$ Junction-to-board characterization parameter</td>
<td>°C/W</td>
<td>°C/W</td>
<td>°C/W</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>1.1</th>
<th>—</th>
<th>—</th>
<th>0.9</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{\theta JC(bot)}$ Junction-to-case (bottom) thermal resistance</td>
<td>°C/W</td>
<td>°C/W</td>
<td>°C/W</td>
<td></td>
</tr>
</tbody>
</table>

(1) For more information about traditional and new thermal metrics, see the Semiconductor and IC Package Thermal Metrics application report, SPRA953.

(2) When surface mount packages are used (SOT-223, TO-252), the junction to ambient thermal resistance can be reduced by increasing the PCB copper area that is thermally connected to the package. See Heatsink Requirements for heatsink techniques.

(3) No heatsink.
### 7.5 Electrical Characteristics

Some specifications apply over full operating temperature range as noted. Unless otherwise specified, \( T_J = 25^\circ C \), \( V_{IN} - V_{OUT} = 5 \) V, and \( I_{OUT} = 10 \) mA.\(^{(1)}\)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reference voltage</td>
<td>( T_J = 25^\circ C )</td>
<td>1.238</td>
<td>1.250</td>
<td>1.262</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>( 3 \text{ V} \leq (V_{IN} - V_{OUT}) \leq 40 \text{ V}, )(^{(2)}) ( 10 \text{ mA} \leq I_{OUT} \leq I_{MAX}^{(1)} ) over full operating temperature range</td>
<td>1.225</td>
<td>1.250</td>
<td>1.270</td>
<td>V</td>
</tr>
<tr>
<td>Line regulation</td>
<td>( 3 \text{ V} \leq (V_{IN} - V_{OUT}) \leq 40 \text{ V} ) over full operating temperature range</td>
<td>( T_J = 25^\circ C )</td>
<td>0.005</td>
<td>0.01</td>
<td>%/V</td>
</tr>
<tr>
<td></td>
<td>( T_J = 25^\circ C ) over full operating temperature range</td>
<td>( T_J = 25^\circ C )</td>
<td>0.01</td>
<td>0.02</td>
<td>%/V</td>
</tr>
<tr>
<td>Load regulation</td>
<td>( 10 \text{ mA} \leq I_{OUT} \leq I_{MAX}^{(1)} ) (over full operating temperature range)</td>
<td>( T_J = 25^\circ C )</td>
<td>0.1%</td>
<td>0.5%</td>
<td>%</td>
</tr>
<tr>
<td></td>
<td>( T_J = 25^\circ C )</td>
<td>0.3%</td>
<td>1%</td>
<td>%</td>
<td></td>
</tr>
<tr>
<td>Thermal regulation</td>
<td>20-ms pulse</td>
<td>0.04</td>
<td>0.07</td>
<td>%/W</td>
<td></td>
</tr>
<tr>
<td>Adjustment pin current</td>
<td>over full operating temperature range</td>
<td>50</td>
<td>100</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Adjustment pin current change</td>
<td>( T_J = 25^\circ C ) over full operating temperature range</td>
<td>( 10 \text{ mA} \leq I_{OUT} \leq I_{MAX}^{(1)} ) (over full operating temperature range)</td>
<td>0.2</td>
<td>5</td>
<td>μA</td>
</tr>
<tr>
<td>Temperature stability</td>
<td>( T_{MIN} \leq T_J \leq T_{MAX} ), over full operating temperature range</td>
<td>1%</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Minimum load current</td>
<td>( (V_{IN} - V_{OUT}) = 40 \text{ V} ) over full operating temperature range</td>
<td>3.5</td>
<td>10</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>Current limit</td>
<td>( (V_{IN} - V_{OUT}) \leq 15 \text{ V} ) SOT-223, TO-220 Packages, over full operating temperature range</td>
<td>1.5</td>
<td>2.2</td>
<td>3.4</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td></td>
<td>TO, TO-252 Packages, over full operating temperature range</td>
<td>0.5</td>
<td>0.8</td>
<td>1.8</td>
</tr>
<tr>
<td></td>
<td>( (V_{IN} - V_{OUT}) = 40 \text{ V} ) SOT-223, TO-220 Packages, over full operating temperature range</td>
<td>0.15</td>
<td>0.40</td>
<td>A</td>
<td></td>
</tr>
<tr>
<td></td>
<td>TO, TO-252 Packages</td>
<td>0.075</td>
<td>0.20</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RMS output noise, % of ( V_{OUT} )</td>
<td>10 Hz ( \leq f \leq 10 \text{ kHz} )</td>
<td>0.003%</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Ripple rejection ratio</td>
<td>( V_{OUT} = 10 \text{ V}, f = 120 \text{ Hz}, C_{ADJ} = 0 \mu F ) over full operating temperature range</td>
<td>65</td>
<td></td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td></td>
<td>( V_{OUT} = 10 \text{ V}, f = 120 \text{ Hz}, C_{ADJ} = 10 \mu F ) over full operating temperature range</td>
<td>66</td>
<td>80</td>
<td>dB</td>
<td></td>
</tr>
<tr>
<td>Long-term stability</td>
<td>( T_J = 125^\circ C, 1000 \text{ hrs} )</td>
<td>0.3%</td>
<td>1%</td>
<td>%</td>
<td></td>
</tr>
</tbody>
</table>

\(^{(1)}\) \( I_{MAX} = 1.5 \) A for the NDE (TO-220), \( I_{MAX} = 1.0 \) A for the DCY (SOT-223) package. \( I_{MAX} = 0.5 \) A for the NDT (TO) and NDP (TO-252) packages. Device power dissipation (\( P_D \)) is limited by ambient temperature (\( T_A \)), device maximum junction temperature (\( T_J \)), and package thermal resistance (\( \theta_{JA} \)). The maximum allowable power dissipation at any temperature is: \( P_{D(MAX)} = (T_J - T_A) / \theta_{JA} \). All minimum and maximum limits are ensured to TI’s Average Outgoing Quality Level (AOQL).  

\(^{(2)}\) Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specifications for thermal regulation.
7.6 Typical Characteristics
Output Capacitor = 0 μF unless otherwise noted

**Figure 1. Load Regulation**

**Figure 2. Current Limit**

**Figure 3. Adjustment Current**

**Figure 4. Dropout Voltage**

**Figure 5. V_{OUT} vs V_{IN}, V_{OUT} = V_{REF}**

**Figure 6. V_{OUT} vs V_{IN}, V_{OUT} = 5V**
Typical Characteristics (continued)

Output Capacitor = 0 µF unless otherwise noted

Figure 7. Temperature Stability

Figure 8. Minimum Operating Current

Figure 9. Ripple Rejection

Figure 10. Ripple Rejection

Figure 11. Ripple Rejection

Figure 12. Output Impedance
Typical Characteristics (continued)

Output Capacitor = 0 \mu F unless otherwise noted

Figure 13. Line Transient Response

Figure 14. Load Transient Response
8 Detailed Description

8.1 Overview

In operation, the LM317A develops a nominal 1.25-V reference voltage, $V_{\text{REF}}$, between the output and adjustment terminal. The reference voltage is impressed across program resistor $R_1$ and, because the voltage is constant, a constant current $I_1$ then flows through the output set resistor $R_2$ giving an output voltage calculated by Equation 1:

$$V_{\text{OUT}} = 1.25\ V \left(1 + \frac{R_2}{R_1}\right) + I_{\text{ADJ}} \left(R_2\right)$$

(1)

Because the 100-$\mu$A current from the adjustment terminal represents an error term, the LM317A was designed to minimize $I_{\text{ADJ}}$ and make it very constant with line and load changes. To do this, all quiescent operating current is returned to the output, establishing a minimum load current requirement. If there is insufficient load on the output, the output will rise.
8.2 Functional Block Diagram
8.3 Feature Description

8.3.1 Load Regulation

The LM317A is capable of providing extremely good load regulation but a few precautions are needed to obtain maximum performance. The current set resistor, R1, should be connected near the output terminal of the regulator rather than near the load. If R1 is placed too far from the output terminal, then the increased trace resistance, Rs, will cause an error voltage drop in the adjustment loop and degrade load regulation performance. Therefore, R1 should be placed as close as possible to the output terminal to minimize Rs and maximize load regulation performance.

Figure 16 shows the effect of the trace resistance, Rs, when R1 is placed far from the output terminal of the regulator. It is clear that Rs will cause an error voltage drop especially during higher current loads, so it is important to minimize the Rs trace resistance by keeping R1 close to the regulator output terminal.

![Figure 16. Regulator with Line Resistance in Output Lead](image)

With the TO package, care should be taken to minimize the wire length of the output lead. The ground of R2 can be returned near the ground of the load to provide remote ground sensing and improve load regulation.

8.4 Device Functional Modes

8.4.1 External Capacitors

An input-bypass capacitor is recommended. A 0.1-μF disc or 1-μF solid tantalum on the input is suitable input bypassing for almost all applications. The device is more sensitive to the absence of input bypassing when adjustment or output capacitors are used, but the above values will eliminate the possibility of problems.

The adjustment terminal can be bypassed to ground on the LM317A to improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. With a 10-μF bypass capacitor, 80-dB ripple rejection is obtainable at any output level. Increases over 10 μF do not appreciably improve the ripple rejection at frequencies above 120 Hz. If the bypass capacitor is used, it is sometimes necessary to include protection diodes to prevent the capacitor from discharging through internal low current paths and damaging the device.

In general, the best type of capacitor to use is solid tantalum. Solid tantalum capacitors have low impedance even at high frequencies. Depending upon capacitor construction, it takes about 25 μF in aluminum electrolytic to equal 1-μF solid tantalum at high frequencies. Ceramic capacitors are also good at high frequencies. However, some types have a large decrease in capacitance at frequencies around 0.5 MHz. For this reason, 0.01-μF disc may seem to work better than a 0.1-μF disc as a bypass.

Although the LM317A is stable with no output capacitors, like any feedback circuit, certain values of external capacitance can cause excessive ringing. This occurs with values between 500 pF and 5000 pF. A 1-μF solid tantalum (or 25-μF aluminum electrolytic) on the output swamps this effect and insures stability. Any increase of the load capacitance larger than 10 μF will merely improve the loop stability and output impedance.
Device Functional Modes (continued)

8.4.2 Protection Diodes

When external capacitors are used with any IC regulator, it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low-current points into the regulator. Most 10-μF capacitors have low enough internal series resistance to deliver 20-A spikes when shorted. Although the surge is short, there is enough energy to damage parts of the IC.

When an output capacitor is connected to a regulator and the input is shorted, the output capacitor will discharge into the output of the regulator. The discharge current depends on the value of the capacitor, the output voltage of the regulator, and the rate of decrease of $V_{IN}$. In the LM317A, this discharge path is through a large junction that is able to sustain 15-A surge with no problem. This is not true of other types of positive regulators. For output capacitors of 25 μF or less, there is no need to use diodes.

The bypass capacitor on the adjustment terminal can discharge through a low current junction. Discharge occurs when either the input, or the output, is shorted. Internal to the LM317A is a 50-Ω resistor which limits the peak discharge current. No protection is needed for output voltages of 25 V or less and 10-μF capacitance. Figure 17 shows an LM317A with protection diodes included for use with outputs greater than 25 V and high values of output capacitance.

Figure 17. Regulator With Protection Diodes

\[ V_{OUT} = 1.25 V \left(1 + \frac{R2}{R1}\right) + I_{ADJ} (R2) \]

D1 protects against C1
D2 protects against C2
9 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information
The LM317A is a versatile, high-performance, linear regulator with 1% output-voltage accuracy. An output capacitor can be added to further improve transient response, and the ADJ pin can be bypassed to achieve very high ripple-rejection ratios. Its functionality can be utilized in many different applications that require high performance regulation, such as battery chargers, constant-current regulators, and microprocessor supplies.

9.2 Typical Applications

9.2.1 1.25-V to 25-V Adjustable Regulator
The LM317A can be used as a simple, low-dropout regulator to enable a variety of output voltages needed for demanding applications. By using an adjustable R2 resistor, a variety of output voltages can be made possible as shown in Figure 18.

![Figure 18. 1.25-V to 25-V Adjustable Regulator](image)

NOTE: Full output current not available at high input-output voltages
*Needed if device is more than 6 inches from filter capacitors.
†Optional—improves transient response. Output capacitors in the range of 1 μF to 1000 μF of aluminum or tantalum electrolytic are commonly used to provide improved output impedance and rejection of transients.

\[ V_{OUT} = 1.25(V_\text{IN} - R_2 + I_{ADJ}R_2) \]

9.2.1.1 Design Requirements
The device component count is very minimal, employing two resistors as part of a voltage-divider circuit and an output capacitor for load regulation. An input capacitor is needed if the device is more than 6 inches from filter capacitors. An optional bypass capacitor across R2 can also be used to improve PSRR.

9.2.1.2 Detailed Design Procedure
The output voltage is set based on the selection of the two resistors, R1 and R2, as shown in Figure 18. For details on capacitor selection, refer to External Capacitors.
Typical Applications (continued)

9.2.1.3 Application Curve

As shown in Figure 19, \( V_{\text{OUT}} \) will rise with \( V_{\text{IN}} \) minus some dropout voltage. This dropout voltage during startup will vary with \( R_{\text{OUT}} \).

\[
\begin{array}{c}
V_{\text{OUT}} = 5V \\
T_J = 25^\circ C
\end{array}
\]

\( R_{\text{OUT}} = R_1 + R_2 \)

\( R_{\text{OUT}} = 5\Omega \)

\( R_1 = 243\Omega \)

\( R_2 = 715\Omega \)

Figure 19. \( V_{\text{OUT}} \) vs \( V_{\text{IN}} \), \( V_{\text{OUT}} = 5 \) V

9.2.2 5-V Logic Regulator With Electronic Shutdown

Figure 20 shows a variation of the 5-V output regulator application uses the LM317A, along with an NPN transistor, to provide shutdown control. The NPN will either block or sink the current from the ADJ pin by responding to the TTL pin logic. When TTL is pulled high, the NPN is on and pulls the ADJ pin to GND, and the LM317A outputs about 1.25 V. When TTL is pulled low, the NPN is off and the regulator outputs according to the programmed adjustable voltage.

\[
\begin{array}{c}
V_{\text{IN}} \text{ 7V–35V} \\
V_{\text{IN}} \text{ ADJ} \\
V_{\text{OUT}} 5V
\end{array}
\]

\( C_1 0.1\mu F \)

\( R_1 240 \)

\( R_2 720 \)

\( 2N2219 \)

\( C_2 0.1\mu F \)

\( 1k \)

TTL

NOTE: * Min. output = 1.25 V

Figure 20. 5-V Logic Regulator With Electronic Shutdown
Typical Applications (continued)

9.2.3 Slow Turnon 15-V Regulator

An application of LM317A includes a PNP transistor with a capacitor to implement slow turnon functionality (see Figure 21). As $V_{\text{IN}}$ rises, the PNP sinks current from the ADJ rail. The output voltage at start up is the addition of the 1.25-V reference plus the drop across the base to emitter. While this is happening, the capacitor begins to charge and eventually opens the PNP. At this point, the device functions normally, regulating the output at 15 V. A diode is placed between C1 and $V_{\text{OUT}}$ to provide a path for the capacitor to discharge. Such controlled turnon is useful for limiting the in-rush current.

![Figure 21. Slow Turnon 15-V Regulator](image)

9.2.4 Adjustable Regulator With Improved Ripple Rejection

To improve ripple rejection, a capacitor is used to bypass the ADJ pin to GND (see Figure 22). This is used to smooth output ripple by cleaning the feedback path and stopping unnecessary noise from being fed back into the device, propagating the noise.

![Figure 22. Adjustable Regulator With Improved Ripple Rejection](image)

NOTE: †Solid tantalum

*Discharges C1 if output is shorted to ground
Typical Applications (continued)

9.2.5 High-Stability 10-V Regulator

Using a high-stability shunt voltage reference in the feedback path, such as the LM329, provides damping necessary for a stable, low noise output (see Figure 23).

![Figure 23. High-Stability 10-V Regulator](image)

9.2.6 High-Current Adjustable Regulator

Using the LM195 power transistor in parallel with the LM317A can increase the maximum possible output load current (see Figure 24). Sense resistor R1 provides the 0.6 V across base to emitter to turn on the PNP. This on switch allows current to flow, and the voltage drop across R3 drives three LM195 power transistors designed to carry an excess of 1 A each.

![Figure 24. High-Current Adjustable Regulator](image)

**NOTE**
The selection of R1 determines a minimum load current for the PNP to turn on. The higher the resistor value, the lower the load current must be before the transistors turn on.

**NOTE:** ‡Optional—improves ripple rejection
†Solid tantalum
*Minimum load current = 30 mA
Typical Applications (continued)

9.2.7 Emitter-Follower Current Amplifier

The LM317A is used as a constant-current source in the emitter-follower circuit (see Figure 25). The LM195 power transistor is being used as a current-gain amplifier, boosting the INPUT current. The LM317A provides a more stable current bias than a current bias from a system using only a resistor.

![Figure 25. Emitter-Follower Current Amplifier](image-url)
Typical Applications (continued)

9.2.8 1-A Current Regulator

A simple, fixed-current regulator can be made by placing a resistor between the \( V_{OUT} \) and ADJ pins of the LM317A (see Figure 26). By regulating a constant 1.25 V between these two terminals, a constant current is delivered to the load.

![1-A Current Regulator Diagram](Diagram)

**Figure 26. 1-A Current Regulator**

9.2.9 Common-Emitter Amplifier

Sometimes it is necessary to use a power transistor for high current gain. In this case, the LM317A provides constant current at the collector of the LM195 in this common emitter application (see Figure 27). The 1.25-V reference between \( V_{OUT} \) and ADJ is maintained across the 2.4-\( \Omega \) resistor, providing about 500-mA constant bias current into the collector of the LM195.

![Common-Emitter Amplifier Diagram](Diagram)

**Figure 27. Common-Emitter Amplifier**
Typical Applications (continued)

9.2.10 Low-Cost 3-A Switching Regulator

The LM317A can be used in a switching buck regulator application in cost sensitive applications that require high efficiency. The switch node above D1 oscillates between ground and VIN, as the voltage across sense resistor R1 drives the power transistor on and off. Figure 28 exhibits self-oscillating behavior by negative feedback through R6 and C3 to the ADJ pin of the LM317A.

![Low-Cost 3-A Switching Regulator Diagram]

NOTE: †Solid tantalum
*Core—Arnold A-254168-2 60 turns

Figure 28. Low-Cost 3-A Switching Regulator
Typical Applications (continued)

9.2.11 Current-Limited Voltage Regulator

A maximum limit on output current can be set using Figure 29. The load current travels through R3 and R4. As the load current increases, the voltage drop across R3 increases until the NPN transistor is driven, during which the ADJ pin is pulled down to ground and the output voltage is pulled down to the reference voltage of 1.25 V.

![Figure 29. Current-Limited Voltage Regulator](image)

—Short circuit current is approximately \( \frac{600 \text{ mV}}{R_3} \), or 210 mA

(Compared to LM117’s higher current limit)

—At 50 mA output only \( \frac{3}{4} \) volt of drop occurs in \( R_3 \) and \( R_4 \)

9.2.12 Adjusting Multiple On-Card Regulators With Single Control

Figure 30 shows how multiple LM317A regulators can be controlled by setting one resistor. Because each device maintains the reference voltage of about 1.25 V between its \( V_{OUT} \) and ADJ pins, we can connect each ADJ rail to a single resistor, setting the same output voltage across all devices. This allows for independent outputs, each responding to its corresponding input only. Designers must also consider that by the nature of the circuit, changes to \( R_1 \) and \( R_2 \) will affect all regulators.

![Figure 30. Adjusting Multiple On-Card Regulators With Single Control](image)

NOTE: *All outputs within ±100 mV
†Minimum load—10 mA
Typical Applications (continued)

9.2.13 AC Voltage Regulator

In Figure 31, the top regulator is 6 V above the bottom regulator. It is clear that when the input rises above 6 V plus the dropout voltage, only the top LM317A regulates 6 V at the output. When the input falls below –6 V minus the dropout voltage, only the bottom LM317A regulates –6 V at the output. For regions where the output is not clipped, there is no regulation taking place, so the output follows the input.

![Figure 31. AC Voltage Regulator](image)

9.2.14 12-V Battery Charger

The LM317A can be used in a battery charger application shown in Figure 32, where the device maintains either constant voltage or constant current mode depending on the current charge of the battery. To do this, the part senses the voltage drop across the battery and delivers the maximum charging current necessary to charge the battery. When the battery charge is low, there exists a voltage drop across the sense resistor $R_S$, providing constant current to the battery at that instant. As the battery approaches full charge, the potential drop across $R_S$ approaches zero, reducing the current and maintaining the fixed voltage of the battery.

![Figure 32. 12-V Battery Charger](image)

*R_S—sets output impedance of charger: $Z_{OUT} = R_S \left( 1 + \frac{R_2}{R_1} \right)$

Use of $R_S$ allows low charging rates with fully charged battery.
Typical Applications (continued)

9.2.15 Adjustable 4-A Regulator

Using three LM317A devices in parallel increases load-current capability (see Figure 33). Output voltage is set by the variable resistor tied to the noninverting terminal of the operational amplifier, and reference current to the transistor is developed across the 100-Ω resistor. When output voltage rises, the operational amplifier corrects by drawing current from the base, closing the transistor. This effectively pulls ADJ down and lowers the output voltage through negative feedback.

Figure 33. Adjustable 4-A Regulator
Typical Applications (continued)

9.2.16 Current-Limited 6-V Charger

The current in a battery charger application is limited by switching between constant current and constant voltage states (see Figure 34). When the battery pulls low current, the drop across the 1 Ω resistor is not substantial and the NPN remains off. A constant voltage is seen across the battery, as regulated by the resistor divider. When current through the battery rises past peak current, the 1 Ω provides enough voltage to turn the transistor on, pulling ADJ close to ground. This results in limiting the maximum current to the battery.

![Figure 34. Current-Limited 6-V Charger](image)

**NOTE:** *Sets peak current (0.6A for 1Ω)

**The 1000-μF is recommended to filter out input transients

9.2.17 Digitally-Selected Outputs

Figure 35 demonstrates a digitally-selectable output voltage. In its default state, all transistors are off and the output voltage is set based on R1 and R2. By driving certain transistors, the associated resistor is connected in parallel to R2, modifying the output voltage of the regulator.

![Figure 35. Digitally-Selected Outputs](image)

**NOTE:** *Sets maximum V_{OUT}
10 Power Supply Recommendations

The input supply to the LM317A should be kept at a voltage level lower than the maximum input-to-output differential voltage of 40 V. When possible, the minimum dropout voltage should also be met with extra headroom to keep the LM317A in regulation. TI recommends the use of an input capacitor, especially when the input pin is located more than 6 inches away from the power supply source. For more information regarding capacitor selection, refer to External Capacitors.

11 Layout

11.1 Layout Guidelines

Some layout guidelines should be followed to ensure proper regulation of the output voltage with minimum noise. Traces carrying the load current should be wide to reduce the amount of parasitic trace inductance and the feedback loop from V_{OUT} to ADJ should be kept as short as possible. To improve PSRR, a bypass capacitor can be placed at the ADJ pin and should be located as close as possible to the IC. In cases when V_{IN} shorts to ground, an external diode should be placed from V_{OUT} to V_{IN} to divert the surge current from the output capacitor and protect the IC. Similarly, in cases when a large bypass capacitor is placed at the ADJ pin and V_{OUT} shorts to ground, an external diode should be placed from ADJ to V_{OUT} to provide a path for the bypass capacitor to discharge. These diodes should be placed close to the corresponding IC pins to increase their effectiveness.

11.2 Layout Examples

Figure 36. Layout Example (SOT-223)
11.3 Thermal Considerations

11.3.1 heatsink requirements

The LM317A regulators have internal thermal shutdown to protect the device from over-heating. Under all operating conditions, the junction temperature of the LM317A should not exceed the rated maximum junction temperature ($T_{J(MAX)}$) of 125°C. A heatsink may be required depending on the maximum device power dissipation and the maximum ambient temperature of the application. To determine if a heatsink is needed, the power dissipated by the regulator, $P_D$, must be calculated by Equation 2:

$$P_D = ((V_{IN} - V_{OUT}) \times I_L) + (V_{IN} \times I_G)$$

Figure 38 shows the voltage and currents which are present in the circuit.

The next parameter which must be calculated is the maximum allowable temperature rise, $T_{R(MAX)}$ in Equation 3:

$$T_{R(MAX)} = T_{J(MAX)} - T_{A(MAX)}$$

where

- $T_{J(MAX)}$ is the maximum allowable junction temperature (125°C for the LM317A),
- and $T_{A(MAX)}$ is the maximum ambient temperature that will be encountered in the application.

Using the calculated values for $T_{R(MAX)}$ and $P_D$, the maximum allowable value for the junction-to-ambient thermal resistance ($\theta_{JA}$) can be calculated by Equation 4:

$$\theta_{JA} = \frac{T_{R(MAX)}}{P_D}$$
Thermal Considerations (continued)

If the calculated maximum allowable thermal resistance is higher than the actual package rating, then no additional work is needed. If the calculated maximum allowable thermal resistance is lower than the actual package rating, either the power dissipation \( P_D \) needs to be reduced, the maximum ambient temperature \( T_A(MAX) \) needs to be reduced, the thermal resistance \( \theta_{JA} \) must be lowered by adding a heatsink, or some combination of these measures should be implemented.

If a heatsink is needed, the value can be calculated from Equation 5:

\[
\theta_{HA} \leq (\theta_{JA} - (\theta_{CH} + \theta_{JC}))
\]

where

- \( \theta_{CH} \) is the thermal resistance of the contact area between the device case and the heatsink surface
- \( \theta_{JC} \) is thermal resistance from the junction of the die to surface of the package case

When a value for \( \theta_{HA} \) is found using the equation shown, a heatsink must be selected that has a value that is less than or equal to this number.

The \( \theta_{HA} \) rating is specified numerically by the heatsink manufacturer in the catalog, or shown in a curve that plots temperature rise vs power dissipation for the heatsink.

11.3.2 Heatsinking Surface Mount Packages

The SOT-223 (DCY) and TO-252 (NDP) packages use a copper plane on the PCB and the PCB itself as a heatsink. To optimize the heat-sinking ability of the plane and PCB, solder the tab of the package to the plane.

11.3.2.1 Heatsinking the SOT-223 (DCY) Package

Figure 39 and Figure 40 show the information for the SOT-223 package. Figure 40 assumes a \( \theta_{JA} \) of 74°C/W for 1-oz. copper and 59.6°C/W for 2-oz. copper and a maximum junction temperature of 125°C. See AN-1028 (SNVA036) for thermal enhancement techniques to be used with SOT-223 and TO-252 packages.
11.3.2.2 Heatsinking the TO-252 (NDP) Package

If the maximum allowable value for $\theta_{JA}$ is found to be $\geq 54^\circ\text{C/W}$ (typical rated value) for the TO-252 package, no heatsink is needed because the package alone will dissipate enough heat to satisfy these requirements. If the calculated value for $\theta_{JA}$ falls below these limits, a heatsink is required.

As a design aid, Table 2 shows the value of the $\theta_{JA}$ of NDP the package for different heatsink area. The copper patterns that we used to measure these $\theta_{JA}$s are shown in Figure 45. Figure 41 reflects the same test results as what are in Table 2.

Figure 42 shows the maximum allowable power dissipation versus ambient temperature for the TO-252 device. Figure 43 shows the maximum allowable power dissipation versus copper area (in$^2$) for the TO-252 device. See AN-1028 (SNVA036) for thermal enhancement techniques to be used with SOT-223 and TO-252 packages.
Thermal Considerations (continued)

Table 2. $\theta_{JA}$ Different Heatsink Area

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<th>Layout</th>
<th>Copper Area</th>
<th>Top Side (in$^2$)</th>
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(1) Tab of device attached to topside of copper.

Figure 41. $\theta_{JA}$ vs 2-oz. Copper Area for TO-252

Figure 42. Maximum Allowable Power Dissipation vs Ambient Temperature for TO-252
Figure 43. Maximum Allowable Power Dissipation vs 2-oz. Copper Area for TO-252

Figure 44. Top View of the Thermal Test Pattern in Actual Scale
Figure 45. Bottom View of the Thermal Test Pattern in Actual Scale
12 Device and Documentation Support

12.1 Documentation Support

12.1.1 Related Documentation
- For applications requiring greater output current, see LM150 series (3A) (SNVS772) and LM138 series (5A) (SNVS771) data sheets.
- For the negative complement, see LM137 (SNVS778) series data sheet.
- For thermal enhancement techniques to be used with SOT-223 and TO-252 packages, see AN-1028, *Maximum Power Enhancement Techniques for Power Packages* (SNVA036).

12.2 Community Resources
The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

**TI E2E™ Online Community** *TI's Engineer-to-Engineer (E2E) Community.* Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

12.3 Trademarks
E2E is a trademark of Texas Instruments. All other trademarks are the property of their respective owners.

12.4 Electrostatic Discharge Caution
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

12.5 Glossary
SLYZ022 — *TI Glossary.*
This glossary lists and explains terms, acronyms, and definitions.

13 Mechanical, Packaging, and Orderable Information
The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
## PACKAGING INFORMATION

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<th>Package Type</th>
<th>Package Drawing</th>
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<th>Package Qty</th>
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<th>Lead/Ball Finish (6)</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
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<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
<td>Level-2-260C-1 YEAR</td>
<td>-40 to 125</td>
<td>LM317 AMDT</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317AT</td>
<td>NRND</td>
<td>TO-220</td>
<td>NDE</td>
<td>3</td>
<td>45</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>-40 to 125</td>
<td>LM317AT P+</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317AT/NOPB</td>
<td>ACTIVE</td>
<td>TO-220</td>
<td>NDE</td>
<td>3</td>
<td>45</td>
<td>Pb-Free (RoHS Exempt)</td>
<td>CU SN</td>
<td>Level-1-NA-UNLIM</td>
<td>-40 to 125</td>
<td>LM317AT P+</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
- **RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
- **Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.
(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
### TAPE AND REEL INFORMATION

#### REEL DIMENSIONS

![Reel Dimensions Diagram]

#### TAPE DIMENSIONS

<table>
<thead>
<tr>
<th>A0</th>
<th>Dimension designed to accommodate the component width</th>
</tr>
</thead>
<tbody>
<tr>
<td>B0</td>
<td>Dimension designed to accommodate the component length</td>
</tr>
<tr>
<td>K0</td>
<td>Dimension designed to accommodate the component thickness</td>
</tr>
<tr>
<td>W</td>
<td>Overall width of the carrier tape</td>
</tr>
<tr>
<td>P1</td>
<td>Pitch between successive cavity centers</td>
</tr>
</tbody>
</table>

#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

![Quadrant Assignments Diagram]

*All dimensions are nominal*

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width (W1) (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
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</thead>
<tbody>
<tr>
<td>LM317AMDTX</td>
<td>TO-252</td>
<td>NDP</td>
<td>3</td>
<td>2500</td>
<td>330.0</td>
<td>16.4</td>
<td>6.9</td>
<td>10.5</td>
<td>2.7</td>
<td>8.0</td>
<td>16.0</td>
<td>Q2</td>
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<td>LM317AMDTX/NOPB</td>
<td>TO-252</td>
<td>NDP</td>
<td>3</td>
<td>2500</td>
<td>330.0</td>
<td>16.4</td>
<td>6.9</td>
<td>10.5</td>
<td>2.7</td>
<td>8.0</td>
<td>16.0</td>
<td>Q2</td>
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<tr>
<td>Device</td>
<td>Package Type</td>
<td>Package Drawing</td>
<td>Pins</td>
<td>SPQ</td>
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<td>Width (mm)</td>
<td>Height (mm)</td>
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<td>LM317AMDTX</td>
<td>TO-252</td>
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<td>LM317AMDTX/NOPB</td>
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<td>38.0</td>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
NOTES:  
A. All linear dimensions are in millimeters (inches).  
B. This drawing is subject to change without notice.  
C. Body dimensions do not include mold flash or protrusion.  
D. Falls within JEDEC TO-261 Variation AA.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-252.
NOTES: (continued)

4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature numbers SLMA002 (www.ti.com/lit/slma002) and SLMA004 (www.ti.com/lit/slma004).

5. Vias are optional depending on application, refer to device data sheet. It is recommended that vias under paste be filled, plugged or tented.
NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

7. Board assembly site may have different recommendations for stencil design.
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