LM134/LM234/LM334 3-Terminal Adjustable Current Sources
Check for Samples: LM134, LM234, LM334

FEATURES

- Operates From 1V to 40V
- 0.02%/V Current Regulation
- Programmable From 1μA to 10mA
- True 2-Terminal Operation
- Available as Fully Specified Temperature Sensor
- ±3% Initial Accuracy

DESCRIPTION

The LM134/LM234/LM334 are 3-terminal adjustable current sources featuring 10,000:1 range in operating current, excellent current regulation and a wide dynamic voltage range of 1V to 40V. Current is established with one external resistor and no other parts are required. Initial current accuracy is ±3%. The LM134/LM234/LM334 are true floating current sources with no separate power supply connections. In addition, reverse applied voltages of up to 20V will draw only a few dozen microamperes of current, allowing the devices to act as both a rectifier and current source in AC applications.

The sense voltage used to establish operating current in the LM134 is 64mV at 25°C and is directly proportional to absolute temperature (°K). The simplest one external resistor connection, then, generates a current with \( \approx+0.33%/°C \) temperature dependence. Zero drift operation can be obtained by adding one extra resistor and a diode.

Applications for the current sources include bias networks, surge protection, low power reference, ramp generation, LED driver, and temperature sensing. The LM234-3 and LM234-6 are specified as true temperature sensors with ensured initial accuracy of ±3°C and ±6°C, respectively. These devices are ideal in remote sense applications because series resistance in long wire runs does not affect accuracy. In addition, only 2 wires are required.

The LM134 is specified over a temperature range of −55°C to +125°C, the LM234 from −25°C to +100°C and the LM334 from 0°C to +70°C. These devices are available in TO hermetic, TO-92 and SOIC-8 plastic packages.

Connection Diagrams

Figure 1. SOIC-8 Surface Mount Package
(LM334M; LM334M/NOPB; LM334MX;
LM334MX/NOPB)
See Package Number D

Figure 2. SOIC-8 Alternative Pinout Surface Mount Package
(LM334SM; LM334SM/NOPB; LM334SMX;
LM334SMX/NOPB)
See Package Number D

Figure 3. TO Metal Can Package (Bottom View)
See Package Number NDV

Figure 4. TO-92 Plastic Package (Bottom View)
See Package Number LP

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

**Absolute Maximum Ratings**

<table>
<thead>
<tr>
<th></th>
<th></th>
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<th></th>
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</thead>
<tbody>
<tr>
<td>$V^+ \text{ to } V^-$ Forward Voltage</td>
<td>40V</td>
<td>30V</td>
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<tr>
<td>$V^+ \text{ to } V^-$ Reverse Voltage</td>
<td></td>
<td>20V</td>
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<tr>
<td>R Pin to $V^-$ Voltage</td>
<td>5V</td>
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<td></td>
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<tr>
<td>Set Current</td>
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<tr>
<td>Power Dissipation</td>
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<tr>
<td>ESD Susceptibility$^{(3)}$</td>
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<tr>
<td>Operating Temperature Range$^{(4)}$</td>
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<td>$-55^\circ \text{C to } +125^\circ \text{C}$</td>
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<tr>
<td></td>
<td>LM234/LM234-3/LM234-6</td>
<td>$-25^\circ \text{C to } +100^\circ \text{C}$</td>
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<td>LM334</td>
<td>0°C to +70°C</td>
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**Soldering Information**

<table>
<thead>
<tr>
<th>Package</th>
<th>Temperature</th>
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<tr>
<td>TO-92 Package (10 sec.)</td>
<td>260°C</td>
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<tr>
<td>TO Package (10 sec.)</td>
<td>300°C</td>
</tr>
<tr>
<td>SOIC Package</td>
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<tr>
<td>Vapor Phase (60 sec.)</td>
<td>215°C</td>
</tr>
<tr>
<td>Infrared (15 sec.)</td>
<td>220°C</td>
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$^{(1)}$ "Absolute Maximum Ratings" indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits.

$^{(2)}$ If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

$^{(3)}$ Human body model, 100pF discharged through a 1.5kΩ resistor.

$^{(4)}$ For elevated temperature operation, $T_J$ max is:

<table>
<thead>
<tr>
<th>Device</th>
<th>$T_J$ max</th>
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<tbody>
<tr>
<td>LM134</td>
<td>150°C</td>
</tr>
<tr>
<td>LM234</td>
<td>125°C</td>
</tr>
<tr>
<td>LM334</td>
<td>100°C</td>
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</table>

See Thermal Characteristics.

**Thermal Characteristics**

over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>Thermal Resistance</th>
<th>TO-92</th>
<th>TO</th>
<th>SOIC-8</th>
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<tbody>
<tr>
<td>$\theta_{ja}$ (Junction to Ambient)</td>
<td>180°C/W (0.4&quot; leads)</td>
<td>440°C/W</td>
<td>165°C/W</td>
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<td>160°C/W (0.125&quot; leads)</td>
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<td>$\theta_{jc}$ (Junction to Case)</td>
<td>N/A</td>
<td>32°C/W</td>
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## Electrical Characteristics (1)

<table>
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<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Units</th>
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<tbody>
<tr>
<td><strong>Set Current Error, V+=2.5V (2)</strong></td>
<td>10μA ≤ I_{SET} ≤ 1mA</td>
<td>Min 3 Typ</td>
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<tr>
<td></td>
<td>1mA &lt; I_{SET} ≤ 5mA</td>
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<td></td>
<td>2μA ≤ I_{SET} &lt; 10μA</td>
<td>8%</td>
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<tr>
<td><strong>Ratio of Set Current to Bias Current</strong></td>
<td>100μA ≤ I_{SET} ≤ 1mA</td>
<td>Min 14 Typ</td>
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<tr>
<td></td>
<td>1mA &lt; I_{SET} ≤ 5mA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>2μA ≤ I_{SET} ≤ 100μA</td>
<td></td>
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<tr>
<td><strong>Minimum Operating Voltage</strong></td>
<td>2μA ≤ I_{SET} ≤ 100μA</td>
<td>0.8 V</td>
</tr>
<tr>
<td></td>
<td>100μA &lt; I_{SET} ≤ 1mA</td>
<td>0.9 V</td>
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<td>1mA &lt; I_{SET} ≤ 5mA</td>
<td>1.0 V</td>
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<tr>
<td><strong>Average Change in Set Current with Input Voltage</strong></td>
<td>2μA ≤ I_{SET} ≤ 1mA</td>
<td>Min 0.02 Typ</td>
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<td>1μA &lt; I_{SET} ≤ 5mA</td>
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<tr>
<td><strong>Temperature Dependence of Set Current (3)</strong></td>
<td>25μA ≤ I_{SET} ≤ 1mA</td>
<td>Min 0.96T Typ</td>
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<tr>
<td><strong>Effective Shunt Capacitance</strong></td>
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<td>Min 15 Typ</td>
</tr>
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</table>

(1) Unless otherwise specified, tests are performed at T_{j} = 25°C with pulse testing so that junction temperature does not change during test.

(2) Set current is the current flowing into the V+ pin. For the Basic 2-Terminal Current Source circuit shown in Figure 13, I_{SET} is determined by the following formula: I_{SET} = 67.7 mV/R_{SET} (at 25°C). Set current error is expressed as a percent deviation from this amount. I_{SET} increases at 0.336%/°C at T_{j} = 25°C (227 μV/°C).

(3) I_{SET} is directly proportional to absolute temperature (°K). I_{SET} at any temperature can be calculated from: I_{SET} = I_{0} (T/T_{0}) where I_{0} is I_{SET} measured at T_{0} (°K).

---

## Electrical Characteristics (1)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Set Current Error, V+=2.5V (2)</strong></td>
<td>100μA ≤ I_{SET} ≤ 1mA</td>
<td>Min ±1 Typ</td>
</tr>
<tr>
<td></td>
<td>1mA &lt; I_{SET} ≤ 5mA</td>
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<tr>
<td><strong>Equivalent Temperature Error</strong></td>
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<td>±3°C</td>
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<tr>
<td><strong>Ratio of Set Current to Bias Current</strong></td>
<td>100μA ≤ I_{SET} ≤ 1mA</td>
<td>Min 14 Typ</td>
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<tr>
<td><strong>Minimum Operating Voltage</strong></td>
<td>100μA ≤ I_{SET} ≤ 1mA</td>
<td>0.9 V</td>
</tr>
<tr>
<td><strong>Average Change in Set Current with Input Voltage</strong></td>
<td>100μA ≤ I_{SET} ≤ 1mA</td>
<td>Min 0.02 Typ</td>
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<tr>
<td><strong>Temperature Dependence of Set Current (3)</strong></td>
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<tr>
<td><strong>Effective Shunt Capacitance</strong></td>
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<td>15 pF</td>
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</table>

(1) Unless otherwise specified, tests are performed at T_{j} = 25°C with pulse testing so that junction temperature does not change during test.

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(3) I_{SET} is directly proportional to absolute temperature (°K). I_{SET} at any temperature can be calculated from: I_{SET} = I_{0} (T/T_{0}) where I_{0} is I_{SET} measured at T_{0} (°K).
Typical Performance Characteristics

- **Output Impedance**
  - Figure 5.

- **Maximum Slew Rate**
  - Linear Operation
  - Figure 6.

- **Start-Up**
  - Figure 7.

- **Transient Response**
  - Figure 8.

- **Voltage Across $R_{SET}$ ($V_{R}$)**
  - Figure 9.

- **Current Noise**
  - Figure 10.
Typical Performance Characteristics (continued)

Turn-On Voltage

Figure 11.

Ratio of $I_{\text{SET}}$ to $I_{\text{BIAS}}$

Figure 12.
APPLICATION HINTS

The LM134 has been designed for ease of application, but a general discussion of design features is presented here to familiarize the designer with device characteristics which may not be immediately obvious. These include the effects of slewing, power dissipation, capacitance, noise, and contact resistance.

Calculating $R_{SET}$

The total current through the LM134 ($I_{SET}$) is the sum of the current going through the SET resistor ($I_R$) and the LM134's bias current ($I_{BIAS}$), as shown in Figure 13.

![Figure 13. Basic Current Source](image)

A graph showing the ratio of these two currents is supplied under **Ratio of $I_{SET}$ to $I_{BIAS}$ in Typical Performance Characteristics**. The current flowing through $R_{SET}$ is determined by $V_R$, which is approximately $214\mu V/^\circ K$ (64 mV/298$^\circ K - 214\mu V/^\circ K$).

$$I_{SET} = I_R + I_{BIAS} = \frac{V_R}{R_{SET}} + I_{BIAS}$$  \hspace{1cm} (1)

Since (for a given set current) $I_{BIAS}$ is simply a percentage of $I_{SET}$, the equation can be rewritten

$$I_{SET} = \left( \frac{V_R}{R_{SET}} \right) \left( \frac{n}{n-1} \right)$$  \hspace{1cm} where

- $n$ is the ratio of $I_{SET}$ to $I_{BIAS}$ as specified in **Electrical Characteristics** and shown in the graph  \hspace{1cm} (2)

Since $n$ is typically 18 for $2\mu A \leq I_{SET} \leq 1mA$, the equation can be further simplified to

$$I_{SET} = \left( \frac{V_R}{R_{SET}} \right) (1.059) = \frac{227 \mu V/^\circ K}{R_{SET}}$$  \hspace{1cm} (3)

for most set currents.

Slew Rate

At slew rates above a given threshold (see curve), the LM134 may exhibit non-linear current shifts. The slewing rate at which this occurs is directly proportional to $I_{SET}$. At $I_{SET} = 10\mu A$, maximum $dV/dt$ is 0.01V/$\mu s$; at $I_{SET} = 1mA$, the limit is 1V/$\mu s$. Slew rates above the limit do not harm the LM134, or cause large currents to flow.

Thermal Effects

Internal heating can have a significant effect on current regulation for $I_{SET}$ greater than 100$\mu A$. For example, each 1V increase across the LM134 at $I_{SET} = 1mA$ will increase junction temperature by $\approx 0.4^\circ C$ in still air. Output current ($I_{SET}$) has a temperature coefficient of $\approx 0.33%/^\circ C$, so the change in current due to temperature rise will be $(0.4) (0.33) = 0.132\%$. This is a 10:1 degradation in regulation compared to true electrical effects. Thermal effects, therefore, must be taken into account when DC regulation is critical and $I_{SET}$ exceeds 100$\mu A$. Heat sinking of the TO package or the TO-92 leads can reduce this effect by more than 3:1.
Shunt Capacitance

In certain applications, the 15 pF shunt capacitance of the LM134 may have to be reduced, either because of loading problems or because it limits the AC output impedance of the current source. This can be easily accomplished by buffering the LM134 with an FET as shown in the applications. This can reduce capacitance to less than 3 pF and improve regulation by at least an order of magnitude. DC characteristics (with the exception of minimum input voltage), are not affected.

Noise

Current noise generated by the LM134 is approximately 4 times the shot noise of a transistor. If the LM134 is used as an active load for a transistor amplifier, input referred noise will be increased by about 12dB. In many cases, this is acceptable and a single stage amplifier can be built with a voltage gain exceeding 2000.

Lead Resistance

The sense voltage which determines operating current of the LM134 is less than 100mV. At this level, thermocouple or lead resistance effects should be minimized by locating the current setting resistor physically close to the device. Sockets should be avoided if possible. It takes only 0.7 Ω contact resistance to reduce output current by 1% at the 1 mA level.

Sensing Temperature

The LM134 makes an ideal remote temperature sensor because its current mode operation does not lose accuracy over long wire runs. Output current is directly proportional to absolute temperature in degrees Kelvin, according to the following formula:

$$I_{SET} = \frac{227 \mu V/^\circ K (T)}{R_{SET}}$$

(4)

Calibration of the LM134 is greatly simplified because of the fact that most of the initial inaccuracy is due to a gain term (slope error) and not an offset. This means that a calibration consisting of a gain adjustment only will trim both slope and zero at the same time. In addition, gain adjustment is a one point trim because the output of the LM134 extrapolates to zero at 0°C, independent of $R_{SET}$ or any initial inaccuracy.

![Figure 14. Gain Adjustment](image)

This property of the LM134 is illustrated in the accompanying graph. Line abc is the sensor current before trimming. Line a'b'c' is the desired output. A gain trim done at T2 will move the output from b to b' and will simultaneously correct the slope so that the output at T1 and T3 will be correct. This gain trim can be done on $R_{SET}$ or on the load resistor used to terminate the LM134. Slope error after trim will normally be less than ±1%. To maintain this accuracy, however, a low temperature coefficient resistor must be used for $R_{SET}$.

A 33 ppm/^°C drift of $R_{SET}$ will give a 1% slope error because the resistor will normally see about the same temperature variations as the LM134. Separating $R_{SET}$ from the LM134 requires 3 wires and has lead resistance problems, so is not normally recommended. Metal film resistors with less than 20 ppm/^°C drift are readily available. Wire wound resistors may also be used where best stability is required.
Application as a Zero Temperature Coefficient Current Source

Adding a diode and a resistor to the standard LM134 configuration can cancel the temperature-dependent characteristic of the LM134. The circuit shown in Figure 15 balances the positive tempco of the LM134 (about +0.23 mV/°C) with the negative tempco of a forward-biased silicon diode (about −2.5 mV/°C).

The set current (I\text{SET}) is the sum of I_1 and I_2, each contributing approximately 50% of the set current, and I\text{BIAS}. I\text{BIAS} is usually included in the I_1 term by increasing the V_R value used for calculations by 5.9%. (See CALCULATING R\text{SET}.)

\[
I_{SET} = I_1 + I_2 + I_{BIAS}, \text{ where} \\
I_1 = \frac{V_R}{R_1} \quad \text{and} \quad I_2 = \frac{V_R + V_D}{R_2}
\]  

(\text{5})

The first step is to minimize the tempco of the circuit, using the following equations. An example is given using a value of +227 \mu V/°C as the tempco of the LM134 (which includes the I\text{BIAS} component), and −2.5 mV/°C as the tempco of the diode (for best results, this value should be directly measured or obtained from the manufacturer of the diode).

\[
\frac{dI_{SET}}{dT} = \frac{dI_1}{dT} + \frac{dI_2}{dT} \\
\approx \frac{227 \mu V/°C}{R_1} + \frac{227 \mu V/°C - 2.5 mV/°C}{R_2} \\
= 0 \text{ (solve for tempco = 0)}
\]  

(\text{6})

\[
R_2 \approx \frac{2.5 mV/°C - 227 \mu V/°C}{227 \mu V/°C} \approx 10.0
\]  

(\text{7})

With the R_1 to R_2 ratio determined, values for R_1 and R_2 should be determined to give the desired set current. The formula for calculating the set current at T = 25°C is shown below, followed by an example that assumes the forward voltage drop across the diode (V_D) is 0.6V, the voltage across R_1 is 67.7mV (64 mV + 5.9% to account for I\text{BIAS}), and R_2/R_1 = 10 (from the previous calculations).
This circuit will eliminate most of the LM134's temperature coefficient, and it does a good job even if the estimates of the diode's characteristics are not accurate (as the following example will show). For lowest tempco with a specific diode at the desired \( I_{\text{SET}} \), however, the circuit should be built and tested over temperature. If the measured tempco of \( I_{\text{SET}} \) is positive, \( R_2 \) should be reduced. If the resulting tempco is negative, \( R_2 \) should be increased. The recommended diode for use in this circuit is the 1N457 because its tempco is centered at 11 times the tempco of the LM134, allowing \( R_2 = 10 \times R_1 \). You can also use this circuit to create a current source with non-zero tempcos by setting the tempco component of the tempco equation to the desired value instead of 0.

**EXAMPLE: A 1mA, Zero-Tempco Current Source**

First, solve for \( R_1 \) and \( R_2 \):

\[
I_{\text{SET}} \approx 1 \text{ mA} = \frac{0.134 \text{ V}}{R_1} \\
R_2 = 134 \Omega = 10 \times R_1 \\
R_2 = 1340 \Omega
\]

(9)

The values of \( R_1 \) and \( R_2 \) can be changed to standard 1% resistor values (\( R_1 = 133 \Omega \) and \( R_2 = 1.33 \text{k}\Omega \)) with less than a 0.75% error.

If the forward voltage drop of the diode was 0.65V instead of the estimate of 0.6V (an error of 8%), the actual set current will be

\[
I_{\text{SET}} = \frac{67.7 \text{ mV}}{R_1} + \frac{67.7 \text{ mV} + 0.65 \text{V}}{R_2} \\
= \frac{67.7 \text{ mV}}{133} + \frac{67.7 \text{ mV} + 0.65 \text{V}}{1330} \\
= 1.049 \text{ mA}
\]

(10)

an error of less than 5%.

If the estimate for the tempco of the diode's forward voltage drop was off, the tempco cancellation is still reasonably effective. Assume the tempco of the diode is 2.6mV/°C instead of 2.5mV/°C (an error of 4%). The tempco of the circuit is now:

\[
\frac{dI_{\text{SET}}}{dT} = \frac{dI_1}{dT} + \frac{dI_2}{dT} \\
= \frac{227 \mu \text{V/°C}}{133 \Omega} + \frac{227 \mu \text{V/°C} - 2.6 \text{ mV/°C}}{1330 \Omega} \\
= -77 \text{ nA/°C}
\]

(11)

A 1mA LM134 current source with no temperature compensation would have a set resistor of 68Ω and a resulting tempco of

\[
\frac{227 \mu \text{V/°C}}{68 \Omega} = 3.3 \mu \text{A/°C}
\]

(12)

So even if the diode's tempco varies as much as ±4% from its estimated value, the circuit still eliminates 98% of the LM134's inherent tempco.
Typical Applications

*Select R3 = V_{REF}/583μA. V_{REF} may be any stable positive voltage ≥ 2V
Trim R3 to calibrate

Figure 16. Ground Referenced Fahrenheit Thermometer

*Output impedance of the LM134 at the "R" pin is approximately
\[ R_3 \]
where \( R_2 \) is the equivalent external resistance connected from the V- pin to ground. This negative resistance can be reduced by a factor of 5 or more by inserting an equivalent resistor \( R_3 = (R_2/16) \) in series with the output.

Figure 18. Low Output Impedance Thermometer
Figure 19. Low Output Impedance Thermometer

Figure 20. Higher Output Current

*Select R1 and C1 for optimum stability

Figure 21. Basic 2-Terminal Current Source
Figure 22. Micropower Bias

Figure 23. Low Input Voltage Reference Driver

Figure 24. Ramp Generator
*Select ratio of R1 to R2 to obtain zero temperature drift

**Figure 25. 1.2V Reference Operates on 10 μA and 2V**

*Select ratio of R1 to R2 for zero temperature drift

**Figure 26. 1.2V Regulator with 1.8V Minimum Input**

**Figure 27. Zener Biasing**
*For ±10% adjustment, select $R_{\text{SET}}$ 10% high, and make $R_1 \approx 3 R_{\text{SET}}$

Figure 28. Alternate Trimming Technique

*Select Q1 or Q2 to ensure at least 1V across the LM134. $V_p (1 - I_{\text{SET}}/I_{\text{DSS}}) \geq 1.2V.$

Figure 29. Buffer for Photoconductive Cell
Figure 30. FET Cascoding for Low Capacitance and/or Ultra High Output Impedance

*\[ Z_{OUT} \approx -16 \times R_1 \] (R1/V_{IN} must not exceed I_{SET})

Figure 31. Generating Negative Output Impedance

*Use minimum value required to ensure stability of protected device. This minimizes inrush current to a direct short.

Figure 32. In-Line Current Limiter
Schematic Diagram
### REVISION HISTORY

**Changes from Revision C (April 2013) to Revision D**

<table>
<thead>
<tr>
<th>Change Description</th>
<th>Page</th>
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</thead>
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<tr>
<td>Changed layout of National Data Sheet to TI format</td>
<td>16</td>
</tr>
</tbody>
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Product Folder Links: [LM134](#) [LM234](#) [LM334](#)
## PACKAGING INFORMATION

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<thead>
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<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
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<th>Package Qty</th>
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<th>Lead/Ball Finish</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
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<td>Green (RoHS &amp; no Sb/Br)</td>
<td>Call TI</td>
<td>Level-1-NA-UNLIM</td>
<td>-55 to 125 (LM134H, LM134H)</td>
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<tr>
<td>LM234Z-3/NOPB</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>1800</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
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<td>-25 to 100</td>
<td>LM234 Z-3</td>
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<tr>
<td>LM234Z-6/NOPB</td>
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<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>1800</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
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<td>-25 to 100</td>
<td>LM234 Z-6</td>
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<td>LM334 MWC</td>
<td>ACTIVE</td>
<td>WAFERSALE</td>
<td>YS</td>
<td>0</td>
<td>1</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>Call TI</td>
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<td>LM334M</td>
<td>NRND</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>95</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>0 to 70</td>
<td>LM334 M</td>
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<tr>
<td>LM334M/NOPB</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>95</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
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<td>LM334MX/NOPB</td>
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<td>0 to 70</td>
<td>LM334 M</td>
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<td>LM334SM/NOPB</td>
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<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>95</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>LM334 SM</td>
<td></td>
</tr>
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<td>LM334SMX</td>
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<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>TBD</td>
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<td>Call TI</td>
<td>0 to 70</td>
<td>LM334 SM</td>
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</tr>
<tr>
<td>LM334SMX/NOPB</td>
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<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
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<td>LM334 SM</td>
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<td>LP</td>
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<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>LM334 Z</td>
<td></td>
</tr>
<tr>
<td>LM334Z/NOPB</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>1800</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>LM334 Z</td>
<td></td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBsolete:** TI has discontinued the production of the device.
RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substances do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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**TAPE AND REEL INFORMATION**

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0  (mm)</th>
<th>B0  (mm)</th>
<th>K0  (mm)</th>
<th>P1  (mm)</th>
<th>W  (mm)</th>
<th>Pin1 Quadrant</th>
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</thead>
<tbody>
<tr>
<td>LM334MX/NOPB</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>6.5</td>
<td>5.4</td>
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<td>12.0</td>
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<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>6.5</td>
<td>5.4</td>
<td>2.0</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
</tr>
<tr>
<td>LM334SMX/NOPB</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>330.0</td>
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<td>8.0</td>
<td>12.0</td>
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</tr>
</tbody>
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*All dimensions are nominal.*
## TAPE AND REEL BOX DIMENSIONS

*All dimensions are nominal

<table>
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<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
</tr>
</thead>
<tbody>
<tr>
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<td>D</td>
<td>8</td>
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<td>35.0</td>
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<td>LM334SMX</td>
<td>SOIC</td>
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<td>8</td>
<td>2500</td>
<td>367.0</td>
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<td>35.0</td>
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<tr>
<td>LM334SMX/NOPB</td>
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<td>8</td>
<td>2500</td>
<td>367.0</td>
<td>367.0</td>
<td>35.0</td>
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</table>
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-46.
EXAMPLE BOARD LAYOUT

NDV0003H

TO-CAN - 2.67 mm max height

LAND PATTERN EXAMPLE
NON-SOLDER MASK DEFINED
SCALE: 12X
NOTES:

1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 [0.15] per side.
4. This dimension does not include interlead flash.
5. Reference JEDEC registration MS-012, variation AA.
NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

9. Board assembly site may have different recommendations for stencil design.
Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Lead dimensions are not controlled within this area.
4. Reference JEDEC TO-226, variation AA.
5. Shipping method:
   a. Straight lead option available in bulk pack only.
   b. Formed lead option available in tape and reel or ammo pack.
   c. Specific products can be offered in limited combinations of shipping medium and lead options.
   d. Consult product folder for more information on available options.
EXAMPLE BOARD LAYOUT

TO-92 - 5.34 mm max height

LAND PATTERN EXAMPLE
STRAIGHT LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X

LAND PATTERN EXAMPLE
FORMED LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X
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