**LM45 SOT-23 Precision Centigrade Temperature Sensors**

Check for Samples: LM45

**FEATURES**
- Calibrated Directly in ° Celsius (Centigrade)
- Linear + 10.0 mV/°C Scale Factor
- ±3°C Accuracy Guaranteed
- Rated for Full −20° to +100°C Range
- Suitable for Remote Applications
- Low Cost Due to Wafer-Level Trimming
- Operates from 4.0V to 10V
- Less than 120 μA Current Drain
- Low Self-Heating, 0.20°C in Still Air
- Nonlinearity Only ±0.8°C Max Over Temp
- Low Impedance Output, 20Ω for 1 mA Load

**APPLICATIONS**
- Battery Management
- FAX Machines
- Printers
- Portable Medical Instruments
- HVAC
- Power Supply Modules
- Disk Drives
- Computers
- Automotive

**DESCRIPTION**
The LM45 series are precision integrated-circuit temperature sensors, whose output voltage is linearly proportional to the Celsius (Centigrade) temperature. The LM45 does not require any external calibration or trimming to provide accuracies of ±2°C at room temperature and ±3°C over a full −20 to +100°C temperature range. Low cost is assured by trimming and calibration at the wafer level. The LM45’s low output impedance, linear output, and precise inherent calibration make interfacing to readout or control circuitry especially easy. It can be used with a single power supply, or with plus and minus supplies. As it draws only 120 μA from its supply, it has very low self-heating, less than 0.2°C in still air. The LM45 is rated to operate over a −20° to +100°C temperature range.

**Connection Diagram**

![Connection Diagram](image)

*Figure 1. SOT-23 Top View Package Number DBZ0003A*

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Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

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Typical Applications

Choose $R_1 = -V_S/50 \, \mu A$

$V_{OUT} = (10 \, mV/°C \times \text{Temp} \, °C)$

$V_{OUT} = +1,000 \, mV \text{ at } +100\, °C$

$V_{OUT} = +250 \, mV \text{ at } +25\, °C$

$V_{OUT} = -200 \, mV \text{ at } -20\, °C$

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

**Absolute Maximum Ratings**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>$+12V \text{ to } -0.2V$</td>
</tr>
<tr>
<td>Output Voltage</td>
<td>$+V_S + 0.6V \text{ to } -1.0V$</td>
</tr>
<tr>
<td>Output Current</td>
<td>10 mA</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>$-65, °C \text{ to } +150, °C$</td>
</tr>
<tr>
<td>ESD Susceptibility</td>
<td>Human Body Model 2000V</td>
</tr>
<tr>
<td></td>
<td>Machine Model 250V</td>
</tr>
</tbody>
</table>

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions.

(2) Human body model, 100 pF discharged through a 1.5 kΩ resistor. Machine model, 200 pF discharged directly into each pin.

**Operating Ratings**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
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<tbody>
<tr>
<td>Specified Temperature Range</td>
<td>$T_{MIN} \text{ to } T_{MAX}$</td>
</tr>
<tr>
<td>LM45B, LM45C</td>
<td>$-20, °C \text{ to } +100, °C$</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>$-40, °C \text{ to } +125, °C$</td>
</tr>
<tr>
<td>Supply Voltage Range (+$V_S$)</td>
<td>$+4.0V \text{ to } +10V$</td>
</tr>
</tbody>
</table>

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions.


(3) Reflow temperature profiles are different for lead-free and non-lead-free packages.

(4) Thermal resistance of the SOT-23 package is 260°C/W, junction to ambient when attached to a printed circuit board with 2 oz. foil as shown in Figure 15.
**Electrical Characteristics**

Unless otherwise noted, these specifications apply for \( +V_S = +5 \text{Vdc} \) and \( I_{\text{LOAD}} = +50 \mu\text{A} \), in the circuit of Figure 3. These specifications also apply from +2.5°C to \( T_{\text{MAX}} \) in the circuit of Figure 2 for \( +V_S = +5 \text{Vdc} \). **Boldface limits apply for** \( T_A = T_J = T_{\text{MIN}} \text{ to } T_{\text{MAX}} \); all other limits \( T_A = T_J = +25 \degree \text{C} \), unless otherwise noted.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM45B</th>
<th>LM45C</th>
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<tbody>
<tr>
<td></td>
<td></td>
<td>Typical</td>
<td>Limit(1)</td>
</tr>
<tr>
<td>Accuracy(2)</td>
<td>( T_A = +25 \degree \text{C} )</td>
<td>±2.0</td>
<td>±3.0</td>
</tr>
<tr>
<td></td>
<td>( T_A = T_{\text{MAX}} )</td>
<td>±3.0</td>
<td>±4.0</td>
</tr>
<tr>
<td></td>
<td>( T_A = T_{\text{MIN}} )</td>
<td>±3.0</td>
<td>±4.0</td>
</tr>
<tr>
<td>Nonlinearity(3)</td>
<td>( T_{\text{MIN}} \leq T_A \leq T_{\text{MAX}} )</td>
<td>±0.8</td>
<td>±0.8</td>
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<tr>
<td>Sensor Gain (Average Slope)</td>
<td>( T_{\text{MIN}} \leq T_A \leq T_{\text{MAX}} )</td>
<td>±9.7</td>
<td>±9.7</td>
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<tr>
<td>Load Regulation(4)</td>
<td>( 0 \leq I_L \leq +1 \text{mA} )</td>
<td>±35</td>
<td>±35</td>
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<tr>
<td>Line Regulation(4)</td>
<td>( +4.0 \text{V} \leq V_S \leq +10 \text{V} )</td>
<td>±0.80</td>
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<tr>
<td></td>
<td></td>
<td>±1.2</td>
<td>±1.2</td>
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<tr>
<td>Quiescent Current(5)</td>
<td>( +4.0 \text{V} \leq V_S \leq +10 \text{V}, +25 \degree \text{C} )</td>
<td>120</td>
<td>120</td>
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<tr>
<td></td>
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<td>160</td>
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<tr>
<td>Change of Quiescent Current(5)</td>
<td>( +4.0 \text{V} \leq V_S \leq +10 \text{V} )</td>
<td>2.0</td>
<td>2.0</td>
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<tr>
<td>Temperature Coefficient of Quiescent Current</td>
<td></td>
<td>+2.0</td>
<td>+2.0</td>
</tr>
<tr>
<td>Minimum Temperature for Rated Accuracy</td>
<td>In circuit of Figure 2, ( I_L = 0 )</td>
<td>+2.5</td>
<td>+2.5</td>
</tr>
<tr>
<td>Long Term Stability(6)</td>
<td>( T_J = T_{\text{MAX}} ), for 1000 hours</td>
<td>±0.12</td>
<td>±0.12</td>
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</tbody>
</table>

(1) Limits are guaranteed to TI's AOQL (Average Outgoing Quality Level).
(2) Accuracy is defined as the error between the output voltage and 10 mV/°C times the device's case temperature, at specified conditions of voltage, current, and temperature (expressed in °C).
(3) Nonlinearity is defined as the deviation of the output-voltage-versus-temperature curve from the best-fit straight line, over the device's rated temperature range.
(4) Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.
(5) Quiescent current is measured using the circuit of Figure 2.
(6) For best long-term stability, any precision circuit will give best results if the unit is aged at a warm temperature, and/or temperature cycled for at least 46 hours before long-term life test begins. This is especially true when a small (Surface-Mount) part is wave-soldered; allow time for stress relaxation to occur.
Typical Performance Characteristics

To generate these curves the LM45 was mounted to a printed circuit board as shown in Figure 15.

**Thermal Resistance Junction to Air**

![Thermal Resistance Junction to Air](Figure 4)

**Thermal Time Constant**

![Thermal Time Constant](Figure 5)

**Thermal Response in Still Air with Heat Sink (Figure 15)**

![Thermal Response in Still Air with Heat Sink](Figure 6)

**Thermal Response in Stirred Oil Bath with Heat Sink**

![Thermal Response in Stirred Oil Bath with Heat Sink](Figure 7)

**Start-Up Voltage vs Temperature**

![Start-Up Voltage vs Temperature](Figure 8)

**Quiescent Current vs Temperature (In Circuit of Figure 2)**

![Quiescent Current vs Temperature](Figure 9)
Typical Performance Characteristics (continued)

To generate these curves the LM45 was mounted to a printed circuit board as shown in Figure 15.

- **Quiescent Current vs Temperature (In Circuit of Figure 3)**
- **Accuracy vs Temperature (Guaranteed)**
- **Noise Voltage**
- **Supply Voltage vs Supply Current**
- **Start-Up Response**
APPLICATIONS

The LM45 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface and its temperature will be within about 0.2°C of the surface temperature.

This presumes that the ambient air temperature is almost the same as the surface temperature; if the air temperature were much higher or lower than the surface temperature, the actual temperature of the LM45 die would be at an intermediate temperature between the surface temperature and the air temperature.

To ensure good thermal conductivity the backside of the LM45 die is directly attached to the GND pin. The lands and traces to the LM45 will, of course, be part of the printed circuit board, which is the object whose temperature is being measured. These printed circuit board lands and traces will not cause the LM45s temperature to deviate from the desired temperature.

Alternatively, the LM45 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LM45 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. Printed-circuit coatings and varnishes such as Humiseal and epoxy paints or dips are often used to insure that moisture cannot corrode the LM45 or its connections.

Temperature Rise of LM45 Due to Self-Heating (Thermal Resistance)

<table>
<thead>
<tr>
<th></th>
<th>SOT-23</th>
<th>SOT-23</th>
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</thead>
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<tr>
<td></td>
<td>no heat sink*</td>
<td>small heat fin**</td>
</tr>
<tr>
<td>Still air</td>
<td>450°C/W</td>
<td>260°C/W</td>
</tr>
<tr>
<td>Moving air</td>
<td>180°C/W</td>
<td></td>
</tr>
</tbody>
</table>

Typical Applications

CAPACITIVE LOADS

Like most micropower circuits, the LM45 has a limited ability to drive heavy capacitive loads. The LM45 by itself is able to drive 500 pF without special precautions. If heavier loads are anticipated, it is easy to isolate or decouple the load with a resistor; see Figure 16. Or you can improve the tolerance of capacitance with a series R-C damper from output to ground; see Figure 17.

Any linear circuit connected to wires in a hostile environment can have its performance affected adversely by intense electromagnetic sources such as relays, radio transmitters, motors with arcing brushes, SCR transients, etc, as its wiring can act as a receiving antenna and its internal junctions can act as rectifiers. For best results in such cases, a bypass capacitor from \( V_{IN} \) to ground and a series R-C damper such as 75Ω in series with 0.2 or 1 μF from output to ground, as shown in Figure 17, are often useful.
Figure 16. LM45 with Decoupling from Capacitive Load

Figure 17. LM45 with R-C Damper

Figure 18. Temperature Sensor, Single Supply, −20°C to +100°C

Figure 19. 4-to-20 mA Current Source (0°C to +100°C)
Figure 20. Fahrenheit Thermometer

Figure 21. Centigrade Thermometer (Analog Meter)

Figure 22. Expanded Scale Thermometer (50° to 80° Fahrenheit, for Example Shown)
Figure 23. Temperature To Digital Converter (Serial Output) (+128°C Full Scale)

Figure 24. Temperature To Digital Converter (Parallel Outputs for Standard Data Bus to μP Interface) (128°C Full Scale)
* = 1% or 2% film resistor
- Trim R_B for V_B = 3.075V
- Trim R_C for V_C = 1.955V
- Trim R_A for V_A = 0.075V + 100mV/°C × T_ambient
- Example, V_A = 2.275V at 22°C

Figure 25. Bar-Graph Temperature Display (Dot Mode)

Figure 26. LM45 With Voltage-To-Frequency Converter And Isolated Output
(2.5°C to +100°C; 25 Hz to 1000 Hz)
Block Diagram
## REVISION HISTORY

Changes from Revision B (February 2013) to Revision C | Page
---|---
 Changed layout of National Data Sheet to TI format | 11
## PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish (6)</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
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<tbody>
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<td>Call TI</td>
<td>-20 to 100</td>
<td>T4B</td>
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<td>-20 to 100</td>
<td>T4B</td>
<td>Samples</td>
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<td>Call TI</td>
<td>Call TI</td>
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</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBsolete: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.
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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
TAPE AND REEL INFORMATION

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<th>Device</th>
<th>Package Type</th>
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<th>SPQ</th>
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<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
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<td>SOT-23</td>
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<td>3</td>
<td></td>
<td>178.0</td>
<td>8.4</td>
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*All dimensions are nominal.*
### TAPE AND REEL BOX DIMENSIONS

*All dimensions are nominal

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</table>
Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.
NOTES:
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-236, except minimum foot length.
NOTES: (continued)

4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

7. Board assembly site may have different recommendations for stencil design.
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