LM4808 Boomer® Audio Power Amplifier Series
Low Voltage High Power Audio Power Amplifier
Check for Samples: LM4808

FEATURES
• WSON, VSSOP, and SOIC Surface Mount Packaging
• Switch On/Off Click Suppression
• Excellent Power Supply Ripple Rejection
• Unity-Gain Stable
• Minimum External Components

APPLICATIONS
• Headphone Amplifier
• Personal Computers
• Portable Electronic Devices

KEY SPECIFICATIONS
• THD+N at 1kHz at 105mW Continuous Average Output Power Into 16Ω 0.1 % (typ)
• THD+N at 1kHz at 70mW Continuous Average Output Power Into 32Ω 0.1 % (typ)
• Output Power at 0.1% THD+N at 1kHz Into 32Ω 70 mW (typ)

DESCRIPTION
The LM4808 is a dual audio power amplifier capable of delivering 105mW per channel of continuous average power into a 16Ω load with 0.1% (THD+N) from a 5V power supply.

Boomer audio power amplifiers were designed specifically to provide high quality output power with a minimal amount of external components using surface mount packaging. Since the LM4808 does not require bootstrap capacitors or snubber networks, it is optimally suited for low-power portable systems.

The unity-gain stable LM4808 can be configured by external gain-setting resistors.

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Typical Application

*Refer to the APPLICATION INFORMATION section for information concerning proper selection of the input and output coupling capacitors.

Figure 1. Typical Audio Amplifier Application Circuit

Connection Diagram

Figure 2. Top View
WSON Package
See Package Number NGL0008B

Figure 3. Top View
SOIC & VSSOP Package
See Package Number D0008A, DGK0008A
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

**ABSOLUTE MAXIMUM RATINGS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>LM4808</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>6.0V</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>−65°C to +150°C</td>
</tr>
<tr>
<td>Input Voltage</td>
<td>−0.3V to VDD + 0.3V</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>Internally limited</td>
</tr>
<tr>
<td>ESD Susceptibility</td>
<td>3500V</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>150°C</td>
</tr>
</tbody>
</table>

**Soldering Information**

<table>
<thead>
<tr>
<th>Soldering Method</th>
<th>Thermal Resistance (\theta_J)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Vapor Phase (60 seconds)</td>
<td>(\theta_J = \frac{56°C}{W})</td>
</tr>
<tr>
<td>Infrared (15 seconds)</td>
<td>(\theta_J = \frac{210°C}{W})</td>
</tr>
</tbody>
</table>

(1) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

(2) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.

(3) The maximum power dissipation must be derated at elevated temperatures and is dictated by \(P_{MAX} = \frac{T_{JMAX} - T_A}{\theta_J}\). 

(4) Human body model, 100 pF discharged through a 1.5 kΩ resistor.


(7) The given \(\theta_J\) is for an LM4808 packaged in an NGL0008B with the Exposed-DAP soldered to a printed circuit board copper pad with an area equivalent to that of the Exposed-DAP itself.

(8) The given \(\theta_J\) is for an LM4808 packaged in an NGL0008B with the Exposed-DAP not soldered to any printed circuit board copper.

**OPERATING RATINGS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM4808</th>
<th>Units (Limits)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Temperature Range</td>
<td>(T_{MIN} \leq T_A \leq T_{MAX})</td>
<td>(-40°C \leq T_A \leq 85°C)</td>
<td></td>
</tr>
<tr>
<td>Supply Voltage</td>
<td>(2.0V \leq V_{DD} \leq 5.5V)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**ELECTRICAL CHARACTERISTICS**

The following specifications apply for \(V_{DD} = 5V\) unless otherwise specified, limits apply to \(T_A = 25°C\).

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>LM4808</th>
<th>Units (Limits)</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{DD})</td>
<td>Supply Voltage</td>
<td>(V_{IN} = 0V, I_O = 0A)</td>
<td>1.2</td>
<td>3.0 mA (max)</td>
</tr>
</tbody>
</table>

(1) All voltages are measured with respect to the ground pin, unless otherwise specified.

(2) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.

(3) Tested limits are specified to AOQL (Average Outgoing Quality Level). Datasheet min/max specification limits are specified by design, test, or statistical analysis.
### ELECTRICAL CHARACTERISTICS (1) (2) (continued)

The following specifications apply for $V_{DD} = 5V$ unless otherwise specified, limits apply to $T_A = 25^\circ C$.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>LM4808</th>
<th>Units (Limits)</th>
</tr>
</thead>
<tbody>
<tr>
<td>$P_{tot}$</td>
<td>Total Power Dissipation</td>
<td>$V_{IN} = 0V$, $I_O = 0A$</td>
<td>6</td>
<td>16.5 mW (max)</td>
</tr>
<tr>
<td>$V_{OS}$</td>
<td>Input Offset Voltage</td>
<td>$V_{IN} = 0V$</td>
<td>10</td>
<td>50 mV (max)</td>
</tr>
<tr>
<td>$I_{bias}$</td>
<td>Input Bias Current</td>
<td></td>
<td>0</td>
<td>pA</td>
</tr>
<tr>
<td>$V_{CM}$</td>
<td>Common Mode Voltage</td>
<td></td>
<td>0</td>
<td>V</td>
</tr>
<tr>
<td>$G_V$</td>
<td>Open-Loop Voltage Gain</td>
<td>$R_L = 5\Omega$</td>
<td>67</td>
<td>dB</td>
</tr>
<tr>
<td>$I_O$</td>
<td>Max Output Current</td>
<td>THD+N &lt; 0.1 %</td>
<td>70</td>
<td>mA</td>
</tr>
<tr>
<td>$R_O$</td>
<td>Output Resistance</td>
<td></td>
<td>0.1</td>
<td>Ω</td>
</tr>
<tr>
<td>$V_O$</td>
<td>Output Swing</td>
<td>$R_L = 32\Omega$, 0.1% THD+N, Min</td>
<td>.3</td>
<td>V</td>
</tr>
<tr>
<td>$V_O$</td>
<td>Output Swing</td>
<td>$R_L = 32\Omega$, 0.1% THD+N, Max</td>
<td>4.7</td>
<td>V</td>
</tr>
<tr>
<td>$PSRR$</td>
<td>Power Supply Rejection Ratio</td>
<td>$C_b = 1.0\mu F$, $V_{ripple} = 100\text{mV}_{pp}$, $f = 100\text{Hz}$</td>
<td>89</td>
<td>dB</td>
</tr>
<tr>
<td>Crosstalk</td>
<td>Channel Separation</td>
<td>$R_L = 32\Omega$</td>
<td>75</td>
<td>dB</td>
</tr>
<tr>
<td>$THD+N$</td>
<td>Total Harmonic Distortion + Noise</td>
<td>$f = 1\text{kHz}$</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 16\Omega$</td>
<td>0.05</td>
<td>%</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_O = 3.5V_{pp}$ (at 0 dB)</td>
<td>66</td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 32\Omega$</td>
<td>0.05</td>
<td>%</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_O = 3.5V_{pp}$ (at 0 dB)</td>
<td>66</td>
<td>dB</td>
</tr>
<tr>
<td>$SNR$</td>
<td>Signal-to-Noise Ratio</td>
<td>$V_O = 3.5V_{pp}$ (at 0 dB)</td>
<td>105</td>
<td>dB</td>
</tr>
<tr>
<td>$f_G$</td>
<td>Unity Gain Frequency</td>
<td>Open Loop, $R_L = 5k\Omega$</td>
<td>5.5</td>
<td>MHz</td>
</tr>
<tr>
<td>$P_o$</td>
<td>Output Power</td>
<td>$THD+N = 0.1%$, $f = 1\text{kHz}$</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 16\Omega$</td>
<td>105</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 32\Omega$</td>
<td>70</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td>THD+N = 10%, $f = 1\text{kHz}$</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 16\Omega$</td>
<td>150</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 32\Omega$</td>
<td>90</td>
<td>mW</td>
</tr>
<tr>
<td>$C_I$</td>
<td>Input Capacitance</td>
<td></td>
<td>3</td>
<td>pF</td>
</tr>
<tr>
<td>$C_L$</td>
<td>Load Capacitance</td>
<td></td>
<td>200</td>
<td>pF</td>
</tr>
<tr>
<td>SR</td>
<td>Slew Rate</td>
<td>Unity Gain Inverting</td>
<td>3</td>
<td>V/µs</td>
</tr>
</tbody>
</table>

### ELECTRICAL CHARACTERISTICS (1) (2)

The following specifications apply for $V_{DD} = 3.3V$ unless otherwise specified, limits apply to $T_A = 25^\circ C$.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Conditions</th>
<th>Units (Limits)</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{DD}$</td>
<td>Supply Current</td>
<td>$V_{IN} = 0V$, $I_O = 0A$</td>
<td>1.0</td>
<td>mA (max)</td>
</tr>
<tr>
<td>$V_{OS}$</td>
<td>Input Offset Voltage</td>
<td>$V_{IN} = 0V$</td>
<td>7</td>
<td>mV (max)</td>
</tr>
</tbody>
</table>

(1) All voltages are measured with respect to the ground pin, unless otherwise specified.

(2) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.

(3) Typicals are measured at 25°C and represent the parametric norm.

(4) Tested limits are specified to AOQL (Average Outgoing Quality Level). Datasheet min/max specification limits are specified by design, test, or statistical analysis.
ELECTRICAL CHARACTERISTICS (1) (2) (continued)
The following specifications apply for $V_{DD} = 3.3V$ unless otherwise specified, limits apply to $T_A = 25^\circ C$.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Conditions</th>
<th>Units (Limits)</th>
</tr>
</thead>
<tbody>
<tr>
<td>$P_o$</td>
<td>Output Power</td>
<td>THD+N = 0.1%, $f = 1$ kHz</td>
<td>$R_L = 16\Omega$</td>
<td>40 mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>$R_L = 32\Omega$</td>
<td>28 mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td>THD+N = 10%, $f = 1$ kHz</td>
<td>$R_L = 16\Omega$</td>
<td>56 mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>$R_L = 32\Omega$</td>
<td>38 mW</td>
</tr>
</tbody>
</table>

ELECTRICAL CHARACTERISTICS (1) (2)
The following specifications apply for $V_{DD} = 2.6V$ unless otherwise specified, limits apply to $T_A = 25^\circ C$.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Conditions</th>
<th>Units (Limits)</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{DD}$</td>
<td>Supply Current</td>
<td>$V_{IN} = 0V$, $I_O = 0A$</td>
<td>0.9 mA (max)</td>
<td></td>
</tr>
<tr>
<td>$V_{OS}$</td>
<td>Input Offset Voltage</td>
<td>$V_{IN} = 0V$</td>
<td>5 mV (max)</td>
<td></td>
</tr>
<tr>
<td>$P_o$</td>
<td>Output Power</td>
<td>THD+N = 0.1%, $f = 1$ kHz</td>
<td>$R_L = 16\Omega$</td>
<td>20 mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>$R_L = 32\Omega$</td>
<td>16 mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td>THD+N = 10%, $f = 1$ kHz</td>
<td>$R_L = 16\Omega$</td>
<td>31 mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>$R_L = 32\Omega$</td>
<td>22 mW</td>
</tr>
</tbody>
</table>

(1) All voltages are measured with respect to the ground pin, unless otherwise specified.
(2) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.
(3) Typicals are measured at $25^\circ C$ and represent the parametric norm.
(4) Tested limits are specified to AOQL (Average Outgoing Quality Level). Datasheet min/max specification limits are specified by design, test, or statistical analysis.

EXTERNAL COMPONENTS DESCRIPTION

(Figure 1)

<table>
<thead>
<tr>
<th>Components</th>
<th>Functional Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. $R_i$</td>
<td>The inverting input resistance, along with $R_i$, set the closed-loop gain. $R_i$ along with $C_i$ form a high pass filter with $f_c = 1/(2\pi R_i C_i)$.</td>
</tr>
<tr>
<td>2. $C_i$</td>
<td>The input coupling capacitor blocks DC voltage at the amplifier's input terminals. $C_i$ along with $R_i$ create a highpass filter with $f_c = 1/(2\pi R_i C_i)$. Refer to the section, SELECTING PROPER EXTERNAL COMPONENTS, for an explanation of determining the value of $C_i$.</td>
</tr>
<tr>
<td>3. $R_f$</td>
<td>The feedback resistance, along with $R_i$, set closed-loop gain.</td>
</tr>
<tr>
<td>4. $C_S$</td>
<td>This is the supply bypass capacitor. It provides power supply filtering. Refer to the APPLICATION INFORMATION section for proper placement and selection of the supply bypass capacitor.</td>
</tr>
<tr>
<td>5. $C_B$</td>
<td>This is the half-supply bypass pin capacitor. It provides half-supply filtering. Refer to the section, SELECTING PROPER EXTERNAL COMPONENTS, for information concerning proper placement and selection of $C_B$.</td>
</tr>
<tr>
<td>6. $C_O$</td>
<td>This is the output coupling capacitor. It blocks the DC voltage at the amplifier's output and forms a high pass filter with $R_L$ at $f_O = 1/(2\pi R_O C_O)$.</td>
</tr>
<tr>
<td>7. $R_B$</td>
<td>This is the resistor which forms a voltage divider that provides 1/2 $V_{DD}$ to the non-inverting input of the amplifier.</td>
</tr>
</tbody>
</table>
**TYPICAL PERFORMANCE CHARACTERISTICS**

**THD+N vs Frequency**

- **Figure 4.**
  - $V_{DD} = 2.6V$
  - $P_o = 15\, mW$
  - $R_L = 8\, \Omega$
  - NO FILTERS

- **Figure 6.**
  - $V_{DD} = 2.6V$
  - $P_o = 15\, mW$
  - $R_L = 32\, \Omega$
  - NO FILTERS

- **Figure 8.**
  - $V_{DD} = 3.3V$
  - $P_o = 25\, mW$
  - $R_L = 16\, \Omega$
  - NO FILTERS

- **Figure 5.**
  - $V_{DD} = 2.6V$
  - $P_o = 15\, mW$
  - $R_L = 16\, \Omega$
  - NO FILTERS

- **Figure 7.**
  - $V_{DD} = 3.3V$
  - $P_o = 25\, mW$
  - $R_L = 8\, \Omega$
  - NO FILTERS

- **Figure 9.**
  - $V_{DD} = 3.3V$
  - $P_o = 25\, mW$
  - $R_L = 32\, \Omega$
  - NO FILTERS
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**THD+N vs Frequency**

- **Figure 10.**
  
  - $V_{DD} = 5V$
  - $P_o = 50 \text{ mW}$
  - $R_L = 8\Omega$
  - NO FILTERS

- **Figure 11.**
  
  - $V_{DD} = 5V$
  - $P_o = 50 \text{ mW}$
  - $R_L = 16\Omega$
  - NO FILTERS

**THD+N vs Frequency**

- **Figure 12.**
  
  - $V_{DD} = 5V$
  - $P_o = 50 \text{ mW}$
  - $R_L = 32\Omega$
  - NO FILTERS

- **Figure 13.**
  
  - $V_{DD} = 5V$
  - $P_o = 50 \text{ mW}$
  - $R_L = 5\Omega$
  - NO FILTERS

**THD+N vs Output Power**

- **Figure 14.**
  
  - $V_{DD} = 2.6V$
  - $R_L = 8\Omega$
  - $f = 1 \text{ kHz}$
  - $A_V = -1$
  - $BW < 80 \text{ kHz}$

- **Figure 15.**
  
  - $V_{DD} = 2.6V$
  - $R_L = 16\Omega$
  - $f = 1 \text{ kHz}$
  - $A_V = -1$
  - $BW < 80 \text{ kHz}$
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

THD+N vs Output Power

**Figure 16.**

![Graph showing THD+N vs Output Power with specifications: V\(_\text{DD} = 2.6V, R_L = 32\Omega, f = 1\text{ kHz}, A_V = -1, BW < 80\text{ kHz}.**

**Figure 17.**

![Graph showing THD+N vs Output Power with specifications: V\(_\text{DD} = 3.3V, R_L = 8\Omega, f = 1\text{ kHz}, A_V = -1, BW < 80\text{ kHz}.**

**Figure 18.**

![Graph showing THD+N vs Output Power with specifications: V\(_\text{DD} = 3.3V, R_L = 16\Omega, f = 1\text{ kHz}, A_V = -1, BW < 80\text{ kHz}.**

**Figure 19.**

![Graph showing THD+N vs Output Power with specifications: V\(_\text{DD} = 5V, R_L = 8\Omega, f = 1\text{ kHz}, A_V = -1, BW < 80\text{ kHz}.**

**Figure 20.**

![Graph showing THD+N vs Output Power with specifications: V\(_\text{DD} = 5V, R_L = 16\Omega, f = 1\text{ kHz}, A_V = -1, BW < 80\text{ kHz}.**

**Figure 21.**
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**THD+N vs Output Power**

- $V_{DD} = 5V$
- $R_L = 32\Omega$
- $f = 1\ kHz$
- $A_V = -1$
- $BW < 80\ kHz$

**Output Power vs Load Resistance**

- $V_{DD} = 2.6V$
- $f = 1\ kHz$
- $A_V = -1$
- $BW < 80\ kHz$

**Output Power vs Supply Voltage**

- $f = 1\ kHz$
- $R_L = 8\Omega$
- $A_V = -1$
- $BW < 80\ kHz$

**Output Power vs Power Supply**

- $f = 1\ kHz$
- $R_L = 16\Omega$
- $A_V = -1$
- $BW < 80\ kHz$
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

### Output Power vs Power Supply

- **Figure 28.**

### Clipping Voltage vs Supply Voltage

- **Figure 29.**

### Power Dissipation vs Output Power

- **Figure 30.**

### Channel Separation

- **Figure 33.**

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TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**Figure 28.**

**Output Power vs Power Supply**

- $f = 1 \text{ kHz}$
- $R_L = 32 \Omega$
- $V_{DD} = 2.6V$
- $BW < 80 \text{ kHz}$
- $A_V = 1$
- $0.1\% \text{ THD+N}$
- $1\% \text{ THD+N}$
- $10\% \text{ THD+N}$

**Figure 29.**

**Clipping Voltage vs Supply Voltage**

- $8\Omega$, TOP
- $32\Omega$, TOP
- $8\Omega$, BOTTOM
- $32\Omega$, BOTTOM

**Figure 30.**

**Power Dissipation vs Output Power**

- $V_{DD} = 2.6V$
- $BW < 80 \text{ kHz}$
- $R_L = 8\Omega$
- $R_L = 16\Omega$
- $R_L = 32\Omega$

**Figure 31.**

**Power Dissipation vs Output Power**

- $V_{DD} = 3.3V$
- $BW < 80 \text{ kHz}$
- $R_L = 8\Omega$
- $R_L = 16\Omega$
- $R_L = 32\Omega$

**Figure 32.**

**Power Dissipation vs Output Power**

- $V_{DD} = 5V$
- $BW < 80 \text{ kHz}$
- $R_L = 8\Omega$
- $R_L = 16\Omega$
- $R_L = 32\Omega$

**Figure 33.**

**Channel Separation**

- $V_{DD} = 5V$
- $R_L = 8\Omega$
- $P_O = 130 \text{ mW}$
- $A_V = -1$
- $C_B = 1.0 \mu F$
- OUTPUT LEVEL (dB)
- FREQUENCY (Hz)
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Channel Separation

![Channel Separation Graph](image)

Noise Floor

![Noise Floor Graph](image)

Power Supply Rejection Ratio

![Power Supply Rejection Ratio Graph](image)

Frequency Response

![Frequency Response Graph](image)

Open Loop Frequency Response

![Open Loop Frequency Response Graph](image)
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Supply Current vs Supply Voltage

![Supply Current vs Supply Voltage Graph]

Figure 40.

Frequency Response vs Output Capacitor Size

![Frequency Response vs Output Capacitor Size Graph]

Figure 41.

Frequency Response vs Output Capacitor Size

![Frequency Response vs Output Capacitor Size Graph]

Figure 42.

Typical Application Frequency Response

![Typical Application Frequency Response Graph]

Figure 43.

Typical Application Frequency Response

![Typical Application Frequency Response Graph]

Figure 44.
APPLICATION INFORMATION

EXPOSED-DAP PACKAGE PCB MOUNTING CONSIDERATION

The LM4808's exposed-dap (die attach paddle) package (LD) provides a low thermal resistance between the die and the PCB to which the part is mounted and soldered. This allows rapid heat transfer from the die to the surrounding PCB copper traces, ground plane, and surrounding air.

The LD package should have its DAP soldered to a copper pad on the PCB. The DAP's PCB copper pad may be connected to a large plane of continuous unbroken copper. This plane forms a thermal mass, heat sink, and radiation area.

However, since the LM4808 is designed for headphone applications, connecting a copper plane to the DAP's PCB copper pad is not required. The LM4808's Power Dissipation vs Output Power Curve in the TYPICAL PERFORMANCE CHARACTERISTICS shows that the maximum power dissipated is just 45mW per amplifier with a 5V power supply and a 32Ω load.

Further detailed and specific information concerning PCB layout, fabrication, and mounting an LD (WSON) package is available from Texas Instruments' Package Engineering Group under application note AN-1187 (literature number SNOA401).

POWER DISSIPATION

Power dissipation is a major concern when using any power amplifier and must be thoroughly understood to ensure a successful design. Equation 1 states the maximum power dissipation point for a single-ended amplifier operating at a given supply voltage and driving a specified output load.

\[
P_{\text{D MAX}} = \frac{(V_{\text{DD}})^2}{2(2\pi^2 R_L)} (1)
\]

Since the LM4808 has two operational amplifiers in one package, the maximum internal power dissipation point is twice that of the number which results from Equation 1. Even with the large internal power dissipation, the LM4808 does not require heat sinking over a large range of ambient temperature. From Equation 1, assuming a 5V power supply and a 32Ω load, the maximum power dissipation point is 40mW per amplifier. Thus the maximum package dissipation point is 80mW. The maximum power dissipation point obtained must not be greater than the power dissipation that results from Equation 2:

\[
P_{\text{D MAX}} = \frac{(T_{\text{J MAX}} - T_A)}{\theta_{JA}}
\]

where
- \(\theta_{JA} = 210^\circ C/W\) for package DGK0008A
- \(T_{\text{J MAX}} = 150^\circ C\) for the LM4808

(2)

Depending on the ambient temperature, \(T_A\), of the system surroundings, Equation 2 can be used to find the maximum internal power dissipation supported by the IC packaging. If the result of Equation 1 is greater than that of Equation 2, then either the supply voltage must be decreased, the load impedance increased or \(T_A\) reduced. For the typical application of a 5V power supply, with a 32Ω load, the maximum ambient temperature possible without violating the maximum junction temperature is approximately 133.2°C provided that device operation is around the maximum power dissipation point. Power dissipation is a function of output power and thus, if typical operation is not around the maximum power dissipation point, the ambient temperature may be increased accordingly. Refer to the TYPICAL PERFORMANCE CHARACTERISTICS curves for power dissipation information for lower output powers.

POWER SUPPLY BYPASSING

As with any power amplifier, proper supply bypassing is critical for low noise performance and high power supply rejection. Applications that employ a 5V regulator typically use a 10µF in parallel with a 0.1µF filter capacitors to stabilize the regulator's output, reduce noise on the supply line, and improve the supply's transient response. However, their presence does not eliminate the need for a local 0.1µF supply bypass capacitor, \(C_S\), connected between the LM4808's supply pins and ground. Keep the length of leads and traces that connect capacitors between the LM4808's power supply pin and ground as short as possible. Connecting a 1.0µF capacitor, \(C_B\), between the IN A(+) / IN B(+) node and ground improves the internal bias voltage's stability and improves the amplifier's PSRR. The PSRR improvements increase as the bypass pin capacitor value increases. Too large, however, increases the amplifier's turn-on time. The selection of bypass capacitor values, especially \(C_B\), depends on desired PSRR requirements, click and pop performance (as explained in the section, SELECTING PROPER EXTERNAL COMPONENTS), system cost, and size constraints.
SELECTING PROPER EXTERNAL COMPONENTS

Optimizing the LM4808's performance requires properly selecting external components. Though the LM4808 operates well when using external components with wide tolerances, best performance is achieved by optimizing component values.

The LM4808 is unity-gain stable, giving a designer maximum design flexibility. The gain should be set to no more than a given application requires. This allows the amplifier to achieve minimum THD+N and maximum signal-to-noise ratio. These parameters are compromised as the closed-loop gain increases. However, low gain demands input signals with greater voltage swings to achieve maximum output power. Fortunately, many signal sources such as audio CODECs have outputs of $1V_{RMS}$ ($2.83V_{P-P}$). Please refer to the AUDIO POWER AMPLIFIER DESIGN section for more information on selecting the proper gain.

Input and Output Capacitor Value Selection

Amplifying the lowest audio frequencies requires high value input and output coupling capacitors ($C_I$ and $C_O$ in Figure 1). A high value capacitor can be expensive and may compromise space efficiency in portable designs. In many cases, however, the speakers used in portable systems, whether internal or external, have little ability to reproduce signals below 150Hz. Applications using speakers with this limited frequency response reap little improvement by using high value input and output capacitors.

Besides affecting system cost and size, $C_I$ has an effect on the LM4808's click and pop performance. The magnitude of the pop is directly proportional to the input capacitor's size. Thus, pops can be minimized by selecting an input capacitor value that is no higher than necessary to meet the desired $-3dB$ frequency.

As shown in Figure 1, the input resistor, $R_I$, and the input capacitor, $C_I$, produce a $-3dB$ high pass filter cutoff frequency that is found using Equation 3. In addition, the output load $R_L$, and the output capacitor $C_O$, produce a $-3dB$ high pass filter cutoff frequency defined by Equation 4.

$$f_{I-3db} = \frac{1}{2\pi R_I C_I}$$  \hspace{1cm} (3)

$$f_{O-3db} = \frac{1}{2\pi R_L C_O}$$  \hspace{1cm} (4)

Also, careful consideration must be taken in selecting a certain type of capacitor to be used in the system. Different types of capacitors (tantalum, electrolytic, ceramic) have unique performance characteristics and may affect overall system performance.

Bypass Capacitor Value

Besides minimizing the input capacitor size, careful consideration should be paid to the value of the bypass capacitor, $C_B$. Since $C_B$ determines how fast the LM4808 settles to quiescent operation, its value is critical when minimizing turn-on pops. The slower the LM4808's outputs ramp to their quiescent DC voltage (nominally $1/2 V_{DD}$), the smaller the turn-on pop. Choosing $C_B$ equal to 1.0µF or larger, will minimize turn-on pops. As discussed above, choosing $C_I$ no larger than necessary for the desired bandwidth helps minimize clicks and pops.
AUDIO POWER AMPLIFIER DESIGN

Design a Dual 70mW/32Ω Audio Amplifier

Given:

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Output</td>
<td>70mW</td>
</tr>
<tr>
<td>Load Impedance</td>
<td>32Ω</td>
</tr>
<tr>
<td>Input Level</td>
<td>1Vrms (max)</td>
</tr>
<tr>
<td>Input Impedance</td>
<td>20kΩ</td>
</tr>
<tr>
<td>Bandwidth</td>
<td>100Hz–20kHz ± 0.50dB</td>
</tr>
</tbody>
</table>

The design begins by specifying the minimum supply voltage necessary to obtain the specified output power. One way to find the minimum supply voltage is to use the Output Power vs Supply Voltage curve in the TYPICAL PERFORMANCE CHARACTERISTICS section. Another way, using Equation 5, is to calculate the peak output voltage necessary to achieve the desired output power for a given load impedance. To account for the amplifier's dropout voltage, two additional voltages, based on the Dropout Voltage vs Supply Voltage in the TYPICAL PERFORMANCE CHARACTERISTICS curves, must be added to the result obtained by Equation 5. For a single-ended application, the result is Equation 6.

\[
V_{\text{peak}} = \sqrt{2P_{\text{out}}} \\
V_{\text{DD}} \geq (2V_{\text{peak}} + (V_{\text{ODtop}} + V_{\text{ODbot}}))
\]

The Output Power vs Supply Voltage graph for a 32Ω load indicates a minimum supply voltage of 4.8V. This is easily met by the commonly used 5V supply voltage. The additional voltage creates the benefit of headroom, allowing the LM4808 to produce peak output power in excess of 70mW without clipping or other audible distortion. The choice of supply voltage must also not create a situation that violates maximum power dissipation as explained above in the POWER DISSIPATION section. Remember that the maximum power dissipation point from Equation 1 must be multiplied by two since there are two independent amplifiers inside the package. Once the power dissipation equations have been addressed, the required gain can be determined from Equation 7.

\[
A_V \geq \frac{V_{\text{out}}}{V_{\text{in}}} = \frac{V_{\text{rms}}}{V_{\text{rms}}}
\]

Thus, a minimum gain of 1.497 allows the LM4808 to reach full output swing and maintain low noise and THD+N performance. For this example, let \(A_V = 1.5\).

The amplifiers overall gain is set using the input (\(R_i\)) and feedback (\(R_f\)) resistors. With the desired input impedance set at 20kΩ, the feedback resistor is found using Equation 8.

\[
A_V = \frac{R_f}{R_i}
\]

The value of \(R_i\) is 30kΩ.

The last step in this design is setting the amplifier's –3db frequency bandwidth. To achieve the desired ±0.25dB pass band magnitude variation limit, the low frequency response must extend to at least one–fifth the lower bandwidth limit and the high frequency response must extend to at least five times the upper bandwidth limit. The gain variation for both response limits is 0.17dB, well within the ±0.25dB desired limit. The results are an

\[
f_l = 100\text{Hz}/5 = 20\text{Hz}
\]

and a

\[
f_H = 20\text{kHz} \times 5 = 100\text{kHz}
\]

As stated in the EXTERNAL COMPONENTS DESCRIPTION section, both \(R_i\) in conjunction with \(C_i\) and \(C_o\) with \(R_o\) create first order highpass filters. Thus to obtain the desired low frequency response of 100Hz within ±0.5dB, both poles must be taken into consideration. The combination of two single order filters at the same frequency forms a second order response. This results in a signal which is down 0.34dB at five times away from the single order filter –3dB point. Thus, a frequency of 20Hz is used in the following equations to ensure that the response is better than 0.5dB down at 100Hz.

\[
C_i \geq \frac{1}{(2\pi \times 20 \text{ kΩ} \times 20 \text{ Hz})} = 0.397\mu F; \text{ use } 0.39\mu F.
\]

\[
C_o \geq \frac{1}{(2\pi \times 32\Omega \times 20 \text{ Hz})} = 249\mu F; \text{ use } 330\mu F.
\]
The high frequency pole is determined by the product of the desired high frequency pole, \( f_H \), and the closed-loop gain, \( A_V \). With a closed-loop gain of 1.5 and \( f_H = 100\text{kHz} \), the resulting GBWP = 150kHz which is much smaller than the LM4808's GBWP of 900kHz. This figure displays that if a designer has a need to design an amplifier with a higher gain, the LM4808 can still be used without running into bandwidth limitations.

**Demonstration Board Layout**

Figure 46. Recommended SO PC Board Layout: Top Silkscreen

![Recommended SO PC Board Layout](image1)

Figure 47. Recommended LD PC Board Layout: Top Silkscreen

![Recommended LD PC Board Layout](image2)

Figure 48. Recommended SOIC PC Board Layout: Top Layer

![Recommended SOIC PC Board Layout](image3)

Figure 49. Recommended LD PC Board Layout: Top Layer

![Recommended LD PC Board Layout](image4)

Figure 50. Recommended SOIC PC Board Layout: Bottom Layer

![Recommended SOIC PC Board Layout](image5)

Figure 51. Recommended LD PC Board Layout: Bottom Layer

![Recommended LD PC Board Layout](image6)
## REVISION HISTORY

<table>
<thead>
<tr>
<th>Changes from Revision C (May 2013) to Revision D</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>• Changed layout of National Data Sheet to TI format</td>
<td>16</td>
</tr>
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## PACKAGING INFORMATION

<table>
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<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
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<td>-40 to 85</td>
<td>LM48 08M</td>
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<td>LM48 08M</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBsolete**: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check [http://www.ti.com/productcontent](http://www.ti.com/productcontent) for the latest availability information and additional product content details.

- **Pb-Free (RoHS)**: TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
- **Pb-Free (RoHS Exempt)**: This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
- **Green (RoHS & no Sb/Br)**: TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.
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**TAPE AND REEL INFORMATION**

*All dimensions are nominal*

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### TAPE AND REEL BOX DIMENSIONS

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NOTES:

1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 [0.15] per side.
4. This dimension does not include interlead flash.
5. Reference JEDEC registration MS-012, variation AA.
NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
EXAMPLE STENCIL DESIGN

NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

9. Board assembly site may have different recommendations for stencil design.

SOLDER PASTE EXAMPLE
BASED ON .005 INCH [0.125 MM] THICK STENCIL
SCALE: 8X
DGK (S-PDSO-G8)  PLASTIC SMALL-OUTLINE PACKAGE

NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 per end.
D. Body width does not include interlead flash. Interlead flash shall not exceed 0.50 per side.
E. Falls within JEDEC MO-187 variation AA, except interlead flash.

4073329/E 05/06

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NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Publication IPC-7351 is recommended for alternate designs.
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.
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