1 Features
• Provides Circuit Protection and Current Limiting
• Single Supply Operation
• –2-V to 76-V Common-Mode Voltage Range
• Variable Gain Set by External Resistor
• Adjustable Bandwidth Set by External Capacitor
• Buffered Output
• 3% Output Accuracy Achievable at $V_{\text{SENSE}} = 100$ mV
• Key Specifications:
  – Supply Voltage Range 2.7 V to 12 V
  – Output Current (Source) 0 to 5 mA
  – Gain Accuracy 2.0% (max)
  – Transconductance 200 $\mu$A/V
  – Offset ±1 mV (Maximum)
  – Quiescent Current 380 $\mu$A
  – Input Bias 12 $\mu$A (Typical)
  – PSRR 85 dB
  – CMRR 95 dB
  – Temperature Range –40°C to 125°C
  – 6-Pin SOT Package

2 Applications
• High-Side and Low-Side Current Limit
• Circuit Fault Protection
• Battery and Supercap Charging
• LED Constant Current Drive
• Power Management

3 Description
The LMP8646 is a precision current limiter used to improve the current limit accuracy of any switching or linear regulator with an available feedback node.

The LMP8646 accepts input signals with a common-mode voltage ranging from –2 V to 76 V. It has a variable gain which is used to adjust the sense current. The gain is configured with a single external resistor, $R_G$, providing a high level of flexibility and accuracy up to 2%. The adjustable bandwidth, which allows the device to be used with a variety of applications, is configurable with a single external capacitor in parallel with $R_G$. In addition, the output is buffered in order to provide a low output impedance.

The LMP8646 is an ideal choice for industrial, automotive, telecommunications, and consumer applications where circuit protection and improved precision systems are required. The LMP8646 is available in a 6-pin SOT package and can operate at temperature range of –40°C to 125°C.

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMP8646</td>
<td>SOT (6)</td>
<td>2.90 mm × 1.60 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum at the end of the datasheet.

Typical Application
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4 Revision History
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision A (March 2013) to Revision B Page

• Added Pin Configuration and Functions section, Handling Rating table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section ................................................................. 1

Changes from Original (March 2013) to Revision A Page

• Changed layout of National Semiconductor Data Sheet to TI format .................................................. 22
5 Pin Configuration and Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>NO.</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>VOUT</td>
<td>1</td>
<td>Single-Ended Output Voltage</td>
</tr>
<tr>
<td>V</td>
<td>2</td>
<td>Negative Supply Voltage. This pin should be connected to ground.</td>
</tr>
<tr>
<td>+IN</td>
<td>3</td>
<td>Positive Input</td>
</tr>
<tr>
<td>-IN</td>
<td>4</td>
<td>Negative Input</td>
</tr>
<tr>
<td>RG</td>
<td>5</td>
<td>External Gain Resistor. An external capacitance (C_G) may be added in parallel with RG to limit the bandwidth.</td>
</tr>
<tr>
<td>V+</td>
<td>6</td>
<td>Positive Supply Voltage</td>
</tr>
</tbody>
</table>
6 Specifications

6.1 Absolute Maximum Ratings
over operating free-air temperature range (unless otherwise noted)\(^{(1)}\)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage ( (V_S = V^+ - V^-) )</td>
<td>13.2 V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Differential voltage ( +IN - (-IN) )</td>
<td>6 V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Voltage at pins ( +IN, -IN )</td>
<td>-6 V</td>
<td>80 V</td>
<td></td>
</tr>
<tr>
<td>Voltage at ( R_G ) pin</td>
<td>13.2 V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Voltage at OUT pin</td>
<td>( V^- )</td>
<td>( V^+ )</td>
<td>V</td>
</tr>
<tr>
<td>Junction Temperature(^{(2)})</td>
<td>150 ( ^\circ )C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Storage temperature range</td>
<td>-65 ( ^\circ )C</td>
<td>150 ( ^\circ )C</td>
<td></td>
</tr>
</tbody>
</table>

For soldering specifications see SNOA549

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Recommended Operating Conditions indicate conditions for which the device is intended to be functional, but specific performance is not ensured. For ensured specifications and the test conditions, see the Electrical Characteristics: 2.7 V tables.

(2) The maximum power dissipation must be derated at elevated temperatures and is dictated by \( T_{J(MAX)} \), \( \theta_{JA} \), and the ambient temperature, \( T_A \). The maximum allowable power dissipation \( P_{DMAX} = (T_{J(MAX)} - T_A) / \theta_{JA} \) or the number given in Absolute Maximum Ratings, whichever is lower.

6.2 ESD Ratings

<table>
<thead>
<tr>
<th>Electrostatic discharge</th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001(^{(1)})</td>
<td>( \pm4000 ) ( V )</td>
<td></td>
</tr>
<tr>
<td>Pins +IN and -IN</td>
<td></td>
<td></td>
</tr>
<tr>
<td>All pins except +IN and -IN</td>
<td>( \pm2000 )</td>
<td></td>
</tr>
<tr>
<td>Charged-device model (CDM), per JEDEC specification JESD22-C101(^{(2)})</td>
<td>( \pm1250 )</td>
<td></td>
</tr>
<tr>
<td>Machine model</td>
<td>( \pm250 )</td>
<td></td>
</tr>
</tbody>
</table>

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage ( (V_S = V^+ - V^-) )</td>
<td>2.7 V</td>
<td>12 V</td>
<td></td>
</tr>
<tr>
<td>Temperature Range(^{(1)})</td>
<td>-40 V</td>
<td>125 V</td>
<td></td>
</tr>
</tbody>
</table>

(1) The maximum power dissipation must be derated at elevated temperatures and is dictated by \( T_{J(MAX)} \), \( \theta_{JA} \), and the ambient temperature, \( T_A \). The maximum allowable power dissipation \( P_{DMAX} = (T_{J(MAX)} - T_A) / \theta_{JA} \) or the number given in Absolute Maximum Ratings, whichever is lower.

6.4 Thermal Information

<table>
<thead>
<tr>
<th>Thermal Metric (^{(1)})</th>
<th>LMP8646</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( R_{JA} ) Junction-to-ambient thermal resistance</td>
<td>96 ( ^\circ )C/W</td>
<td></td>
</tr>
</tbody>
</table>

(1) For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report, SPRA953.
# 6.5 Electrical Characteristics: 2.7 V

Unless otherwise specified, all limits ensured for at $T_A = 25°C$, $V_S = (V^+ - V) = (2.7 V - 0 V) = 2.7 V$, $-2 V < V_{CM} < 76 V$, $R_G = 25 kΩ$, $R_L = 10 kΩ$.\(^{(1)}\)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN(^{(2)})</th>
<th>TYP(^{(3)})</th>
<th>MAX(^{(2)})</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{OFFSET}$ Input Offset Voltage</td>
<td>$V_{CM} = 2.1 V$</td>
<td>$-1$</td>
<td>$1$</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1 V$, $-40°C \leq T_J \leq 125°C$</td>
<td>$-1.7$</td>
<td>$1.7$</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$TCV_{OS}$ Input Offset Voltage Drift(^{(4)})((^{(5)}))</td>
<td>$V_{CM} = 2.1 V$</td>
<td></td>
<td></td>
<td>$7$</td>
<td>$μV/°C$</td>
</tr>
<tr>
<td>$I_{B}$ Input Bias Current(^{(6)})</td>
<td>$V_{CM} = 2.1 V$</td>
<td>$12$</td>
<td>$20$</td>
<td></td>
<td>$μA$</td>
</tr>
<tr>
<td>$\epsilon_n$ Input Voltage Noise(^{(5)})</td>
<td>$f &gt; 10 kHz$, $R_G = 5 kΩ$</td>
<td>$120$</td>
<td></td>
<td></td>
<td>$nV/√Hz$</td>
</tr>
<tr>
<td>$V_{SENSE}$ Max Input Sense Voltage(^{(5)})</td>
<td>$V_{CM} = 12 V$, $R_G = 5 kΩ$</td>
<td></td>
<td></td>
<td>$600$</td>
<td>mV</td>
</tr>
<tr>
<td>Gain $A_V$ Adjustable Gain Setting(^{(5)})</td>
<td>$V_{CM} = 12 V$</td>
<td>$1$</td>
<td>$100$</td>
<td></td>
<td>V/V</td>
</tr>
<tr>
<td>$Gm$ Transconductance $= 1/R_{IN}$</td>
<td>$V_{CM} = 2.1 V$</td>
<td>$200$</td>
<td></td>
<td></td>
<td>$μA/V$</td>
</tr>
<tr>
<td></td>
<td>Accuracy</td>
<td>$V_{CM} = 2.1 V$, $-40°C \leq T_J \leq 125°C$</td>
<td>$-2%$</td>
<td>$2%$</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$V_{CM} = 2.1 V$, $-2 V &lt; V_{CM} &lt; 2.1 V$</td>
<td>$-3.4%$</td>
<td>$3.4%$</td>
<td></td>
</tr>
<tr>
<td>$Gm$ drift(^{(5)})</td>
<td>$-40°C$ to $125°C$, $V_{CM} = 2.1 V$</td>
<td></td>
<td></td>
<td>$140$</td>
<td>ppm $/°C$</td>
</tr>
<tr>
<td>PSRR Power Supply Rejection Ratio</td>
<td>$V_{CM} = 2.1 V$, $2.7 V &lt; V^+ &lt; 12 V$</td>
<td></td>
<td></td>
<td>$85$</td>
<td>dB</td>
</tr>
<tr>
<td>CMRR Common-Mode Rejection Ratio</td>
<td>$2.1 V &lt; V_{CM} &lt; 76 V$</td>
<td></td>
<td></td>
<td>$95$</td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td>$-2 V &lt; V_{CM} &lt; 2.1 V$</td>
<td></td>
<td></td>
<td>$55$</td>
<td></td>
</tr>
<tr>
<td>$SR$ Slew Rate(^{(7)})((^{(6)}))</td>
<td>$V_{CM} = 5 V$, $C_G = 4 pF$, $V_{SENSE}$ from $25$ mV to $175$ mV, $C_L = 30 pF$, $R_L = 1MΩ$</td>
<td></td>
<td></td>
<td>$0.5$</td>
<td>$V/μs$</td>
</tr>
<tr>
<td>$I_S$ Supply Current</td>
<td>$V_{CM} = 2.1 V$</td>
<td>$380$</td>
<td>$610$</td>
<td></td>
<td>$μA$</td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1 V$, $-40°C \leq T_J \leq 125°C$</td>
<td></td>
<td></td>
<td>$807$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = -2 V$</td>
<td></td>
<td></td>
<td>$2000$</td>
<td>$2500$</td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = -2 V$, $-40°C \leq T_J \leq 125°C$</td>
<td></td>
<td></td>
<td>$2700$</td>
<td></td>
</tr>
<tr>
<td>$V_{OUT}$ Maximum Output Voltage</td>
<td>$V_{CM} = 2.1 V$, $R_G = 500 kΩ$</td>
<td></td>
<td></td>
<td>$1.1$</td>
<td>$V$</td>
</tr>
<tr>
<td></td>
<td>Minimum Output Voltage</td>
<td>$V_{CM} = 2.1 V$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Maximum Output Voltage</td>
<td>$V_S = V_{CM} = 3.3 V$, $R_G = 500 kΩ$</td>
<td></td>
<td></td>
<td>$1.6$</td>
</tr>
<tr>
<td></td>
<td>Minimum Output Voltage</td>
<td>$V_S = V_{CM} = 3.3 V$, $R_G = 500 kΩ$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{OUT}$ Output current(^{(5)})</td>
<td>Sourcing, $V_{OUT} = 600$ mV, $R_G = 150 kΩ$</td>
<td></td>
<td></td>
<td>$5$</td>
<td>mA</td>
</tr>
<tr>
<td>$C_{LOAD}$ Max Output Capacitance Load(^{(5)})</td>
<td></td>
<td></td>
<td></td>
<td>$30$</td>
<td>pF</td>
</tr>
</tbody>
</table>

\(^{(1)}\) Electrical Table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device such that $T_J = T_A$. No assurance of parametric performance is indicated in the electrical tables under conditions of internal self-heating where $T_J > T_A$.

\(^{(2)}\) All limits are specified by testing, design, or statistical analysis.

\(^{(3)}\) Typical values represent the most likely parametric norm at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not specified on shipped production material.

\(^{(4)}\) Offset voltage temperature drift is determined by dividing the change in $V_{OS}$ at the temperature extremes by the total temperature change.

\(^{(5)}\) This parameter is specified by design and/or characterization and is not tested in production.

\(^{(6)}\) Positive Bias Current corresponds to current flowing into the device.

\(^{(7)}\) The number specified is the average of rising and falling slew rates and measured at 90% to 10%.
### 6.6 Electrical Characteristics: 5 V

Unless otherwise specified, all limits ensured for at $T_A = 25°C$, $V_S= V^+ - V^-$, $V^+ = 5 V$, $V^- = 0 V$, $-2 V < V_{CM} < 76 V$, $R_G= 25 kΩ$, $R_L= 10 kΩ$. (1)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN(2)</th>
<th>TYP(3)</th>
<th>MAX(2)</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{OFFSET}$</td>
<td>Input Offset Voltage</td>
<td>$V_{CM} = 2.1 V$</td>
<td>$-1$</td>
<td>$1$</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1 V$, $-40°C \leq T_J \leq 125°C$</td>
<td>$-1.7$</td>
<td>$1.7$</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$TCV_{OS}$</td>
<td>Input Offset Voltage Drift(4)(5)</td>
<td>$V_{CM} = 2.1 V$</td>
<td>7</td>
<td></td>
<td>μV/°C</td>
</tr>
<tr>
<td>$I_B$</td>
<td>Input Bias Current(6)</td>
<td>$V_{CM} = 2.1 V$</td>
<td>12.5</td>
<td>22</td>
<td>μA</td>
</tr>
<tr>
<td>$e_n$</td>
<td>Input Voltage Noise(5)</td>
<td>$f &gt; 10 kHz$, $R_G= 5 kΩ$</td>
<td>120</td>
<td></td>
<td>nV/√Hz</td>
</tr>
<tr>
<td>$V_{SENSE(MAX)}$</td>
<td>Max Input Sense Voltage(5)</td>
<td>$V_{CM} = 12 V$, $R_G = 5 kΩ$</td>
<td>600</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Gain $A_V$</td>
<td>Adjustable Gain Setting(5)</td>
<td>$V_{CM} = 12 V$</td>
<td>1</td>
<td>100</td>
<td>V/V</td>
</tr>
<tr>
<td>$Gm$</td>
<td>Transconductance = $1/R_{IN}$</td>
<td>$V_{CM} = 2.1 V$</td>
<td>200</td>
<td></td>
<td>μA/V</td>
</tr>
<tr>
<td>Accuracy</td>
<td>$V_{CM} = 2.1 V$</td>
<td>$-2%$</td>
<td>$2%$</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1 V$, $-40°C \leq T_J \leq 125°C$</td>
<td>$-3.4%$</td>
<td>$3.4%$</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$Gm$ drift(5)</td>
<td>$-40°C$ to $125°C$, $V_{CM} = 2.1 V$</td>
<td>140</td>
<td></td>
<td>ppm/°C</td>
<td></td>
</tr>
<tr>
<td>PSRR</td>
<td>Power Supply Rejection Ratio</td>
<td>$V_{CM} = 2.1 V$, $2.7 V &lt; V^+ &lt; 12 V$,</td>
<td>85</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>CMRR</td>
<td>Common-Mode Rejection Ratio</td>
<td>$V_{CM} &lt; 2 V$</td>
<td>95</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>SR</td>
<td>Slew Rate(7)(6)</td>
<td>$V_{CM} = 5 V$, $C_G = 4 $ pF, $V_{SENSE}$ from 100 mV to 500 mV, $C_L = 30 $ pF, $R_L= 1 MΩ$</td>
<td>0.5</td>
<td></td>
<td>V/μs</td>
</tr>
<tr>
<td>$I_S$</td>
<td>Supply Current</td>
<td>$V_{CM} = 2.1 V$</td>
<td>450</td>
<td>660</td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1 V$, $-40°C \leq T_J \leq 125°C$</td>
<td>939</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = -2 V$</td>
<td>2100</td>
<td>2800</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = -2 V$, $-40°C \leq T_J \leq 125°C$</td>
<td>3030</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{OUT}$</td>
<td>Maximum Output Voltage</td>
<td>$V_{CM} = 5 V$, $R_G= 500 kΩ$</td>
<td>3.3</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Minimum Output Voltage</td>
<td>$V_{CM} = 2.1 V$</td>
<td>22</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>$I_{OUT}$</td>
<td>Output current(5)</td>
<td>Sourcing, $V_{OUT}= 1.65 V$, $R_G = 150 kΩ$</td>
<td>5</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$C_{LOAD}$</td>
<td>Max Output Capacitance Load(5)</td>
<td>30</td>
<td></td>
<td></td>
<td>pF</td>
</tr>
</tbody>
</table>

(1) Electrical Table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device such that $T_J = T_A$. No assurance of parametric performance is indicated in the electrical tables under conditions of internal self-heating where $T_J > T_A$.

(2) All limits are specified by testing, design, or statistical analysis.

(3) Typical values represent the most likely parametric norm at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not specified on shipped production material.

(4) Offset voltage temperature drift is determined by dividing the change in $V_{OS}$ at the temperature extremes by the total temperature change.

(5) This parameter is specified by design and/or characterization and is not tested in production.

(6) Positive Bias Current corresponds to current flowing into the device.

(7) The number specified is the average of rising and falling slew rates and measured at 90% to 10%.
6.7 Electrical Characteristics: 12 V

Unless otherwise specified, all limits ensured for at $T_A = 25^\circ C$, $V_S= V^+ - V$, $V^- = 12$ V, $V^- = 0$ V, $-2$ $V < V_{CM} < 76$ V, $R_S= 25$ k$\Omega$, $R_L= 10$ k$\Omega$.\(^{(1)}\)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN(^{(2)})</th>
<th>TYP(^{(3)})</th>
<th>MAX(^{(2)})</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{OFFSET}$ Input Offset Voltage</td>
<td>$V_{CM} = 2.1$ V</td>
<td>$-1$</td>
<td>$1$</td>
<td>$1.7$</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1$ V, $-40^\circ C \leq T_J \leq 125^\circ C$</td>
<td>$-1.7$</td>
<td>$1.7$</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$TCV_{OS}$ Input Offset Voltage Drift(^{(4)})(^{(5)})</td>
<td>$V_{CM} = 2.1$ V</td>
<td>$7$</td>
<td></td>
<td></td>
<td>$\mu V/\circ C$</td>
</tr>
<tr>
<td>$I_B$ Input Bias Current(^{(6)})</td>
<td>$V_{CM} = 2.1$ V</td>
<td>$13$</td>
<td>$23$</td>
<td></td>
<td>$\mu A$</td>
</tr>
<tr>
<td>$e_{in}$ Input Voltage Noise(^{(5)})</td>
<td>$f &gt; 10$ kHz, $R_G = 5$ k$\Omega$</td>
<td>$120$</td>
<td></td>
<td></td>
<td>$nV/\sqrt{Hz}$</td>
</tr>
<tr>
<td>$V_{SENSE(MAX)}$ Max Input Sense Voltage(^{(5)})</td>
<td>$V_{CM} = 12$ V, $R_G = 5$ k$\Omega$</td>
<td>$600$</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Gain $A_V$ Adjustable Gain Setting(^{(5)})</td>
<td>$V_{CM} = 12$ V</td>
<td>$1$</td>
<td>$100$</td>
<td></td>
<td>V/V</td>
</tr>
<tr>
<td>$G_m$ Transconductance $= 1/R_{IN}$</td>
<td>$V_{CM} = 2.1$ V</td>
<td>$200$</td>
<td></td>
<td></td>
<td>$\mu A/V$</td>
</tr>
<tr>
<td>Accuracy</td>
<td>$V_{CM} = 2.1$ V</td>
<td>$-2%$</td>
<td>$2%$</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1$ V, $-40^\circ C \leq T_J \leq 125^\circ C$</td>
<td>$-3.4%$</td>
<td>$3.4%$</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$G_m$ drift(^{(5)})</td>
<td>$-40^\circ C$ to $125^\circ C$, $V_{CM} = 2.1$ V</td>
<td>$140$</td>
<td></td>
<td></td>
<td>ppm /$^\circ C$</td>
</tr>
<tr>
<td>PSRR</td>
<td>Power Supply Rejection Ratio</td>
<td>$2.1$ V, $2.7$ V $&lt; V^+ &lt; 12$ V</td>
<td>$85$</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>CMRR</td>
<td>Common-Mode Rejection Ratio</td>
<td>$2.1$ V $&lt; V_{CM} &lt; 76$ V</td>
<td>$95$</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td>$-2$ V $&lt; V_{CM} &lt; 2.1$ V</td>
<td>$55$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$S_R$ Slew Rate(^{(7)})(^{(6)})</td>
<td>$V_{CM} = 5$ V, $C_G = 4$ pF, $V_{SENSE}$ from 100 mV to 500 mV, $C_L = 30$ pF, $R_L = 1$ M$\Omega$</td>
<td>$0.6$</td>
<td></td>
<td></td>
<td>V/$\mu$ s</td>
</tr>
<tr>
<td>$I_S$ Supply Current</td>
<td>$V_{CM} = 2.1$ V</td>
<td>$555$</td>
<td>$845$</td>
<td></td>
<td>$\mu A$</td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = 2.1$ V, $-40^\circ C \leq T_J \leq 125^\circ C$</td>
<td>$1123$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>$V_{CM} = -2$ V</td>
<td>$2200$</td>
<td>$2900$</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>$CM = -2$ V, $-40^\circ C \leq T_J \leq 125^\circ C$</td>
<td>$3110$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{OUT}$ Maximum Output Voltage</td>
<td>$V_{CM} = 12$ V, $R_G = 500$ k$\Omega$,</td>
<td>$10$</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Minimum Output Voltage</td>
<td>$V_{CM} = 2.1$ V</td>
<td>$24$</td>
<td></td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>$I_{OUT}$ Output current(^{(5)})</td>
<td>Sourcing, $V_{OUT} = 5.25$ V, $R_G = 150$ k$\Omega$</td>
<td>$5$</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$C_{LOAD}$ Max Output Capacitance Load(^{(5)})</td>
<td>$30$</td>
<td></td>
<td></td>
<td></td>
<td>pF</td>
</tr>
</tbody>
</table>

\(^{(1)}\) Electrical Table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device such that $T_J = T_A$. No assurance of parametric performance is indicated in the electrical tables under conditions of internal self-heating where $T_J > T_A$.

\(^{(2)}\) All limits are specified by testing, design, or statistical analysis.

\(^{(3)}\) Typical values represent the most likely parametric norm at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration. The typical values are not tested and are not specified on shipped production material.

\(^{(4)}\) Offset voltage temperature drift is determined by dividing the change in $V_{OS}$ at the temperature extremes by the total temperature change.

\(^{(5)}\) This parameter is specified by design and/or characterization and is not tested in production.

\(^{(6)}\) Positive Bias Current corresponds to current flowing into the device.

\(^{(7)}\) The number specified is the average of rising and falling slew rates and measured at 90% to 10%.
### 6.8 Typical Characteristics

Unless otherwise specified: $T_A = 25^\circ C$, $V_S = V^+ - V^-$, $V_{SENSE} = +IN - (-IN)$, $R_L = 10 \, k\Omega$.

**Figure 1. Supply Current vs. Supply Voltage for $V_{CM} = 2 \, V$**

**Figure 2. Supply Current vs. $V_{CM}$**

**Figure 3. AC PSRR vs. Frequency**

**Figure 4. AC CMRR vs. Frequency**

**Figure 5. CMRR vs. High $V_{CM}$**

**Figure 6. Gain vs. Frequency (BW = 1kHz)**
Typical Characteristics (continued)

Unless otherwise specified: $T_A = 25^\circ C$, $V_S = V^+ - V^-$, $V_{\text{SENSE}} = +\text{IN} - (-\text{IN})$, $R_L = 10 \, k\Omega$.

Figure 7. Gain vs. Frequency (BW = 35 kHz)

Figure 8. Gain Accuracy vs. $V_{\text{CM}}$

Figure 9. Gain Accuracy vs. $V_{\text{CM}}$

Figure 10. $V_{\text{OUT}}$ vs. $V_{\text{SENSE}}$

Figure 11. $V_{\text{OUT\_MAX}}$ vs. Gain at $V_S = 2.7$ V

Figure 12. $V_{\text{OUT\_MAX}}$ vs. Gain at $V_S = 5.0$ V
Typical Characteristics (continued)

Unless otherwise specified: $T_A = 25^\circ C$, $V_S = V^+ - V^-$, $V_{\text{SENSE}} = +\text{IN} - (-\text{IN})$, $R_L = 10\, \text{k}\Omega$.

Figure 13. $V_{\text{OUT\_MAX}}$ vs. Gain at $V_S = 12\, \text{V}$

Figure 14. $V_{\text{OUT\_MAX}}$ vs. $V_S$ at $V_{\text{CM}} = -2\, \text{V}$

Figure 15. $V_{\text{OUT\_MAX}}$ vs. $V_S$ at $V_{\text{CM}} = 2.1\, \text{V}$

Figure 16. Large Step Response at $BW = 1\, \text{kHz}$

Figure 17. Large Step Response at $BW = 35\, \text{kHz}$

Figure 18. Small Step Response at $BW = 1\, \text{kHz}$
Typical Characteristics (continued)

Unless otherwise specified: $T_A = 25^\circ C$, $V_S = V^+ - V^-$, $V_{\text{SENSE}} = +\text{IN} - (-\text{IN})$, $R_L = 10 \, \text{k}\Omega$.

![Figure 19. Small Step Response at BW = 35 kHz](image1)

![Figure 20. Settling Time (Rise) at 1 kHz](image2)

![Figure 21. Settling Time (Fall) at 1 kHz](image3)

![Figure 22. Settling Time (Rise) at 35 kHz](image4)

![Figure 23. Settling Time (Fall) at 35 kHz](image5)

![Figure 24. Common-Mode Step Response (Rise) at 35 kHz](image6)
Typical Characteristics (continued)

Unless otherwise specified: $T_A = 25^\circ C$, $V_S = V^+ - V^-$, $V_{\text{SENSE}} = +\text{IN} - (-\text{IN})$, $R_L = 10 \, \Omega$.

Figure 25. Common-Mode Step Response (Fall) at 35 kHz
7 Detailed Description

7.1 Overview

The LMP8646 is a single-supply precision current limiter with variable gain selected through an external resistor \( R_G \) and a variable bandwidth selected through an external capacitor \( C_G \) in parallel with \( R_G \). Its common-mode of operation is –2 V to 76 V, and the LMP8646 has an buffered output to provide a low-output impedance. More details of the LMP8646’s functional description can be seen in the following subsections.

7.2 Functional Block Diagram

![Functional Block Diagram](image)

7.3 Feature Description

7.3.1 Theory of Operation

As seen from Figure 26, the sense current flowing through \( R_{SENSE} \) develops a voltage drop equal to \( V_{SENSE} \). The high impedance inputs of the amplifier does not conduct this current and the high open-loop gain of the sense amplifier forces its noninverting input to the same voltage as the inverting input. In this way the voltage drop across \( R_{IN} \) matches \( V_{SENSE} \). The current \( I_{IN} \) flowing through \( R_{IN} \) has the following equation:

\[
I_{IN} = \frac{V_{SENSE}}{R_{IN}} = \frac{R_{SENSE} \times I_{SENSE}}{R_{IN}}
\]

where

- \( R_{IN} = \frac{1}{Gm} = \frac{1}{(200 \mu A/V)} = 5 \text{kOhm} \) (1)

\( I_{IN} \) flows entirely across the external gain resistor \( R_G \) to develop a voltage drop equal to:

\[
V_{RG} = I_{IN} \times R_G = (\frac{V_{SENSE}}{R_{IN}}) \times R_G = (\frac{R_{SENSE} \times I_{SENSE}}{R_{IN}}) \times R_G
\]

This voltage is buffered and showed at the output with a very low impedance allowing a very easy interface of the LMP8646 with the feedback of many voltage regulators. This output voltage has the following equation:

\[
V_{OUT} = V_{RG} = (\frac{R_{SENSE} \times I_{SENSE}}{R_{IN}}) \times R_G
\]

where

- \( \text{Gain} = R_G / R_{IN} \) (6)
Feature Description (continued)

![Figure 26. Current Monitor](image)

7.3.1.1 Maximum Output Voltage, $V_{OUT_{\text{MAX}}}$

The maximum output voltage, $V_{OUT_{\text{MAX}}}$, depends on the supply voltage, $V_S = V^+ - V^-$, and on the common-mode voltage, $V_{CM} = (+IN + -IN) / 2$.

The following subsections show three cases to calculate for $V_{OUT_{\text{MAX}}}$.

7.3.1.1.1 Case 1: $-2 \text{ V} < V_{CM} < 1.8 \text{ V}$, and $V_S > 2.7 \text{ V}$

If $V_S \geq 5 \text{ V}$,
then $V_{OUT_{\text{MAX}}} = 1.3 \text{ V}$.

Else if $V_S = 2.7 \text{ V}$,
then $V_{OUT_{\text{MAX}}} = 1.1 \text{ V}$.

7.3.1.1.2 Case 2: $1.8 \text{ V} < V_{CM} < V_S$, and $V_S > 3.3 \text{ V}$

In this case, $V_X$ is a fixed value that depends on the supply voltage. $V_X$ has the following values:

- If $V_S = 12 \text{ V}$, then $V_X = 10 \text{ V}$.
- Else if $V_S = 5 \text{ V}$, then $V_X = 3.3 \text{ V}$.
- Else if $V_S = 2.7 \text{ V}$, then $V_X = 1.1 \text{ V}$.

If $V_X \leq (V_{CM} - V_{SENSE} - 0.25)$,
then $V_{OUT_{\text{MAX}}} = V_X$.

Else,

$V_{OUT_{\text{MAX}}} = (V_{CM} - V_{SENSE} - 0.25)$.

For example, if $V_{CM} = 4 \text{ V}$, $V_S = 5 \text{ V}$ (and thus $V_X = 3.3 \text{ V}$), $V_{SENSE} = 0.1 \text{ V}$, then $V_{OUT_{\text{MAX}}} = 3.3 \text{ V}$ because $3.3 \text{ V} \leq (4 - 0.1 - 0.25)$.

7.3.1.1.3 Case 3: $V_{CM} > V_S$, and $V_S > 2.7 \text{ V}$

If $V_S = 12 \text{ V}$, then $V_{OUT_{\text{MAX}}} = 10 \text{ V}$.

Else if $V_S = 5 \text{ V}$, then $V_{OUT_{\text{MAX}}} = 3.3 \text{ V}$.

Else if $V_S = 2.7 \text{ V}$, then $V_{OUT_{\text{MAX}}} = 1.1 \text{ V}$.
7.4 Device Functional Modes

7.4.1 Output Accuracy

The output accuracy is the device error contributed by the LMP8646 based on its offset and gain errors. The LMP8646 output accuracy has the following equations:

\[
\text{Output Accuracy} = \left| \frac{V_{\text{OUT, THEO}} - V_{\text{OUT, CALC}}}{V_{\text{OUT, THEO}}} \right| \times 100(\%)
\]

where \( V_{\text{OUT, THEO}} = \left( V_{\text{SENSE}} \times \frac{R_G}{1/G_m} \right) \)

and \( V_{\text{OUT, CALC}} = \left( \frac{(V_{\text{SENSE}} + V_{\text{OFFSET}}) \times R_G}{1/G_m (1 + G_m_{\text{Accuracy}})} \right) \)

Output Accuracy Equations \( (7) \)

For example, assume \( V_{\text{SENSE}} = 100 \text{ mV} \), \( R_G = 10 \\text{kOhm} \), and it is known that \( V_{\text{OFFSET}} = 1 \text{ mV} \) and \( G_m_{\text{Accuracy}} = 2\% \) (Electrical Characteristics Table), then the output accuracy can be calculated as:

\[
V_{\text{OUT, THEO}} = \left( 100 \text{ mV} \times \frac{10 \text{ k} \Omega}{1/(200 \mu)} \right) = 0.2\text{V}
\]

\[
V_{\text{OUT, CALC}} = \frac{(100 \text{ mV} + 1 \text{ mV}) \times 10 \text{ k} \Omega}{1/[200 \mu (1 + 2/100)]} = 0.20604\text{V}
\]

Output Accuracy = \[
\frac{0.2\text{V} - 0.20604\text{V}}{0.2\text{V}} \times 100 = 3.02\%
\]

Output Accuracy Example \( (8) \)

In fact, as \( V_{\text{SENSE}} \) decreases, the output accuracy worsens as seen in Figure 27. These equations provide a valuable tool to estimate how the LMP8646 affects the overall system performance. Knowing this information allows the system designer to pick the appropriate external resistances \( (R_{\text{SENSE}} \text{ and } R_G) \) to adjust for the tolerable system error. Examples of this tolerable system error can be seen in the next sections.

Figure 27. Output Accuracy vs. \( V_{\text{SENSE}} \)

7.4.2 Selection of the Sense Resistor, \( R_{\text{SENSE}} \)

The accuracy of the current measurement also depends on the value of the shunt resistor \( R_{\text{SENSE}} \). Its value depends on the application and is a compromise between small-signal accuracy and maximum permissible voltage loss in the load line.

\( R_{\text{SENSE}} \) is directly proportional to \( V_{\text{SENSE}} \) through the equation \( R_{\text{SENSE}} = \left( V_{\text{SENSE}} \right) / (I_{\text{SENSE}}) \). If \( V_{\text{SENSE}} \) is small, then there is a smaller voltage loss in the load line, but the output accuracy is worse because the LMP8646 offset error will contribute more. Therefore, high values of \( R_{\text{SENSE}} \) provide better output accuracy by minimizing the effects of offset, while low values of \( R_{\text{SENSE}} \) minimize the voltage loss in the load line. For most applications, best performance is obtained with an \( R_{\text{SENSE}} \) value that provides a \( V_{\text{SENSE}} \) of 100 mV to 200 mV.
Device Functional Modes (continued)

7.4.2.1 \(R_{\text{SENSE}}\) Consideration for System Error

The output accuracy described in the previous section talks about the error contributed just by the LMP8646. The system error, however, consists of the errors contributed by the LMP8646 as well as other external resistors such as \(R_{\text{SENSE}}\) and \(R_G\). Let’s rewrite the output accuracy equation for the system error assuming that \(R_{\text{SENSE}}\) is non-ideal and \(R_G\) is ideal. This equation can be seen as:

\[
\text{System Error} = \left| \frac{V_{\text{OUT THEO}} - V_{\text{OUT CALC}}}{V_{\text{OUT THEO}}} \right| \times 100(\%)
\]

where \(V_{\text{OUT THEO}} = (R_{\text{SENSE}} \times I_{\text{SENSE}}) \times \frac{R_G}{1/G_m}\)

and \(V_{\text{OUT CALC}} = \frac{[R_{\text{SENSE}} (1+\text{Tolerance}) \times I_{\text{SENSE}} + V_{\text{OFFSET}}] \times R_G}{1/[G_m (1 + G_m_{\text{Accuracy}})]}\)

System Error Example Assuming \(R_{\text{SENSE}}\) is Non-ideal and \(R_G\) is Ideal

Continuing from the previous output accuracy example, we can calculate for the system error assuming that \(R_{\text{SENSE}} = 100\ \text{m}\Omega\) (with 1% tolerance), \(I_{\text{SENSE}} = 1\ \text{A}\), and \(R_G = 10\ \text{k}\Omega\). From the Electrical Characteristics Table, it is also known that \(V_{\text{OFFSET}} = 1\ \text{mV}\) and \(G_m_{\text{Accuracy}} = 2\%\).

\[
V_{\text{OUT THEO}} = (100\ \text{m}\Omega \times 1\ \text{A}) \times \frac{10\ \text{k}\Omega}{1/(200\ \mu\Omega)} = 0.2\text{V}
\]

\[
V_{\text{OUT CALC}} = \left[100\ \text{m}\Omega \times (1+1/100) \times 1\ \text{A} + 1\ \text{mV}\right] \times 10\ \text{k}\Omega \times \frac{1/200\ \mu\Omega}{1 + (2/100)} = 0.20808\text{V}
\]

System Error Example Assuming \(R_{\text{SENSE}}\) is Non-ideal and \(R_G\) is Ideal

Because an \(R_{\text{SENSE}}\) tolerance will increase the system error, we recommend selecting an \(R_{\text{SENSE}}\) resistor with low tolerance.
8 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information
The LMP8646 can be driven by many different regulators with a feedback pin and connected to many different types of loads such as capacitive and resistive. The following sections gives three typical applications of the LMP8646.

8.2 Typical Applications

8.2.1 Application #1: Current Limiter With a Capacitive Load

8.2.1.1 Design Requirements
A supercap application requires a very high capacitive load to be charged. This example assumes the output capacitor is 5F with a limited sense current at 1.5A. The LM3102 will provide the current to charge the supercap, and the LMP8646 will monitor this current to make sure it does not exceed the desired 1.5A value.

8.2.1.2 Detailed Design Procedure
To limit the capacitor current, first connect the LMP8646 output to the feedback pin of the LM3102, as shown in Figure 28. This feedback voltage at the FB pin is compared to a 0.8V internal reference. Any voltage above this 0.8V means the output current is above the desired value of 1.5A, and the LM3102 will reduce its output current to maintain the desired 0.8V at the FB pin.

The following steps show the design procedures for this supercap application. In summary, the steps consist of selecting the components for the voltage regulator, integrating the LMP8646 and selecting the proper values for its gain, bandwidth, and output resistor, and adjusting these components to yield the desired performance.

Step 1: Choose the components for the Regulator.
Refer to the LM3102 evaluation board application note (AN-1646) to select the appropriate components for the LM3102 voltage regulator.
Typical Applications (continued)

Step 2: Choose the sense resistor, \( R_{\text{Sense}} \)

\( R_{\text{SENSE}} \) sets the voltage \( V_{\text{SENSE}} \) between \(+\text{IN}\) and \(-\text{IN}\) and has the following equation:

\[
R_{\text{SENSE}} = \frac{V_{\text{OUT}}}{[I_{\text{LIMIT}}] \times (R_{G} / 5\text{kOhm})}
\]  

(11)

In general, \( R_{\text{SENSE}} \) depends on the output voltage, limit current, and gain. Refer to section Selection of the Sense Resistor, \( R_{\text{SENSE}} \) to choose the appropriate \( R_{\text{SENSE}} \) value; this example uses 55 mOhm.

Step 3: Choose the gain resistor, \( R_{G} \), for LMP8646

\( R_{G} \) is chosen from the limited sense current. As stated, \( V_{\text{OUT}} = (R_{\text{SENSE}} \times I_{\text{LIMIT}}) \times (R_{G} / 5\text{kOhm}) \). Since \( V_{\text{OUT}} = V_{\text{FB}} = 0.8\text{V} \), the limited sense current is 1.5A, and \( R_{\text{SENSE}} \) is 55 mOhm, \( R_{G} \) can be calculated as:

\[
R_{G} = \frac{(V_{\text{OUT}} \times 5\text{ kOhm})}{(R_{\text{SENSE}} \times I_{\text{LIMIT}})}
\]  

(12)

\[
R_{G} = \frac{(0.8 \times 5\text{ kOhm})}{(55 \text{ mOhm} \times 1.5\text{A})} = 50 \text{ kOhm} \quad \text{(approximate)}
\]  

(13)

Step 4: Choose the Bandwidth Capacitance, \( C_{G} \).

The product of \( C_{G} \) and \( R_{G} \) determines the bandwidth for the LMP8646. Refer to the Typical Performance Characteristics plots to see the range for the LMP8646 bandwidth and gain. Since each application is very unique, the LMP8646 bandwidth capacitance, \( C_{G} \), needs to be adjusted to fit the appropriate application.

Bench data has been collected for the supercap application with the LM3102 regulator, and we found that this application works best for a bandwidth of 500 Hz to 3 kHz. Operating outside of this recommended bandwidth range might create an undesirable load current ringing. We recommend choosing a bandwidth that is in the middle of this range and using the equation \( C_{G} = 1/(2\pi R_{G} \times \text{Bandwidth}) \) to find \( C_{G} \). For example, if the bandwidth is 1.75 kHz and \( R_{G} \) is 50 kOhm, then \( C_{G} \) is approximately 1.8 \( \text{nF} \). After this selection, capture the plot for \( I_{\text{LIMIT}} \) and adjust \( C_{G} \) until a desired load current plot is obtained.

Step 5: Calculate the Output Accuracy and Tolerable System Error

Since the LMP8646 is a precision current limiter, the output current accuracy is extremely important. This accuracy is affected by the system error contributed by the LMP8646 device error and other errors contributed by external resistances, such as \( R_{\text{SENSE}} \) and \( R_{G} \).

In this application, \( V_{\text{SENSE}} = I_{\text{LIMIT}} \times R_{\text{SENSE}} = 1.5\text{A} \times 55 \text{ mOhm} = 0.0825\text{V} \), and \( R_{G} = 50 \text{ kOhm} \). From the Electrical Characteristics Table, it is known that \( V_{\text{OFFSET}} = 1 \text{mV} \) and Gm_Accuracy = 2%. Using the equations shown in Equation 8, the output accuracy can be calculated as 3.24%.

After figuring out the LMP8646 output accuracy, choose a tolerable system error or the output current accuracy that is bigger than the LMP8646 output accuracy. This tolerable system error will be labeled as \( I_{\text{ERROR}} \), and it has the equation \( I_{\text{ERROR}} = (I_{\text{MAX}} - I_{\text{LIMIT}})/I_{\text{MAX}} \text{%} \). In this example, we will choose an \( I_{\text{ERROR}} \) of 5%, which will be used to calculate for \( ROUT \) shown in the next step.

Step 6: Choose the output resistor, \( ROUT \)

At start-up, the capacitor is not charged yet and thus the output voltage of the LM3102 is very small. Therefore, at start-up, the output current is at its maximum (\( I_{\text{MAX}} \)). When the output voltage is at its nominal, then the output current will settle to the desired limited value. Because a large current error is not desired, ROUT needs to be chosen to stabilize the loop with minimal initial start-up current error. Follow the equations and example below to choose the appropriate value for ROUT to minimize this initial error.

As discussed in step 4, the allowable \( I_{\text{ERROR}} \) is 5%, where \( I_{\text{ERROR}} = (I_{\text{MAX}} - I_{\text{LIMIT}})/I_{\text{MAX}} \text{%} \). Therefore, the maximum allowable current is calculated as: \( I_{\text{MAX}} = I_{\text{LIMIT}} \times (1 + I_{\text{ERROR}}) = 1.5\text{A} \times (1 + 5/100) = 1.575 \text{A} \).

Next, use Equation 14 below to calculate for \( ROUT \):

\[
ROUT = \frac{(I_{\text{MAX}} \times R_{\text{SENSE}} \times \text{Gain} - V_{\text{FB}})}{V_{\text{FB}} \times \text{RFBB} - \text{RFBT}}
\]

(14)

For example, assume the minimum LM3102 output voltage, \( V_{\text{O}_{\text{REG\_MIN}}} \), is 0.6V, then ROUT can be calculated as \( ROUT = [1.575\text{A} \times 55 \text{ mOhm} \times (49.9\text{k} / 5\text{k}) - 0.8] / [(0.8 / 2\text{k}) - (0.6 - 0.8) / 10\text{k}] = 153.6 \text{Ohm} \).
Typical Applications (continued)

Populate ROUT with a resistor that is as close as possible to 153.6 Ohm (this application uses 160 Ohm). If the limited sense current has a gain error and is not 1.5A at any point in time, then adjust this ROUT value to obtain the desired limit current.

We recommend that the value for ROUT is at least 50 Ohm.

**Step 7: Adjusting Components**

Capture the output current and output voltage plots and adjust the components as necessary. The most common components to adjust are \( C_G \) to decrease the current ripple and ROUT to get a low current error. An example output current and voltage plot can be seen in Figure 29.

### 8.2.1.3 Application Curve

![Figure 29. SuperCap Application With LM3102 Regulator Plot](image)

### 8.2.2 Application #2: Current Limiter With a Resistive Load

![Figure 30. Resistive Load Application With LMZ12003 Regulator](image)
Typical Applications (continued)

8.2.2.1 Design Requirements

This subsection describes the design process for a resistive load application with the LMZ12003 voltage regulator as seen in Figure 30. To see the current limiting capability of the LMP8646, the open-loop current must be greater than the close-loop current. An open-loop occurs when the LMP8646 output is not connected to the LMZ12003’s feedback pin. For this example, we will let the open-loop current to be 1.5A and the close-loop current, I_LIMIT, to be 1A.

8.2.2.2 Detailed Design Procedure

Step 1: Choose the components for the Regulator.

Refer to the LMZ12003 application note (AN-2031) to select the appropriate components for the LMZ12003.

Step 2: Choose the sense resistor, R_SENSE

R_SENSE sets the voltage V_SENSE between +IN and -IN and has the following equation:

\[ R_{\text{SENSE}} = \frac{V_{\text{OUT}}}{(|I_{\text{LIMIT}}|) \cdot \left(\frac{R_G}{5k\Omega}\right)} \] (15)

In general, R_SENSE depends on the output voltage, limit current, and gain. Refer to section Selection of the Sense Resistor, R_SENSE to choose the appropriate R_SENSE value; this example uses 50 mOhm.

Step 3: Choose the gain resistor, R_G, for LMP8646

R_G is chosen from I_LIMIT. As stated, V_OUT = (R_SENSE * I_LIMIT) * (R_G / 5kOhm). Since V_OUT = V_FB = 0.8V, I_LIMIT = 1A, and R_SENSE = 50 mOhm, R_G can be calculated as:

\[ R_G = \frac{(V_{\text{OUT}} \cdot 5 \text{ kOhm})}{(R_{\text{SENSE}} \cdot I_{\text{LIMIT}})} \] (16)

\[ R_G = \frac{(0.8 \cdot 5 \text{ kOhm})}{(50 \text{ mOhm} \cdot 1A)} = 0.8 \text{ kOhm} \] (17)

Step 4: Choose the Bandwidth Capacitance, C_G.

The product of C_G and R_G determines the bandwidth for the LMP8646. Refer to the Typical Performance Characteristics plots to see the range for the LMP8646 bandwidth and gain. Since each application is very unique, the LMP8646 bandwidth capacitance, C_G, needs to be adjusted to fit the appropriate application.

Bench data has been collected for this resistive load application with the LMZ12003 regulator, and we found that this application works best for a bandwidth of 2 kHz to 30 kHz. Operating anything less than this recommended bandwidth might prevent the LMP8646 from quickly limiting the current. We recommend choosing a bandwidth that is in the middle of this range and using the equation: C_G = 1/(2*pi*R_G*Bandwidth) to find C_G (this example uses a C_G value of 0.1nF). After this selection, capture the load current plot and adjust C_G until a desired output current plot is obtained.

Step 5: Choose the output resistor, ROUT, for the LMP8646

ROUT plays a very small role in the overall system performance for the resistive load application. ROUT was important in the supercap application because it affects the initial current error. Because current is directly proportional to voltage for a resistive load, the output current is not large at start-up. The bigger the ROUT, the longer it takes for the output voltage to reach its final value. We recommend that the value for ROUT is at least 50 Ohm, which is the chosen value for this example.

Step 6: Adjusting Components

Capture the output current and output voltage plots and adjust the components as necessary. The most common component to adjust is C_G for the bandwidth. An example of the output current and voltage plot can be seen in Figure 31.
Typical Applications (continued)

8.2.2.3 Application Curve

![Plot for the Resistive Load Application With LMZ12003 Regulator Plot](image)

**Figure 31.** Plot for the Resistive Load Application With LMZ12003 Regulator Plot

8.2.3 Application #3: Current Limiter With a Low-Dropout Regulator and Resistive Load

![Resistive Load Application With LP38501 Regulator](image)

**Figure 32.** Resistive Load Application With LP38501 Regulator

8.2.3.1 Design Requirements

This next example is the same as the last example, except that the regulator is now a low-dropout regulator, the LP38501, as seen in Figure 32. For this example, we will let the open-loop current to be 1.25A and the close-loop current, $I_{\text{LIMIT}}$, to be 1A.

8.2.3.2 Detailed Design Procedure

**Step 1: Choose the components for the Regulator.**

Refer to the LP38501 application note (AN-1830) to select the appropriate components for the LP38501.

**Step 2: Choose the sense resistor, $R_{\text{SENSE}}$.**

$R_{\text{SENSE}}$ sets the voltage $V_{\text{SENSE}}$ between $+\text{IN}$ and $-\text{IN}$ and has the following equation:

$$R_{\text{SENSE}} = \frac{V_{\text{OUT}}}{[(I_{\text{LIMIT}}) \times (R_G / 5\text{kOhm})]}$$

(18)

In general, $R_{\text{SENSE}}$ depends on the output voltage, limit current, and gain. Refer to section *Selection of the Sense Resistor, $R_{\text{SENSE}}$* to choose the appropriate $R_{\text{SENSE}}$ value; this example uses 58 mOhm.

**Step 3: Choose the gain resistor, $R_G$, for LMP8646**
Typical Applications (continued)

R_G is chosen from I_LIMIT. As stated, V_OUT = (R_SENSE * I_LIMIT) * (R_G / 5kOhm). Since V_OUT = ADJ = 0.6V, I_LIMIT = 1A, and R_SENSE = 58 mOhm, R_G can be calculated as:

\[ R_G = \frac{(V_{OUT} * 5 \text{ kOhm})}{(R_{SENSE} * I_{LIMIT})} \]

\[ R_G = \frac{(0.6 * 5 \text{ kOhm})}{(58 \text{ mOhm} * 1A)} = 51.7 \text{ kOhm} \]

(19) (20)

Step 4: Choose the Bandwidth Capacitance, C_G.

The product of C_G and R_G determines the bandwidth for the LMP8646. Refer to the Typical Performance Characteristics plots to see the range for the LMP8646 bandwidth and gain. Since each application is very unique, the LMP8646 bandwidth capacitance, C_G, needs to be adjusted to fit the appropriate application.

Bench data has been collected for this resistive load application with the LP38501 regulator, and we found that this application works best for a bandwidth of 50 Hz to 300 Hz. Operating anything larger than this recommended bandwidth might prevent the LMP8646 from quickly limiting the current. We recommend choosing a bandwidth that is in the middle of this range and using the equation: C_G = 1/(2*pi*R_G*Bandwidth) to find C_G (this example uses a C_G value of 10 nF). After this selection, capture the plot for I_SENSE and adjust C_G until a desired sense current plot is obtained.

Step 5: Choose the output resistor, ROUT, for the LMP8646

ROUT plays a very small role in the overall system performance for the resistive load application. ROUT was important in the supercap application because it affects the initial current error. Because current is directly proportional to voltage for a resistive load, the output current is not large at start-up. The bigger the ROUT, the longer it takes for the output voltage to reach its final value. We recommend that the value for ROUT is at least 50 Ohm, which is the value we used for this example.

Step 6: Adjusting Components

Capture the output current and output voltage plots and adjust the components as necessary. The most common component to adjust is C_G for the bandwidth. An example plot of the output current and voltage can be seen in Figure 33.

8.2.3.3 Application Curve

Figure 33. Plot for the Resistive Load Application With the LP38501 LDO Regulator
9 Power Supply Recommendations

Source V+ with an external voltage as recommended in the electrical characteristics table. It is recommended to place a 100nF ceramic bypass capacitor to ground as close to possible to the V+ pin. In addition, an electrolytic or tantalum capacitor of 10μF is recommended. The bulk capacitor does not need to be in close vicinity with the LMP8646 and could be close to the voltage source terminals or at the output of the voltage regulator powering the LMP8646.

10 Layout

10.1 Layout Guidelines

• In a 4-layer board design, the recommended layer stack order from top to bottom is: signal, power, ground, and signal
• Bypass capacitors should be placed in close proximity to the V+ pin
• The trace for pins +IN and -IN should be big enough to handle the current running through it.

10.2 Layout Example

![Figure 34. LMP8646 Evaluation Board Layout](image-url)
11 Device and Documentation Support

11.1 Trademarks
All trademarks are the property of their respective owners.

11.2 Electrostatic Discharge Caution

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

11.3 Glossary

SLYZ022 — Ti Glossary.
This glossary lists and explains terms, acronyms, and definitions.

12 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
# PACKAGING INFORMATION

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<th>Package Drawing</th>
<th>PIns</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMP8646MK/NOPB</td>
<td>ACTIVE</td>
<td>SOT-23-THIN</td>
<td>DDC</td>
<td>6</td>
<td>1000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 125</td>
<td>AK7A</td>
<td>Samples</td>
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<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check [http://www.ti.com/productcontent](http://www.ti.com/productcontent) for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material).

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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## TAPE AND REEL INFORMATION

### TAPE DIMENSIONS

- **A0**: Dimension designed to accommodate the component width
- **B0**: Dimension designed to accommodate the component length
- **K0**: Dimension designed to accommodate the component thickness
- **W**: Overall width of the carrier tape
- **P1**: Pitch between successive cavity centers

### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

- Sprocket Holes
- User Direction of Feed
- Pocket Quadrants

### Package Materials Information

*All dimensions are nominal.*

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<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
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<th>A0 (mm)</th>
<th>B0 (mm)</th>
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**TAPE AND REEL BOX DIMENSIONS**

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<td>35.0</td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion.
D. Falls within JEDEC MO-193 variation AA (6 pin).
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Publication IPC-7351 is recommended for alternate designs.
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.
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