LMT85-Q1 1.8-V, SC70, Analog Temperature Sensors

1 Features

- LMT85-Q1 is AEC-Q100 Qualified for Automotive Applications:
  - Device Temperature Grade 0: –40°C to +150°C
  - Device HBM ESD Classification Level 2
  - Device CDM ESD Classification Level C6
- Very Accurate: ±0.4°C Typical
- Low 1.8-V Operation
- Average Sensor Gain of -8.2 mV/°C
- Low 5.4-µA Quiescent Current
- Wide Temperature Range: –50°C to 150°C
- Output is Short-Circuit Protected
- Push-Pull Output With ±50-µA Drive Capability
- Footprint Compatible With the Industry-Standard LM20/19 and LM35 Temperature Sensors
- Cost-Effective Alternative to Thermistors

2 Applications

- Infotainment and Cluster
- Powertrain Systems
- Smoke and Heat Detectors
- Drones
- Appliances

3 Description

The LMT85-Q1 is a precision CMOS temperature sensor with ±0.4°C typical accuracy (±2.7°C maximum) and a linear analog output voltage that is inversely proportional to temperature. The 1.8-V supply voltage operation, 5.4-µA quiescent current, and 0.7-ms power-on time enable effective power-cycling architectures to minimize power consumption for battery-powered applications such as drones and sensor nodes. The LMT85-Q1 device is AEC-Q100 Grade 0 qualified and maintains ±2.7°C maximum accuracy over the full operating temperature range without calibration; this makes the LMT85-Q1 suitable for automotive applications such as infotainment, cluster, and powertrain systems. The accuracy over the wide operating range and other features make the LMT85-Q1 an excellent alternative to thermistors.

For devices with different average sensor gains and comparable accuracy, refer to Comparable Alternative Devices for alternative devices in the LMT8x family.

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMT85-Q1</td>
<td>SOT (5)</td>
<td>2.00 mm × 1.25 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum addendum at the end of the data sheet.

Output Voltage vs Temperature

Thermal Time Constant

* Fast thermal response NTC

An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.
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# 4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

<table>
<thead>
<tr>
<th>DATE</th>
<th>REVISION</th>
<th>NOTES</th>
</tr>
</thead>
<tbody>
<tr>
<td>October 2017</td>
<td>*</td>
<td>Initial release. Moved the automotive device from the SNIS200 to a standalone data sheet.</td>
</tr>
</tbody>
</table>
5 Device Comparison Tables

Table 1. Available Device Packages

<table>
<thead>
<tr>
<th>ORDER NUMBER(1)</th>
<th>PACKAGE</th>
<th>PIN</th>
<th>BODY SIZE (NOM)</th>
<th>MOUNTING TYPE</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMT85DCK</td>
<td>SOT (AKA(2): SC70, DCK)</td>
<td>5</td>
<td>2.00 mm × 1.25 mm</td>
<td>Surface Mount</td>
</tr>
<tr>
<td>LMT85LP</td>
<td>TO-92 (AKA(2): LP)</td>
<td>3</td>
<td>4.30 mm × 3.50 mm</td>
<td>Through-hole; straight leads</td>
</tr>
<tr>
<td>LMT85LPG</td>
<td>TO-92S (AKA(2): LPG)</td>
<td>3</td>
<td>4.00 mm × 3.15 mm</td>
<td>Through-hole; straight leads</td>
</tr>
<tr>
<td>LMT85LPM</td>
<td>TO-92 (AKA(2): LPM)</td>
<td>3</td>
<td>4.30 mm × 3.50 mm</td>
<td>Through-hole; formed leads</td>
</tr>
<tr>
<td>LMT85DCK-Q1</td>
<td>SOT (AKA(2): SC70, DCK)</td>
<td>5</td>
<td>2.00 mm × 1.25 mm</td>
<td>Surface Mount</td>
</tr>
</tbody>
</table>

(1) For all available packages and complete order numbers, see the Package Option addendum at the end of the data sheet.
(2) AKA = Also Known As

Table 2. Comparable Alternative Devices

<table>
<thead>
<tr>
<th>DEVICE NAME</th>
<th>AVERAGE OUTPUT SENSOR GAIN</th>
<th>POWER SUPPLY RANGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMT84-Q1</td>
<td>–5.5 mV/°C</td>
<td>1.5 V to 5.5 V</td>
</tr>
<tr>
<td>LMT85-Q1</td>
<td>–8.2 mV/°C</td>
<td>1.8 V to 5.5 V</td>
</tr>
<tr>
<td>LMT86-Q1</td>
<td>–10.9 mV/°C</td>
<td>2.2 V to 5.5 V</td>
</tr>
<tr>
<td>LMT87-Q1</td>
<td>–13.6 mV/°C</td>
<td>2.7 V to 5.5 V</td>
</tr>
</tbody>
</table>

6 Pin Configuration and Functions

DCK Package
5-Pin SOT/SC70
(Top View)

Pin Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>TYPE</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>2(1), 5</td>
<td>Ground</td>
</tr>
<tr>
<td>OUT</td>
<td>3</td>
<td>Analog Output</td>
</tr>
<tr>
<td>VDD</td>
<td>1, 4</td>
<td>Power</td>
</tr>
</tbody>
</table>

(1) Direct connection to the back side of the die
7 Specifications

7.1 Absolute Maximum Ratings

See (1)(2)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply voltage</td>
<td>−0.3</td>
<td>6</td>
<td>V</td>
</tr>
<tr>
<td>Voltage at output pin</td>
<td>−0.3</td>
<td>(VDD + 0.5)</td>
<td>V</td>
</tr>
<tr>
<td>Output current</td>
<td>−7</td>
<td>7</td>
<td>mA</td>
</tr>
<tr>
<td>Input current at any pin (3)</td>
<td>−5</td>
<td>5</td>
<td>mA</td>
</tr>
<tr>
<td>Maximum junction temperature (TJMAX)</td>
<td>150</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature, TSAT</td>
<td>−65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) Soldering process must comply with Reflow Temperature Profile specifications. Refer to www.ti.com/packaging.

(3) When the input voltage (VI) at any pin exceeds power supplies (VI < GND or VI > V), the current at that pin should be limited to 5 mA.

7.2 ESD Ratings

<table>
<thead>
<tr>
<th></th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMT85DCK-Q1 in SC70 package</td>
<td></td>
<td></td>
</tr>
<tr>
<td>V(ESD) Electrostatic discharge</td>
<td>Human-body model (HBM), per AEC Q100-002(1)</td>
<td>±2500</td>
</tr>
<tr>
<td></td>
<td>Charged-device model (CDM), per AEC Q101-011</td>
<td>±1000</td>
</tr>
</tbody>
</table>

(1) AEC Q100-002 indicates that HBM stressing shall be in accordance with the ANSI/ESDA/JEDEC JS-001 specification.

7.3 Recommended Operating Conditions

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Specified temperature</td>
<td>TMIN ≤ TA ≤ TMAX</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td></td>
<td>−50 ≤ TA ≤ 150</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>Supply voltage (VDD)</td>
<td>1.8</td>
<td>5.5</td>
<td>V</td>
</tr>
</tbody>
</table>

7.4 Thermal Information(1)

<table>
<thead>
<tr>
<th>THERMAL METRIC(2)</th>
<th>LMT85-Q1</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>RθJA Junction-to-ambient thermal resistance</td>
<td>275</td>
<td>°C/W</td>
</tr>
<tr>
<td>RθJC(top) Junction-to-case (top) thermal resistance</td>
<td>84</td>
<td>°C/W</td>
</tr>
<tr>
<td>RθJB Junction-to-board thermal resistance</td>
<td>56</td>
<td>°C/W</td>
</tr>
<tr>
<td>ψJT Junction-to-top characterization parameter</td>
<td>1.2</td>
<td>°C/W</td>
</tr>
<tr>
<td>ψJB Junction-to-board characterization parameter</td>
<td>55</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

(1) For information on self-heating and thermal response time, see section Mounting and Thermal Conductivity.

(2) For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report.

(3) The junction to ambient thermal resistance (RθJA) under natural convection is obtained in a simulation on a JEDEC-standard, High-K board as specified in JESD51-7, in an environment described in JESD51-2. Exposed pad packages assume that thermal vias are included in the PCB, per JESD 51-5.

(4) Changes in output due to self-heating can be computed by multiplying the internal dissipation by the thermal resistance.
7.5 Accuracy Characteristics

These limits do not include DC load regulation. These stated accuracy limits are with reference to the values in Table 3.

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN(1)</th>
<th>TYP(2)</th>
<th>MAX(1)</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Temperature accuracy (3)</td>
<td>TA = TJ = 20°C to 150°C; VDD = 1.8 V to 5.5 V</td>
<td>-2.7</td>
<td>±0.4</td>
<td>2.7</td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td>TA = TJ = 0°C to 150°C; VDD = 1.9 V to 5.5 V</td>
<td>-2.7</td>
<td>±0.7</td>
<td>2.7</td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td>TA = TJ = 0°C to 150°C; VDD = 2.6 V to 5.5 V</td>
<td>±0.3</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td>TA = TJ = -50°C to 0°C; VDD = 2.3 V to 5.5 V</td>
<td>-2.7</td>
<td>±0.7</td>
<td>2.7</td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td>TA = TJ = -50°C to 0°C; VDD = 2.9 V to 5.5 V</td>
<td>±0.25</td>
<td></td>
<td></td>
<td>°C</td>
</tr>
</tbody>
</table>

(1) Limits are specific to TI’s AOQL (Average Outgoing Quality Level).
(2) Typicals are at TJ = TA = 25°C and represent most likely parametric norm.
(3) Accuracy is defined as the error between the measured and reference output voltages, tabulated in the Transfer Table at the specified conditions of supply gain setting, voltage, and temperature (expressed in °C). Accuracy limits include line regulation within the specified conditions. Accuracy limits do not include load regulation; they assume no DC load.

7.6 Electrical Characteristics

Unless otherwise noted, these specifications apply for VDD = +1.8V to +5.5V. MIN and MAX limits apply for TA = TJ = TMIN to TMAX, unless otherwise noted; typical values apply for TA = TJ = 25°C.

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN(1)</th>
<th>TYP(2)</th>
<th>MAX(1)</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Average sensor gain (output transfer function slope)</td>
<td>–30°C and 90°C used to calculate average sensor gain</td>
<td>-8.2</td>
<td></td>
<td></td>
<td>mV/°C</td>
</tr>
<tr>
<td>Load regulation (3)</td>
<td>Source ≤ 50 μA, (VDD - VOUT) ≥ 200 mV</td>
<td>-1</td>
<td>-0.22</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>Sink ≤ 50 μA, VOUT ≥ 200 mV</td>
<td>0.26</td>
<td>1</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Line regulation (4)</td>
<td>TA = TJ = 30°C to 150°C; (VDD - VOUT) ≥ 100 mV</td>
<td>5.4</td>
<td>8.1</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td></td>
<td>TA = TJ = -50°C to 150°C; (VDD - VOUT) ≥ 100 mV</td>
<td>5.4</td>
<td>9</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>IS</td>
<td>COUT = 0 pF to 1100 pF</td>
<td>1100</td>
<td></td>
<td></td>
<td>pF</td>
</tr>
<tr>
<td>Power-on time (5)</td>
<td>COUT = 0 pF to 1100 pF</td>
<td>0.7</td>
<td>1.9</td>
<td></td>
<td>ms</td>
</tr>
<tr>
<td>Output drive</td>
<td>TA = TJ = 25°C</td>
<td>-50</td>
<td>+50</td>
<td></td>
<td>μA</td>
</tr>
</tbody>
</table>

(1) Limits are specific to TI’s AOQL (Average Outgoing Quality Level).
(2) Typicals are at TJ = TA = 25°C and represent most likely parametric norm.
(3) Source currents are flowing out of the LMT85-Q1. Sink currents are flowing into the LMT85-Q1.
(4) Line regulation (DC) is calculated by subtracting the output voltage at the highest supply voltage from the output voltage at the lowest supply voltage. The typical DC line regulation specification does not include the output voltage shift discussed in Output Voltage Shift.
(5) Specified by design and characterization.
7.7 Typical Characteristics

Figure 1. Temperature Error vs Temperature

Figure 2. Minimum Operating Temperature vs Supply Voltage

Figure 3. Supply Current vs Temperature

Figure 4. Supply Current vs Supply Voltage

Figure 5. Load Regulation, Sourcing Current

Figure 6. Load Regulation, Sinking Current
Typical Characteristics (continued)

Figure 7. Change in Vout vs Overhead Voltage

Figure 8. Supply-Noise Gain vs Frequency

Figure 9. Output Voltage vs Supply Voltage
8 Detailed Description

8.1 Overview
The LMT85-Q1 is an analog output temperature sensor. The temperature sensing element is comprised of a simple base emitter junction that is forward biased by a current source. The temperature-sensing element is then buffered by an amplifier and provided to the OUT pin. The amplifier has a simple push-pull output stage thus providing a low impedance output source.

8.2 Functional Block Diagram
Full-Range Celsius Temperature Sensor (−50°C to 150°C).

8.3 Feature Description

8.3.1 LMT85-Q1 Transfer Function
The output voltage of the LMT85-Q1, across the complete operating temperature range, is shown in Table 3. This table is the reference from which the LMT85 accuracy specifications (listed in the Accuracy Characteristics section) are determined. This table can be used, for example, in a host processor look-up table. A file containing this data is available for download at the LMT85-Q1 product folder under Tools and Software Models.

Table 3. LMT85-Q1 Transfer Table

<table>
<thead>
<tr>
<th>TEMP (°C)</th>
<th>VOUT (mV)</th>
<th>TEMP (°C)</th>
<th>VOUT (mV)</th>
<th>TEMP (°C)</th>
<th>VOUT (mV)</th>
<th>TEMP (°C)</th>
<th>VOUT (mV)</th>
<th>TEMP (°C)</th>
<th>VOUT (mV)</th>
</tr>
</thead>
<tbody>
<tr>
<td>-50</td>
<td>1955</td>
<td>-10</td>
<td>1648</td>
<td>30</td>
<td>1324</td>
<td>70</td>
<td>991</td>
<td>110</td>
<td>651</td>
</tr>
<tr>
<td>-49</td>
<td>1949</td>
<td>-9</td>
<td>1639</td>
<td>31</td>
<td>1316</td>
<td>71</td>
<td>983</td>
<td>111</td>
<td>642</td>
</tr>
<tr>
<td>-48</td>
<td>1942</td>
<td>-8</td>
<td>1631</td>
<td>32</td>
<td>1308</td>
<td>72</td>
<td>974</td>
<td>112</td>
<td>634</td>
</tr>
<tr>
<td>-47</td>
<td>1935</td>
<td>-7</td>
<td>1623</td>
<td>33</td>
<td>1299</td>
<td>73</td>
<td>966</td>
<td>113</td>
<td>625</td>
</tr>
<tr>
<td>-46</td>
<td>1928</td>
<td>-6</td>
<td>1615</td>
<td>34</td>
<td>1291</td>
<td>74</td>
<td>957</td>
<td>114</td>
<td>617</td>
</tr>
<tr>
<td>-45</td>
<td>1921</td>
<td>-5</td>
<td>1607</td>
<td>35</td>
<td>1283</td>
<td>75</td>
<td>949</td>
<td>115</td>
<td>608</td>
</tr>
<tr>
<td>-44</td>
<td>1915</td>
<td>-4</td>
<td>1599</td>
<td>36</td>
<td>1275</td>
<td>76</td>
<td>941</td>
<td>116</td>
<td>599</td>
</tr>
<tr>
<td>-43</td>
<td>1908</td>
<td>-3</td>
<td>1591</td>
<td>37</td>
<td>1267</td>
<td>77</td>
<td>932</td>
<td>117</td>
<td>591</td>
</tr>
<tr>
<td>-42</td>
<td>1900</td>
<td>-2</td>
<td>1583</td>
<td>38</td>
<td>1258</td>
<td>78</td>
<td>924</td>
<td>118</td>
<td>582</td>
</tr>
<tr>
<td>-41</td>
<td>1892</td>
<td>-1</td>
<td>1575</td>
<td>39</td>
<td>1250</td>
<td>79</td>
<td>915</td>
<td>119</td>
<td>573</td>
</tr>
<tr>
<td>-40</td>
<td>1885</td>
<td>0</td>
<td>1567</td>
<td>40</td>
<td>1242</td>
<td>80</td>
<td>907</td>
<td>120</td>
<td>565</td>
</tr>
<tr>
<td>-39</td>
<td>1877</td>
<td>1</td>
<td>1559</td>
<td>41</td>
<td>1234</td>
<td>81</td>
<td>898</td>
<td>121</td>
<td>556</td>
</tr>
<tr>
<td>-38</td>
<td>1869</td>
<td>2</td>
<td>1551</td>
<td>42</td>
<td>1225</td>
<td>82</td>
<td>890</td>
<td>122</td>
<td>547</td>
</tr>
</tbody>
</table>
Although the LMT85-Q1 is very linear, its response does have a slight umbrella parabolic shape. This shape is very accurately reflected in Table 3. The Transfer Table can be calculated by using the parabolic equation (Equation 1).

\[ V_{\text{TEMP}} (\text{mV}) = 1324.0 \text{mV} \cdot \left[ 8.194 \frac{\text{mV}}{\text{°C}} (T - 30^\circ \text{C}) \right] + 0.00262 \frac{\text{mV}}{\text{°C}^2} (T - 30^\circ \text{C})^2 \]  

(1)

The parabolic equation is an approximation of the transfer table and the accuracy of the equation degrades slightly at the temperature range extremes. Equation 1 can be solved for T resulting in:

\[ T = \frac{-8.194 - \sqrt{(-8.194)^2 + 4 \times 0.00262 \times (1324 - V_{\text{TEMP}} (\text{mV}))}}{2 \times -0.00262} + 30 \]  

(2)

For an even less accurate linear transfer function approximation, a line can easily be calculated over the desired temperature range using values from the Table and a two-point equation (Equation 3):

\[ V = V_1 \left( \frac{V_2 - V_1}{T_2 - T_1} \right) \times (T - T_1) \]  

where
- \( V \) is in mV,
- \( T \) is in °C,
- \( T_1 \) and \( V_1 \) are the coordinates of the lowest temperature,
- and \( T_2 \) and \( V_2 \) are the coordinates of the highest temperature.
For example, if the user wanted to resolve this equation, over a temperature range of 20°C to 50°C, they would proceed as follows:

\[
V \cdot 1405 \text{ mV} = \left( \frac{1159 \text{ mV} - 1405 \text{ mV}}{50^\circ \text{C} - 20^\circ \text{C}} \right) \times (T - 20^\circ \text{C})
\]

\[
V \cdot 1405 \text{ mV} = (-8.20 \text{ mV} / ^\circ \text{C}) \times (T - 20^\circ \text{C})
\]

\[
V = (-8.20 \text{ mV} / ^\circ \text{C}) \times T + 1569 \text{ mV}
\]

Using this method of linear approximation, the transfer function can be approximated for one or more temperature ranges of interest.

### 8.4 Device Functional Modes

#### 8.4.1 Mounting and Thermal Conductivity

The LMT85-Q1 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface.

To ensure good thermal conductivity, the backside of the LMT85 die is directly attached to the GND pin. The temperatures of the lands and traces to the other leads of the LMT85-Q1 will also affect the temperature reading.

Alternatively, the LMT85-Q1 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LMT85-Q1 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. If moisture creates a short circuit from the output to ground or \( V_{\text{DD}} \), the output from the LMT85 will not be correct. Printed-circuit coatings are often used to ensure that moisture cannot corrode the leads or circuit traces.

The thermal resistance junction to ambient (\( R_{\text{thJA}} \) or \( \theta_{\text{JA}} \)) is the parameter used to calculate the rise of a device junction temperature due to its power dissipation. Use Equation 7 to calculate the rise in the LMT85-Q1 die temperature:

\[
T_J = T_A + \theta_{\text{JA}} \left[ (V_{\text{DD}}I_S) + (V_{\text{DD}} - V_{\text{OUT}})I_L \right]
\]

where

- \( T_A \) is the ambient temperature,
- \( I_S \) is the supply current,
- \( I_L \) is the load current on the output,
- and \( V_O \) is the output voltage.

For example, in an application where \( T_A = 30^\circ \text{C} \), \( V_{\text{DD}} = 5 \text{ V} \), \( I_S = 5.4 \mu \text{A} \), \( V_{\text{OUT}} = 1324 \text{ mV} \), and \( I_L = 2 \mu \text{A} \), the junction temperature would be 30.014°C, showing a self-heating error of only 0.014°C. Because the junction temperature of the LMT85-Q1 is the actual temperature being measured, take care to minimize the load current that the LMT85-Q1 is required to drive. *Thermal Information* shows the thermal resistance of the LMT85-Q1.

#### 8.4.2 Output and Noise Considerations

A push-pull output gives the LMT85-Q1 the ability to sink and source significant current. This is beneficial when, for example, driving dynamic loads like an input stage on an analog-to-digital converter (ADC). In these applications the source current is required to quickly charge the input capacitor of the ADC. The LMT85-Q1 device is ideal for this and other applications which require strong source or sink current.

The LMT85-Q1 supply-noise gain (the ratio of the AC signal on \( V_{\text{OUT}} \) to the AC signal on \( V_{\text{DD}} \)) was measured during bench tests. The typical attenuation is shown in Figure 8 found in the *Typical Characteristics*. A load capacitor on the output can help to filter noise.

For operation in very noisy environments, some bypass capacitance should be present on the supply within approximately 5 centimeters of the LMT85-Q1.
Device Functional Modes (continued)

8.4.3 Capacitive Loads

The LMT85-Q1 handles capacitive loading well. In an extremely noisy environment, or when driving a switched sampling input on an ADC, it may be necessary to add some filtering to minimize noise coupling. Without any precautions, the LMT85-Q1 can drive a capacitive load less than or equal to 1100 pF as shown in Figure 10. For capacitive loads greater than 1100 pF, a series resistor may be required on the output, as shown in Figure 11.

---

**Figure 10.** LMT85-Q1 No Decoupling Required for Capacitive Loads Less Than 1100 pF

**Figure 11.** LMT85-Q1 with Series Resistor for Capacitive Loading Greater Than 1100 pF

---

Table 4. Recommended Series Resistor Values

<table>
<thead>
<tr>
<th>C_{LOAD}</th>
<th>MINIMUM R_S</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.1 nF to 99 nF</td>
<td>3 kΩ</td>
</tr>
<tr>
<td>100 nF to 999 nF</td>
<td>1.5 kΩ</td>
</tr>
<tr>
<td>1 μF</td>
<td>800 Ω</td>
</tr>
</tbody>
</table>

8.4.4 Output Voltage Shift

The LMT85-Q1 is very linear over temperature and supply voltage range. Due to the intrinsic behavior of an NMOS/PMOS rail-to-rail buffer, a slight shift in the output can occur when the supply voltage is ramped over the operating range of the device. The location of the shift is determined by the relative levels of V_{DD} and V_{OUT}. The shift typically occurs when V_{DD} - V_{OUT} = 1 V.

This slight shift (a few millivolts) takes place over a wide change (approximately 200 mV) in V_{DD} or V_{OUT}. Because the shift takes place over a wide temperature change of 5°C to 20°C, V_{OUT} is always monotonic. The accuracy specifications in the Accuracy Characteristics table already include this possible shift.
9 Application and Implementation

NOTE

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information

The LMT85-Q1 features make it suitable for many general temperature-sensing applications. It can operate down to 1.8-V supply with 5.4-µA power consumption, making it ideal for battery powered devices.

9.2 Typical Applications

9.2.1 Connection to an ADC

![Simplified Input Circuit of SAR Analog-to-Digital Converter](image)

**Figure 12. Suggested Connection to a Sampling Analog-to-Digital Converter Input Stage**

9.2.1.1 Design Requirements

Most CMOS ADCs found in microcontrollers and ASICs have a sampled data comparator input structure. When the ADC charges the sampling cap, it requires instantaneous charge from the output of the analog source such as the LMT85 temperature sensor and many op amps. This requirement is easily accommodated by the addition of a capacitor (C_FILTER).

9.2.1.2 Detailed Design Procedure

The size of C_FILTER depends on the size of the sampling capacitor and the sampling frequency. Because not all ADCs have identical input stages, the charge requirements will vary. This general ADC application is shown as an example only.

9.2.1.3 Application Curve

![Analog Output Transfer Function Graph](image)

**Figure 13. Analog Output Transfer Function**
Typical Applications (continued)

9.2.2 Conserving Power Dissipation With Shutdown

![Simple Shutdown Connection of the LMT85-Q1](image)

Figure 14. Simple Shutdown Connection of the LMT85-Q1

9.2.2.1 Design Requirements

Because the power consumption of the LMT85-Q1 is less than 9 µA, it can simply be powered directly from any logic gate output and therefore not require a specific shutdown pin. The device can even be powered directly from a micro controller GPIO. In this way, it can easily be turned off for cases such as battery-powered systems where power savings are critical.

9.2.2.2 Detailed Design Procedure

Simply connect the \(V_{DD}\) pin of the LMT85-Q1 directly to the logic shutdown signal from a microcontroller.

9.2.2.3 Application Curves

![Output Turnon Response Time Without a Capacitive Load and \(V_{DD}\) = 3.3 V](image)

Figure 15. Output Turnon Response Time Without a Capacitive Load and \(V_{DD}\) = 3.3 V

![Output Turnon Response Time Without a Capacitive Load and \(V_{DD}\) = 5 V](image)

Figure 16. Output Turnon Response Time Without a Capacitive Load and \(V_{DD}\) = 5 V

![Output Turnon Response Time With 1.1-nF Capacitive Load and \(V_{DD}\) = 3.3 V](image)

Figure 17. Output Turnon Response Time With 1.1-nF Capacitive Load and \(V_{DD}\) = 3.3 V

![Output Turnon Response Time With 1.1-nF Capacitive Load and \(V_{DD}\) = 5 V](image)

Figure 18. Output Turnon Response Time With 1.1-nF Capacitive Load and \(V_{DD}\) = 5 V

10 Power Supply Recommendations

The low supply current and supply range (1.8 V to 5.5 V) of the LMT85-Q1 allow the device to easily be powered from many sources. Power supply bypassing is optional and is mainly dependent on the noise on the power supply used. In noisy systems it may be necessary to add bypass capacitors to lower the noise that is coupled to the output of the LMT85-Q1.
11 Layout

11.1 Layout Guidelines
The LMT85-Q1 is extremely simple to layout. If a power-supply bypass capacitor is used, it should be connected as shown in the Layout Example.

11.2 Layout Example

- VIA to ground plane
- VIA to power plane

Figure 19. SC70 Package Recommended Layout
12 Device and Documentation Support

12.1 Receiving Notification of Documentation Updates
To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on Alert me to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

12.2 Community Resources
The following links connect to TI community resources. Linked contents are provided “AS IS” by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI’s views; see TI’s Terms of Use.

TI E2E™ Online Community TI’s Engineer-to-Engineer (E2E) Community. Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.
Design Support TI’s Design Support Quickly find helpful E2E forums along with design support tools and contact information for technical support.

12.3 Trademarks
E2E is a trademark of Texas Instruments. All other trademarks are the property of their respective owners.

12.4 Electrostatic Discharge Caution
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

12.5 Glossary
SLYZ022 — TI Glossary. This glossary lists and explains terms, acronyms, and definitions.

13 Mechanical, Packaging, and Orderable Information
The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
## PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMT85QDCKRQ1</td>
<td>ACTIVE</td>
<td>SC70</td>
<td>DCK</td>
<td>5</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-50 to 150</td>
<td>BRA</td>
</tr>
<tr>
<td>LMT85QDCKTQ1</td>
<td>ACTIVE</td>
<td>SC70</td>
<td>DCK</td>
<td>5</td>
<td>250</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-50 to 150</td>
<td>BRA</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

---

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OTHER QUALIFIED VERSIONS OF LMT85-Q1:

- Catalog: LMT85

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product
### TAPE AND REEL INFORMATION

#### TAPE DIMENSIONS

- **K0**: Dimension designed to accommodate the component width
- **B0**: Dimension designed to accommodate the component length
- **W**: Overall width of the carrier tape
- **P1**: Pitch between successive cavity centers

#### PACKAGE MATERIALS INFORMATION

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
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</thead>
<tbody>
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<td>250</td>
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<td>2.45</td>
<td>1.2</td>
<td>4.0</td>
<td>8.0</td>
<td>Q3</td>
</tr>
</tbody>
</table>

*All dimensions are nominal.*
## TAPE AND REEL BOX DIMENSIONS

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
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</thead>
<tbody>
<tr>
<td>LMT85QDCKRQ1</td>
<td>SC70</td>
<td>DCK</td>
<td>5</td>
<td>3000</td>
<td>210.0</td>
<td>185.0</td>
<td>35.0</td>
</tr>
<tr>
<td>LMT85QDCKTQ1</td>
<td>SC70</td>
<td>DCK</td>
<td>5</td>
<td>250</td>
<td>210.0</td>
<td>185.0</td>
<td>35.0</td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
D. Falls within JEDEC MO-203 variation AA.
LAND PATTERN DATA

DCK (R-PDSO-G5) PLASTIC SMALL OUTLINE

Example Board Layout

Stencil Openings
Based on a stencil thickness
of 0.127mm (.005 inch).

6x0.35
0.85

2.2

2x0.65
2x1.30

Solder Mask Opening

Pd Geometry

NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
D. Publication IPC-7351 is recommended for alternate designs.
E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7526 for other stencil recommendations.

4210556-2/C 07/11
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