Two independent VCOs in a 16-Pin Package

Output Frequency Set by Single External Component:
  Capacitor for Fixed- or Variable-Frequency Operation

Separate Supply Voltage Pins for Isolation of Frequency Control Inputs and Oscillators from Output Circuitry

Highly Stable Operation over Specified Temperature and/or Supply Voltage Ranges

Typical \( f_{\text{max}} \) .......... 86 MHz
Typical Power Dissipation .......... 526 mW

Frequency Spectrum ........ 1 Hz to 60 MHz

description

The 'S124 features two independent voltage-controlled oscillators (VCO) in a single monolithic chip. The output frequency of each VCO is established by an external capacitor in combination with two voltage-sensitive inputs, one for frequency range and one for frequency control. These inputs can be used to vary the output frequency as shown under typical characteristics. These highly stable oscillators can be set to operate at any frequency typically between 0.12 hertz and 65 megahertz.\(^1\)

While the enable input is low, the output is enabled. While the enable input is high, the output is high.

These devices can operate from a single 5-volt supply. However, one set of supply-voltage and ground pins (\( V_{\text{CC}} \) and \( \text{GND} \)) is provided for the enable, synchronization-gating, and output sections, and a separate set (\( \Theta V_{\text{CC}} \) and \( \Theta \text{GND} \)) is provided for the oscillator and associated frequency-control circuits so that effective isolation can be accomplished in the system.

The enable input of these devices starts or stops the output pulses when it is low or high, respectively. The internal oscillator of the 'S124 is started and stopped by the enable input. The enable input is one standard load; it and the buffered output operate at standard Schottky-clamped TTL levels.

The pulse synchronization-gating section ensures that the first output pulse is neither clipped nor extended. Duty cycle of the square-wave output is fixed at approximately 50 percent.

The SN54S124 is characterized for operation over the full military temperature range of \(-55^\circ\text{C} \) to \(125^\circ\text{C} \); the SN74S124 is characterized for operation from \(0^\circ\text{C} \) to \(70^\circ\text{C} \).

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\(^1\) Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.
SN54S124, SN74S124
DUAL VOLTAGE-CONTROLLED OSCILLATORS

logic symbol

This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.
Pin numbers shown are for D, J, N, and W packages.

schematics of inputs and outputs

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, Vcc (See Notes 1 and 2) .................................................. 7V
Input voltage ......................................................................................... 5.5 V
Operating free-air temperature range: SN54S124 .................................. -65°C to 125°C
SN74S124 ......................................................................................... 0°C to 70°C
Storage temperature range ................................................................... -65°C to 150°C

NOTES: 1. Voltage values are with respect to the appropriate ground terminal.
2. Throughout this data sheet, the symbol Vcc is used for the voltage applied to both the Vcc and \( V_{cc} \) terminals, unless otherwise noted.
## Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>SN54S124</th>
<th>SN74S124</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply voltage, ( V_{CC} ) (see Note 1)</td>
<td>4.5</td>
<td>5</td>
<td>5.5</td>
</tr>
<tr>
<td>Input voltage at frequency control or range input, ( V_{IH(freq)} ) or ( V_{IL(freq)} )</td>
<td>4.75</td>
<td>5</td>
<td>6.25</td>
</tr>
<tr>
<td>High-level output current, ( I_{OH} )</td>
<td>1</td>
<td>1</td>
<td>5</td>
</tr>
<tr>
<td>Low-level output current, ( I_{OL} )</td>
<td>20</td>
<td>20</td>
<td></td>
</tr>
<tr>
<td>Output frequency (enabled), ( f_o )</td>
<td>1</td>
<td>1</td>
<td>60</td>
</tr>
<tr>
<td>Operating free-air temperature, ( T_A )</td>
<td>-55</td>
<td>125</td>
<td>0</td>
</tr>
</tbody>
</table>

## Electrical Characteristics over Recommended Operating Free-Air Temperature Range (unless otherwise noted)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions¹</th>
<th>MIN</th>
<th>TYP ²</th>
<th>MAX</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{IH} ) High-level input voltage at enable</td>
<td>( V_{CC} - 0.8 )</td>
<td>2</td>
<td>V</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>( V_{IL} ) Low-level input voltage at enable</td>
<td>( V_{CC} - 0.8 )</td>
<td>2</td>
<td>V</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>( V_{IH} ) Input clamp voltage at enable</td>
<td>( V_{CC} - 2 )</td>
<td>2.6</td>
<td>V</td>
<td>3.4</td>
<td>V</td>
</tr>
<tr>
<td>( I_{OH} ) High-level output voltage</td>
<td>( V_{CC} - 2 )</td>
<td>2.7</td>
<td>V</td>
<td>3.4</td>
<td>V</td>
</tr>
<tr>
<td>( V_{OL} ) Low-level output voltage</td>
<td>( V_{CC} - 0.8 )</td>
<td>0.5</td>
<td>V</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>( I_{I} ) Input current</td>
<td>( V_{CC} - MAX )</td>
<td>10</td>
<td>50</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>( I_{II} ) Input current at maximum input voltage</td>
<td>( V_{CC} - MAX )</td>
<td>1</td>
<td>10</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>( I_{IL} ) Low-level input current</td>
<td>( V_{CC} - MAX )</td>
<td>1</td>
<td>10</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>( I_{OS} ) Short-circuit output current</td>
<td>( V_{CC} - MAX )</td>
<td>1</td>
<td>10</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>( I_{CC} ) Supply current, total into ( V_{CC} ) and ( V_{CC} )</td>
<td>( V_{CC} - MAX ), See Note 3</td>
<td>105</td>
<td>150</td>
<td></td>
<td>mA</td>
</tr>
</tbody>
</table>

¹For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.
²All typical values are at \( V_{CC} = 5 \) V, \( T_A = 25^\circ C \).
³Not more than one output should be shorted at a time and duration of the short-circuit should not exceed one second.

## Switching Characteristics, \( V_{CC} = 5 \) V, \( R_L = 280 \) Ω, \( C_L = 15 \) nF, \( T_A = 25^\circ C \) (see note 4)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>MIN</th>
<th>TYP ²</th>
<th>MAX</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>( f_o ) Output frequency</td>
<td>( f_{0(freq)} ) = 4 V, ( f_{0(freq)} ) = 1 V</td>
<td>60</td>
<td>85</td>
<td></td>
<td>MHz</td>
</tr>
<tr>
<td>Output duty cycle</td>
<td>( f_{0(freq)} ) = 1 V, ( f_{0(freq)} ) = 5 V</td>
<td>25</td>
<td>40</td>
<td></td>
<td></td>
</tr>
<tr>
<td>( T_{PHL} ) Propagation delay time, high-to-low-level output from enable</td>
<td>( f_o = 1 ) Hz to 20 MHz</td>
<td>1.4</td>
<td></td>
<td></td>
<td>μs</td>
</tr>
<tr>
<td></td>
<td>( f_o &gt; 20 ) MHz</td>
<td>70</td>
<td>70</td>
<td></td>
<td>ns</td>
</tr>
</tbody>
</table>

¹For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.
²All typical values are at \( V_{CC} = 5 \) V, \( T_A = 25^\circ C \).
³Not more than one output should be shorted at a time and duration of the short-circuit should not exceed one second.

## Notes

3. \( I_{CC} \) is measured with the outputs disabled and open.
4. Load circuits and voltage waveforms are shown in Section 1.
TYPICAL CHARACTERISTICS

**BASE OUTPUT FREQUENCY vs EXTERNAL CAPACITANCE**

\[ f_{o(bas)} = f_{o} \times f_{o(bas)} \]

**NORMALIZED OUTPUT FREQUENCY vs INPUT VOLTAGE**

\[ V_{CC} = 5 \text{ V} \]
\[ V_{I(freq)} = V_{I(rng)} = 2 \text{ V} \]
\[ T_A = 25^\circ \text{C} \]

**FIGURE 1**

![Base Output Frequency vs External Capacitance Graph](image1)

![Normalized Output Frequency vs Input Voltage Graph](image2)

**FIGURE 2**
## PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>PackageQty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>SN54S124J</td>
<td>ACTIVE</td>
<td>CDIP</td>
<td>J</td>
<td>16</td>
<td>1</td>
<td>TBD</td>
<td>A42</td>
<td>N / A for Pkg Type</td>
<td>-55 to 125</td>
<td>SN54S124J</td>
<td>Samples</td>
</tr>
<tr>
<td>SN74S124D</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>16</td>
<td>40</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>S124</td>
<td>Samples</td>
</tr>
<tr>
<td>SN74S124N</td>
<td>ACTIVE</td>
<td>PDIP</td>
<td>N</td>
<td>16</td>
<td>25</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU NIPDAU</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>SN74S124N</td>
<td>Samples</td>
</tr>
<tr>
<td>SN74S124NE4</td>
<td>ACTIVE</td>
<td>PDIP</td>
<td>N</td>
<td>16</td>
<td>25</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU NIPDAU</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>SN74S124N</td>
<td>Samples</td>
</tr>
<tr>
<td>SNJ54S124J</td>
<td>ACTIVE</td>
<td>CDIP</td>
<td>J</td>
<td>16</td>
<td>1</td>
<td>TBD</td>
<td>A42</td>
<td>N / A for Pkg Type</td>
<td>-55 to 125</td>
<td>SNJ54S124J</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:

**ACTIVE**: Product device recommended for new designs.

**LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substances do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) **Lead/Ball Finish** - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.
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OTHER QUALIFIED VERSIONS OF SN54S124, SN74S124:
- Catalog: SN74S124
- Military: SN54S124

NOTE: Qualified Version Definitions:
- Catalog - TI's standard catalog product
- Military - QML certified for Military and Defense Applications
NOTES:
A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.

⚠️ Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0.15) each side.
⚠️ Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0.43) each side.

E. Reference JEDEC MS-012 variation AC.
NOTES:  
A. All linear dimensions are in millimeters. 
B. This drawing is subject to change without notice. 
C. Publication IPC-7351 is recommended for alternate designs. 
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations. 
E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.
### J (R—GDIP—T**)  
14 LEADS SHOWN

#### CERAMIC DUAL IN-LINE PACKAGE

**DIMENSIONS:**  

<table>
<thead>
<tr>
<th>PINS **</th>
<th>14</th>
<th>16</th>
<th>18</th>
<th>20</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>A</strong></td>
<td>0.300 (7,62) BSC</td>
<td>0.300 (7,62) BSC</td>
<td>0.300 (7,62) BSC</td>
<td>0.300 (7,62) BSC</td>
</tr>
<tr>
<td><strong>B MAX</strong></td>
<td>0.785 (19,94)</td>
<td>.840 (21,34)</td>
<td>.960 (24,38)</td>
<td>1.060 (26,92)</td>
</tr>
<tr>
<td><strong>B MIN</strong></td>
<td>——</td>
<td>——</td>
<td>——</td>
<td>——</td>
</tr>
<tr>
<td><strong>C MAX</strong></td>
<td>0.300 (7,62)</td>
<td>0.300 (7,62)</td>
<td>0.310 (7,87)</td>
<td>0.300 (7,62)</td>
</tr>
<tr>
<td><strong>C MIN</strong></td>
<td>0.245 (6,22)</td>
<td>0.245 (6,22)</td>
<td>0.220 (5,59)</td>
<td>0.245 (6,22)</td>
</tr>
</tbody>
</table>

### NOTES:  

A. All linear dimensions are in inches (millimeters).  
B. This drawing is subject to change without notice.  
C. This package is hermetically sealed with a ceramic lid using glass frit.  
D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.  
E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.
PLASTIC DUAL-IN-LINE PACKAGE

NOTES:
A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
\[\text{falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).}\]
\[\text{The 20 pin end lead shoulder width is a vendor option, either half or full width.}\]
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