1 Introduction

1.1 Features

• Power Management Core
  – Dual Input Power Path
  – Switch Mode Charger
  – Integrated Charge Current Sense FET
  – Automatic Battery Supplement Mode
  – 2 Boost Converters
    • 1 Boost supports 2 strings of up to 6 LEDs with Internal and External Dimming Control
    • 1 Boost supports 1 string of 6 LEDs
    • Boost Converters can also be used in Constant Voltage Mode
  – LED Matrix Controller
  – RGB Controller
  – I²C™ Interface to Device for Low Latency Communication

1.2 Applications

• Portable Applications

1.3 Description

The TPS658310 Power Management Unit is a broad use, multi-channel device, for portable applications. The device consists of an Integrated Power Path Management and Switch Mode Li-Ion Battery Charger that provides system power from a regulated wall adapter or a USB port. It also handles lighting management with integrated Backlight Boosts, LED Matrix Controller for keypad, Camera Flash LED Controller, Current Source and RGB channels.

To request a full data sheet, please send an email to:

pmu_contact@list.ti.com

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I²C is a trademark of NPX Corporation.
1.4 Block Diagram

The simplified TPS658310 system diagram is shown in Figure 1-1.

![Simplified System Diagram](image-url)
2 Application Schematic

NOTE: Component values shown are the minimum required.

Figure 2-1. Application Schematic
## PACKAGE INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish (6)</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS658310YFFR</td>
<td>NRND</td>
<td>DSBGA</td>
<td>YFF</td>
<td>49</td>
<td>1500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SNAGCU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>TPS658310</td>
<td></td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:

**ACTIVE**: Product device recommended for new designs.

**LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

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(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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YFF (R-XBGA-N49)  DIE-SIZE BALL GRID ARRAY

D: Max = 3.416 mm, Min = 3.19 mm, Min = 3.356 mm
E: Max = 3.19 mm, Min = 3.13 mm

NOTES:  
A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.  
B. This drawing is subject to change without notice.  
C. NanoFree™ package configuration.

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