TPS92610-Q1 Automotive Single-Channel Linear LED Driver

1 Features

- Qualified for Automotive Applications
- AEC-Q100 Qualified With the Following Results:
  - Temperature Grade 1: –40°C to 125°C
  - Ambient Operating Temperature Range
  - Device HBM ESD Classification Level H2
  - Device CDM ESD Classification Level C3B
- Functional safety capable
  - Documentation available to aid functional safety system design
- Single-Channel Constant-Current LED Driver With PWM Dimming
- Wide Input-Voltage Range: 4.5 V–40 V
- Constant Output Current, Adjustable by Sense Resistor
- Precision Current Regulation, Tolerance ±4.6% Across Junction Temperature –40°C to 150°C
- Maximum Current: 450 mA
- Heat Sharing With External Resistor
- Low Dropout Voltage (Sense–Resistor Voltage Drop Included)
  - Maximum Dropout: 150 mV at 10 mA
  - Maximum Dropout: 400 mV at 70 mA
  - Maximum Dropout: 700 mV at 150 mA
  - Maximum Dropout: 1.3 V at 300 mA
- Diagnostics and Protection
  - Single-LED Short-Circuit Detection With Auto-Recovery
  - LED Open-Circuit and Short-Circuit Detection With Auto-Recovery
  - Diagnostic-Enable With Adjustable Threshold for Low-Dropout Operation
  - Fault Bus up to 15 Devices, Configurable As Either One-Fails–All-Fail or Only-Failed-Channel-Off
  - Low Quiescent Current and Fault-Mode Current (<250 µA per Device)
- Operating Junction Temperature Range: –40°C to 150°C

2 Applications

- Automotive Convenience Lighting: Dome Light, Door Handles, Reading Lamp, and Miscellaneous Lamps
- Automotive Rear Lamp, Center High-Mounted Stop Lamp, Side Markers, Blind-Spot Detection Indicator, Charging Inlet Indicator
- General-Purpose LED Driver Application

3 Description

With LEDs being widely used in automotive applications, simple LED drivers are more and more popular. Compared to discrete solutions, a low-cost monolithic solution lowers system level component counts and significantly improves current accuracy and reliability.

The TPS92610-Q1 device is a simple single-channel high-side LED driver operating from an automotive car battery. It is a simple and elegant solution to deliver constant current for a single LED string with full LED diagnostics. Its one-fails–all-fail feature is able to work together with other LED drivers, such as the TPS9261x-Q1, TPS9263x-Q1, and TPS9283x-Q1 devices, to address different requirements.

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS92610-Q1</td>
<td>HTSSOP (14)</td>
<td>5 mm × 4.4 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Typical Application Diagram

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4 Revision History

Changes from Revision A (December 2017) to Revision B Page
• Added functional safety capable link to the Features section ................................................................. 1

Changes from Original (November 2017) to Revision A Page
• Changed data sheet from ADVANCE INFORMATION to PRODUCTION DATA ........................................ 1
5 Pin Configuration and Functions

PWP PowerPAD™ Package
14-Pin HTSSOP With Exposed Termal Pad
Top View

NC – No internal connection

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>DIAGEN</td>
<td>2</td>
<td>Diagnostics enable, to avoid false open-circuit diagnostics during low-voltage operation</td>
</tr>
<tr>
<td>EN</td>
<td>1</td>
<td>Device enable</td>
</tr>
<tr>
<td>FAULT</td>
<td>6</td>
<td>One-fails–all-fail fault bus</td>
</tr>
<tr>
<td>GND</td>
<td>7</td>
<td>Ground</td>
</tr>
<tr>
<td>IN</td>
<td>13</td>
<td>Current input</td>
</tr>
<tr>
<td>NC</td>
<td>3, 5, 10, 12</td>
<td>Not connected</td>
</tr>
<tr>
<td>OUT</td>
<td>11</td>
<td>Constant-current output</td>
</tr>
<tr>
<td>PWM</td>
<td>4</td>
<td>PWM input</td>
</tr>
<tr>
<td>SSH</td>
<td>9</td>
<td>Single-LED short high-side reference</td>
</tr>
<tr>
<td>SSL</td>
<td>8</td>
<td>Single-LED short low-side reference</td>
</tr>
<tr>
<td>SUPPLY</td>
<td>14</td>
<td>Device supply voltage</td>
</tr>
</tbody>
</table>

6 Specifications

6.1 Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Part Description</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>High-voltage input</td>
<td>-0.3</td>
<td>45</td>
<td>V</td>
</tr>
<tr>
<td>High-voltage output</td>
<td>-0.3</td>
<td>45</td>
<td>V</td>
</tr>
<tr>
<td>Fault bus</td>
<td>-0.3</td>
<td>22</td>
<td>V</td>
</tr>
<tr>
<td>IN to OUT</td>
<td>-0.3</td>
<td>45</td>
<td>V</td>
</tr>
<tr>
<td>SUPPLY to IN</td>
<td>-0.3</td>
<td>1</td>
<td>V</td>
</tr>
<tr>
<td>Operating junction temperature, $T_J$</td>
<td>-40</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature, $T_{stg}$</td>
<td>-40</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
6.2 ESD Ratings

<table>
<thead>
<tr>
<th>TPS92610-Q1</th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Human-body model (HBM), per AEC Q100-002 (1)</td>
<td>±2000</td>
<td>V</td>
</tr>
<tr>
<td>Charged-device model (CDM), per AEC Q100-011</td>
<td>±500</td>
<td>V</td>
</tr>
<tr>
<td>Corner pins (1, 7, 8, and 14)</td>
<td>±750</td>
<td>V</td>
</tr>
</tbody>
</table>

(1) AEC Q100-002 indicates that HBM stressing shall be in accordance with the ANSI/ESDA/JEDEC JS-001 specification.

6.3 Recommended Operating Conditions

over operating ambient temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>SUPPLY</td>
<td>Device supply voltage</td>
<td>4.5</td>
<td>40</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>IN</td>
<td>Sense voltage</td>
<td>4.4</td>
<td>40</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>PWM</td>
<td>PWM input</td>
<td>0</td>
<td>40</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>DIAGEN</td>
<td>Diagnostics enable pin</td>
<td>0</td>
<td>40</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>OUT</td>
<td>Driver output</td>
<td>0</td>
<td>40</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>SSH</td>
<td>Single LED short high-side reference</td>
<td>0</td>
<td>5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>SSL</td>
<td>Single LED short low-side reference</td>
<td>0</td>
<td>5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>EN</td>
<td>Device enable</td>
<td>0</td>
<td>40</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>FAULT</td>
<td>Fault bus</td>
<td>0</td>
<td>7</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>T_A</td>
<td>Operating ambient temperature</td>
<td>–40</td>
<td>125</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

6.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC (1)</th>
<th>TPS92610-Q1</th>
</tr>
</thead>
<tbody>
<tr>
<td>R_θJA (PWP (HTSSOP))</td>
<td>52.4 °C/W</td>
</tr>
<tr>
<td>R_θJC(top)</td>
<td>43.5 °C/W</td>
</tr>
<tr>
<td>R_θJB</td>
<td>22  °C/W</td>
</tr>
<tr>
<td>ψJT</td>
<td>1.6  °C/W</td>
</tr>
<tr>
<td>ψJB</td>
<td>22.3 °C/W</td>
</tr>
<tr>
<td>R_θJC(bot)</td>
<td>6.5  °C/W</td>
</tr>
</tbody>
</table>

(1) For more information about traditional and new thermal metrics, see Semiconductor and IC Package Thermal Metrics.

6.5 Electrical Characteristics

V_(SUPPLY) = 5 V – 40 V, T_J = –40°C–150°C unless otherwise noted

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>BIAS</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>V_(POR_rising)</td>
<td>Supply voltage POR rising threshold</td>
<td>3.2</td>
<td>4</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>V_(POR_falling)</td>
<td>Supply voltage POR falling threshold</td>
<td>2.2</td>
<td>3</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>I_(Shutdown)</td>
<td>Device shutdown current</td>
<td>EN = LOW</td>
<td>5</td>
<td>10</td>
<td>μA</td>
</tr>
<tr>
<td>I_(Quiescent)</td>
<td>Device quiescent current</td>
<td>PWM = HIGH, EN = HIGH</td>
<td>0.1</td>
<td>0.2</td>
<td>0.25</td>
</tr>
<tr>
<td>I_(FAULT)</td>
<td>Device current in fault mode</td>
<td>EN = HIGH, PWM = HIGH, FAULT externally pulled LOW</td>
<td>0.1</td>
<td>0.2</td>
<td>0.25</td>
</tr>
</tbody>
</table>
Electrical Characteristics (continued)

\( V_{\text{SUPPLY}} = 5 \, \text{V} \) – 40 V, \( T_J = -40^\circ\text{C} \) – 150°C unless otherwise noted

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{\text{IL(DIAGEN)}} )</td>
<td>Input logic-low voltage, DIAGEN</td>
<td>1.045</td>
<td>1.1</td>
<td>1.155</td>
<td>V</td>
</tr>
<tr>
<td>( V_{\text{IH(DIAGEN)}} )</td>
<td>Input logic-high voltage, DIAGEN</td>
<td>1.14</td>
<td>1.2</td>
<td>1.26</td>
<td>V</td>
</tr>
<tr>
<td>( V_{\text{IL(PWM)}} )</td>
<td>Input logic-low voltage, PWM</td>
<td>1.045</td>
<td>1.1</td>
<td>1.155</td>
<td>V</td>
</tr>
<tr>
<td>( V_{\text{IH(PWM)}} )</td>
<td>Input logic-high voltage, PWM</td>
<td>1.14</td>
<td>1.2</td>
<td>1.26</td>
<td>V</td>
</tr>
<tr>
<td>( V_{\text{IL(EN)}} )</td>
<td>Input logic-low voltage, EN</td>
<td>2</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>( V_{\text{IH(EN)}} )</td>
<td>Input logic-high voltage, EN</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( I_{\text{PD(EN)}} )</td>
<td>EN pin pulldown current ( V_{\text{EN}} = 12 , \text{V} )</td>
<td>1.5</td>
<td>3.3</td>
<td>4.5</td>
<td>µA</td>
</tr>
<tr>
<td>( I_{\text{(OUT)}} )</td>
<td>Device output-current range</td>
<td>100% duty-cycle</td>
<td>4</td>
<td>450</td>
<td>mA</td>
</tr>
<tr>
<td>( V_{\text{(CS_REG)}} )</td>
<td>Sense-resistor regulation voltage ( T_A = 25^\circ\text{C}, V_{\text{SUPPLY}} = 4.5 , \text{V} ) to 18 V</td>
<td>94</td>
<td>98</td>
<td>102</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( T_A = -40^\circ\text{C} ) to 125°C, ( V_{\text{SUPPLY}} = 4.5 , \text{V} ) to 18 V</td>
<td>93.5</td>
<td>98</td>
<td>102.5</td>
</tr>
<tr>
<td>( R_{\text{(SENS)}} )</td>
<td>Sense-resistor range</td>
<td></td>
<td>24.5</td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>( V_{\text{(DROPOUT)}} )</td>
<td>Voltage dropout from SUPPLY to OUT</td>
<td>( V_{\text{(CS_REG)}} ) voltage included, current setting = 10 mA</td>
<td>120</td>
<td>150</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{(CS_REG)}} ) voltage included, current setting = 70 mA</td>
<td>250</td>
<td>400</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{(CS_REG)}} ) voltage included, current setting = 150 mA</td>
<td>430</td>
<td>700</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( V_{\text{(CS_REG)}} ) voltage included, current setting = 300 mA</td>
<td>800</td>
<td>1300</td>
<td>mV</td>
</tr>
</tbody>
</table>

DIAGNOSTICS

| \( V_{\text{(OPEN_th_rising)}} \) | LED open rising threshold, \( V_{\text{(IN)}} \) – \( V_{\text{(OUT)}} \) | 70 | 100 | 135 | mV |
| \( V_{\text{(OPEN_th_falling)}} \) | LED open falling threshold, \( V_{\text{(IN)}} \) – \( V_{\text{(OUT)}} \) | 235 | 290 | 335 | mV |
| \( V_{\text{(SG_th_falling)}} \) | Channel output \( V_{\text{(OUT)}} \) short-to-ground falling threshold | 1.14 | 1.2 | 1.26 | V |
| \( V_{\text{(SG_th_rising)}} \) | Channel output \( V_{\text{(OUT)}} \) short-to-ground rising threshold | 0.82 | 0.865 | 0.91 | V |
| \( I_{\text{(retry)}} \) | Channel output retry current \( V_{\text{(OUT)}} = 0 \, \text{V} \) | 0.64 | 1.08 | 1.528 | mA |
| \( V_{\text{(SSH_th)}} \) | Single-LED short-detection high-side threshold \( V_{\text{(SSL)}} \) – \( V_{\text{(SSH)}} \) | 140 | 190 | 235 | mV |
| \( V_{\text{(SSL_th)}} \) | Single-LED short-detection low-side threshold | 0.8 | 0.86 | 0.91 | V |

FAULT

| \( V_{\text{IL(FAULT)}} \) | Logic-input low threshold | 0.7 | | | V |
| \( V_{\text{IH(FAULT)}} \) | Logic-input high threshold | 2 | | | V |
| \( V_{\text{OL(FAULT)}} \) | Logic-output low voltage With 500-µA external pullup | 0.4 | | | V |
| \( V_{\text{OH(FAULT)}} \) | Logic-output high voltage With 1-µA external pulldown, \( V_{\text{SUPPLY}} = 12 \, \text{V} \) | 5 | 7 | | V |
| \( I_{\text{(FAULT_pulldown)}} \) | FAULT internal pulldown current | 500 | 750 | 1000 | µA |
| \( I_{\text{(FAULT_pullup)}} \) | FAULT internal pullup current | 5 | 8 | 12 | µA |

THERMAL PROTECTION

| \( T_{\text{(TSD)}} \) | Thermal shutdown junction temperature threshold | 167 | 172 | 178 | °C |
| \( T_{\text{(TSD_HYS)}} \) | Thermal shutdown junction temperature hysteresis | 15 | | | °C |
### 6.6 Timing Requirements

<table>
<thead>
<tr>
<th>Parameter</th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$t_{(PWM_delay_rising)}$</td>
<td>10</td>
<td>17</td>
<td>25</td>
<td>µs</td>
</tr>
<tr>
<td>$t_{(PWM_delay_falling)}$</td>
<td>15</td>
<td>21</td>
<td>30</td>
<td>µs</td>
</tr>
<tr>
<td>$t_{(TSD_deg)}$</td>
<td></td>
<td></td>
<td>60</td>
<td>µs</td>
</tr>
<tr>
<td>$t_{(DEVICE_STARTUP)}$</td>
<td></td>
<td></td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>$t_{(OPEN_deg)}$</td>
<td>80</td>
<td>125</td>
<td>175</td>
<td>µs</td>
</tr>
<tr>
<td>$t_{(SG_deg)}$</td>
<td>80</td>
<td>125</td>
<td>175</td>
<td>µs</td>
</tr>
<tr>
<td>$t_{(SS_deg)}$</td>
<td>80</td>
<td>125</td>
<td>175</td>
<td>µs</td>
</tr>
<tr>
<td>$t_{(Recover_deg)}$</td>
<td>16</td>
<td></td>
<td></td>
<td>µs</td>
</tr>
</tbody>
</table>

**Figure 1. Output Timing Diagram**

![Output Timing Diagram](image_url)
6.7 Typical Characteristics

Figure 2. Output Current vs Supply Voltage

Figure 3. Output Current vs Current-Sense Resistor

Figure 4. Output Current vs Dropout Voltage

Figure 5. Output Current vs Temperature

Figure 6. Shutdown, Quiescent, and Fault Current vs Supply Voltage

Figure 7. PWM Output Duty Cycle vs Input Duty Cycle
Typical Characteristics (continued)

**Figure 8. PWM Dimming via External Input**
- Ch. 1 = V\(_{\text{OUT}}\)
- Ch. 2 = V\(_{\text{PWM}}\)
- Ch. 4 = I\(_{\text{OUT}}\)
- \(f_{\text{PWM}} = 200\) Hz
- Duty cycle = 50%

**Figure 9. PWM Dimming via Power Supply**
- Ch. 1 = V\(_{\text{SUPPLY}}\)
- Ch. 2 = V\(_{\text{OUT}}\)
- Ch. 3 = FAULT
- Ch. 4 = I\(_{\text{OUT}}\)
- \(f_{\text{PWM}} = 1000\) Hz
- Duty cycle = 30%
- SUPPLY dimming between 2.5 V and 12 V
- FAULT floating

**Figure 10. Transient Undervoltage**
- Ch. 1 = SUPPLY
- Ch. 2 = V\(_{\text{OUT}}\)
- Ch. 3 = FAULT
- Ch. 4 = I\(_{\text{OUT}}\)

**Figure 11. Transient Overvoltage**
- Ch. 1 = SUPPLY
- Ch. 2 = V\(_{\text{OUT}}\)
- Ch. 3 = FAULT
- Ch. 4 = I\(_{\text{OUT}}\)

**Figure 12. Jump Start**
- Ch. 1 = SUPPLY
- Ch. 2 = V\(_{\text{OUT}}\)
- Ch. 3 = FAULT
- Ch. 4 = I\(_{\text{OUT}}\)

**Figure 13. Superimposed Alternating Voltage, 15 Hz**
- Ch. 1 = SUPPLY
- Ch. 2 = V\(_{\text{OUT}}\)
- Ch. 3 = FAULT
- Ch. 4 = I\(_{\text{OUT}}\)
Typical Characteristics (continued)

Figure 14. Superimposed Alternating Voltage, 1 kHz

Figure 15. Slow Decrease, Quick Increase of Supply Voltage

Figure 16. Slow Decrease and Slow Increase of Supply Voltage

Figure 17. LED Open-Circuit Protection and Recovery

Figure 18. LED Short-Circuit Protection and Recovery

Figure 19. Single-LED-Short Protection and Recovery
7 Detailed Description

7.1 Overview
The TPS92610-Q1 device is one of a family of single-channel linear LED drivers. The family provides a simple solution for automotive LED applications. Different package options in the family provide a variety of current ranges and diagnostic options. The TPS92610-Q1 device in an HTSSOP-14 package supports LED open-circuit detection and short-to-ground detection. Unique single-LED-short detection in the TPS92610-Q1 device can help diagnose if one LED within a string is shorted. A one-fails–all-fail fault bus allows the TPS92610-Q1 device to be used together with the TPS9261x-Q1, TPS9263x-Q1, and TPS9283x-Q1 families.

The output current can be set by an external \( R_{\text{SNS}} \) resistor. Current flows from the supply through the \( R_{\text{SNS}} \) resistor into the internal current source and to the LEDs.

7.2 Functional Block Diagram

7.3 Feature Description

7.3.1 Device Bias

7.3.1.1 Power-On Reset (POR)
The TPS92610-Q1 device has an internal power-on-reset (POR) function. When power is applied to SUPPLY, the internal POR holds the device in the reset state until \( V_{\text{SUPPLY}} \) is above \( V_{\text{POR_rising}} \).

7.3.1.2 Low-Quiescent-Current Fault Mode
The TPS92610-Q1 device consumes minimal quiescent current when its \( \text{FAULT} \) pin is externally pulled LOW. At the same time, the device shuts down the output driver.
Feature Description (continued)

If device detects an internal fault, it pulls the FAULT output LOW with constant current to signal a fault alarm on the one-fails–all-fail fault bus.

7.3.2 Constant-Current Driver

The TPS92610-Q1 device has a high-side constant-current integrated driver. The device senses channel current with an external high-side current-sense resistor, \( R_{(SNS)} \). A current regulation loop drives an internal transistor and regulates the current-sense voltage at the current-sense resistor to \( V_{(CS\_REG)} \). When the output driver is in regulation, the output current can be set using the following equation.

\[
I_{(OUT)} = \frac{V_{(CS\_REG)}}{R_{(SNS)}},
\]

(1)

7.3.3 Device Enable

The TPS92610-Q1 device has an enable input, EN. When EN is low, the device is in sleep mode with ultralow quiescent current, \( I_{(Shutdown)} \). This low current helps to save system-level current consumption in applications where battery voltage directly connects to the device without high-side switches.

7.3.4 PWM Dimming

The TPS92610-Q1 device supports PWM output dimming via PWM input dimming and supply dimming.

The PWM input functions as an enable for the output current. When the PWM input is low, the device also disables the diagnostic features.

Supply dimming applies PWM dimming on the power input. For an accurate PWM threshold, TI recommends using a resistor divider on the PWM input to set the PWM threshold higher than \( V_{(POR\_rising)} \).

7.3.5 Diagnostics

The TPS92610-Q1 device provides advanced diagnostics and fault protection features for automotive exterior lighting systems. The device is able to detect and protect from LED string short-to-GND, LED string open-circuit, and single-LED-short scenarios. It also supports a one-fails–all-fail fault bus that could flexibly fit different legislative requirements.

7.3.5.1 DIAGEN

The TPS92610-Q1 device supports the DIAGEN pin with an accurate threshold to disable the open-circuit and single-LED-short diagnostic functions. With a resistor divider, the DIAGEN pin can be used to sense SUPPLY voltage with a resistor-programmable threshold. With the DIAGEN feature, the device is able to avoid false error reports due to low-dropout voltage and to drive maximum current in low-dropout mode when the input voltage is not high enough for current regulation.

When \( V_{(DIAGEN)} \) is higher than the threshold \( V_{(IH\_DIAGEN)} \), the device enables LED open-circuit and single-LED-short diagnostics. When \( V_{(DIAGEN)} \) is lower than the threshold \( V_{(IL\_DIAGEN)} \), the device disables LED-open-circuit and single-LED-short diagnostics.

7.3.5.2 Low-Dropout Mode

When the supply voltage drops, the TPS92610-Q1 device tries to regulate current by driving internal transistors in the linear region, also known as low-dropout mode, because the voltage across the sense resistor fails to reach the regulation target.

In low-dropout mode, the open-circuit diagnostic must be disabled. Otherwise, the device treats the low-dropout mode as an open-circuit fault. The DIAGEN pin is used to avoid false diagnostics on the output channel due to low supply voltage.

When the DIAGEN voltage is low, single-LED short- and open-circuit detection is ignored. When the DIAGEN voltage is high, single-LED short- and open-circuit detection return to normal operation.

In dropout mode, a diode in parallel with the sense resistor is recommended to clamp the voltage between SUPPLY and IN (across the sense resistor) in case of a large current pulse during recovery.
7.3.5.3 Open-Circuit Detection

The TPS92610-Q1 device has LED open-circuit detection. Open-circuit detection monitors the output voltage when the channel is in the ON state. Open-circuit detection is only enabled when DIAGEN is HIGH. A short-to-battery fault is also detected as an LED open-circuit fault.

The device monitors dropout-voltage differences between the IN and OUT pins when PWM is HIGH. The voltage difference \( V_{IN} - V_{OUT} \) is compared with the internal reference voltage \( V_{OPEN\_th\_rising} \) to detect an LED open-circuit failure. If \( V_{IN} - V_{OUT} \) falls below the \( V_{OPEN\_th\_rising} \) voltage longer than the deglitch time of \( t_{OPEN\_deg} \), the device asserts an open-circuit fault. Once an LED open-circuit failure is detected, the constant-current source pulls the fault bus down. During the deglitch time period, if \( V_{IN} - V_{OUT} \) rises above \( V_{OPEN\_th\_falling} \), the deglitch timer is reset.

When the device is in auto-retry, the device keeps the output ON to retry if the PWM input is HIGH; the device sources a small current \( I_{RETRY} \) from IN to OUT when PWM input is LOW. In either scenario, once a faulty channel recovers, the device resumes normal operation and releases the FAULT pulldown.

7.3.5.4 Short-to-GND Detection

The TPS92610-Q1 device has LED short-to-GND detection. Short-to-GND detection monitors the output voltage when the channel is in the ON state. Once a short-to-GND LED failure is detected, the device turns off the output channel and retries automatically, ignoring the PWM input. If the retry mechanism detects removal of the LED short-to-GND fault, the device resumes normal operation.

The device monitors the \( V_{OUT} \) voltage and compares it with the internal reference voltage to detect a short-to-GND failure. If \( V_{OUT} \) falls below \( V_{SG\_th\_rising} \) longer than the deglitch time of \( t_{SG\_deg} \), the device asserts the short-to-GND fault and pulls FAULT low. During the deglitching time period, if \( V_{OUT} \) rises above \( V_{SG\_th\_falling} \), the timer is reset.

Once the device has asserted a short-to-GND fault, the device turns OFF the output channel and retries automatically with a small current. When retrying, the device sources a small current \( I_{RETRY} \) from IN to OUT to pull up the LED loads continuously. Once auto-retry detects output voltage rising above \( V_{SG\_th\_falling} \), it clears the short-to-GND fault and resumes normal operation.

7.3.5.5 Single-LED-Short Detection

The TPS92610-Q1 device supports single-LED-short detection by using the SSH and SSL pins. In case there is no need of this feature, SSH and SSL must be tied together to a resistor divider to avoid false alarms as shown in Figure 21.

The TPS92610-Q1 device has integrated a precision comparator to monitor a single-LED-short failure. The comparator uses the bottom LED forward voltage \( V_{SSL} \) as a reference and monitors the string voltage \( V_{OUT} \) with resistor divider \( R_1 \) and \( R_2 \) at \( V_{SSH} \).

If a single-LED short is detected, the device turns off the output channel and retries with a small current \( I_{RETRY} \). Once the fault is removed, the device automatically resumes normal operation.
Feature Description (continued)

Use the following equation to calculate the ratio of R1 and R2.

\[
R_2 = (\text{No. of LEDs} - 1) \times R_1
\]

By using the resistor divider with values calculated in Equation 2, the voltages of SSH and SSL are then equal to the forward voltage of a single LED. With built-in comparators, the device can report failure if any of the LEDs is shorted within this string.

An internal resistor string on SSL and resistors R1 and R2 draw current from OUT. TI recommends total resistance of R1 and R2 greater than 100-kΩ, so the current has negligible effect on LED luminance.

Even within the same batch of LEDs, the LED forward voltage may vary from one to another. Taking account of forward voltage differences is necessary to avoid any false faults.

7.3.5.6 Overtemperature Protection

The TPS92610-Q1 device monitors device junction temperature. When the junction temperature reaches thermal shutdown threshold \( T_{(TSD)} \), the output shuts down. Once the junction temperature falls below \( T_{(TSD)} - T_{(TSD,HYS)} \), the device resumes normal operation. During overtemperature protection, the FAULT bus is pulled low.

7.3.6 FAULT Bus Output With One-Fails–All-Fail

The TPS92610-Q1 device has a FAULT bus for diagnostics output. In normal operation, FAULT is weakly pulled up by an internal pullup current source \( I_{(FAULT,pullup)} \) higher than \( V_{OH(FAULT)} \). If any fault scenario occurs, the FAULT bus is strongly pulled low by the internal pulldown current source \( I_{(FAULT,pulldown)} \). Once \( V_{(FAULT)} \) falls below \( V_{IL(FAULT)} \), all outputs shut down for protection. The faulty channel keeps retrying until the fault scenario is removed.

If FAULT is externally pulled up with a current larger than \( I_{(FAULT,pulldown)} \), the one-fails–all-fail function is disabled and only the faulty channel is turned off.

The FAULT bus is able to support up to 15 pieces of TPS9261x-Q1, TPS9263x-Q1, or TPS9283x-Q1 devices.
Feature Description (continued)

Table 1. Fault Table With DIAGEN = HIGH

<table>
<thead>
<tr>
<th>FAULT BUS STATUS</th>
<th>FAULT TYPE</th>
<th>DETECTION MECHANISM</th>
<th>CHANNEL STATE</th>
<th>DEGLITCH TIME</th>
<th>FAULT BUS</th>
<th>FAULT HANDLING ROUTINE</th>
<th>FAULT RECOVERY</th>
</tr>
</thead>
<tbody>
<tr>
<td>FAULT floating or externally pulled up</td>
<td>Open-circuit or short-to-supply</td>
<td>$V_{(IN)} - V_{(OUT)} &lt; V_{(OPEN_th_rising)}$</td>
<td>On</td>
<td>$I_{(OPEN_deg)}$</td>
<td>Constant-current pulldown</td>
<td>Device works normally with FAULT pin pulled low. Device sources $I_{(retry)}$ current when PWM is LOW. Device keeps output normal when PWM is HIGH.</td>
<td>Auto recover</td>
</tr>
<tr>
<td>Short-to-ground</td>
<td>$V_{(OUT)} &lt; V_{(SG_th_rising)}$</td>
<td>On</td>
<td>$I_{(SG_deg)}$</td>
<td>Constant-current pulldown</td>
<td>Device turns output off and retries with constant current $I_{(retry)}$, ignoring the PWM input.</td>
<td>Auto recover</td>
<td></td>
</tr>
<tr>
<td>Single-LED short</td>
<td>$V_{(SSL)} &lt; V_{(SSH)}$ or $V_{(SSL)} &lt; V_{(SSL_th)}$</td>
<td>On</td>
<td>$I_{(SS_deg)}$</td>
<td>Constant-current pulldown</td>
<td>Device turns output off and retries with constant current $I_{(retry)}$, ignoring the PWM input.</td>
<td>Auto recover</td>
<td></td>
</tr>
<tr>
<td>Overtemperature</td>
<td>$T_J &gt; T_{(TSD)}$</td>
<td>On or off</td>
<td>$I_{(TSD_deg)}$</td>
<td>Constant-current pulldown</td>
<td>Devices turns output off.</td>
<td>Auto recover</td>
<td></td>
</tr>
</tbody>
</table>

Externally pulled low | Device turns output off |

Table 2. Fault Table With DIAGEN = LOW

<table>
<thead>
<tr>
<th>FAULT BUS STATUS</th>
<th>FAULT TYPE</th>
<th>DETECTION MECHANISM</th>
<th>CHANNEL STATE</th>
<th>DEGLITCH TIME</th>
<th>FAULT BUS</th>
<th>FAULT HANDLING ROUTINE</th>
<th>FAULT RECOVERY</th>
</tr>
</thead>
<tbody>
<tr>
<td>FAULT floating or externally pulled up</td>
<td>Open-circuit or short-to-supply</td>
<td>Ignored</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Short-to-ground</td>
<td>$V_{(OUT)} &lt; V_{(SG_th_rising)}$</td>
<td>On</td>
<td>$I_{(SG_deg)}$</td>
<td>Constant-current pulldown</td>
<td>Device turns output off and retries with constant current $I_{(retry)}$, ignoring the PWM input.</td>
<td>Auto recover</td>
<td></td>
</tr>
<tr>
<td>Single-LED short</td>
<td>Ignored</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Overtemperature</td>
<td>$T_J &gt; T_{(TSD)}$</td>
<td>On or off</td>
<td>$I_{(TSD_deg)}$</td>
<td>Constant-current pulldown</td>
<td>Device turns output off.</td>
<td>Auto recover</td>
<td></td>
</tr>
</tbody>
</table>

Externally pulled low | Device turns output off |

7.4 Device Functional Modes

7.4.1 Undervoltage Lockout, $V_{(SUPPLY)} < V_{(POR_rising)}$

When the device is in undervoltage lockout mode, the TPS92610-Q1 device disables all functions until the supply rises above the UVLO-rising threshold.
Device Functional Modes (continued)

7.4.2 Normal Operation $V_{\text{SUPPLY}} \geq 4.5$ V

The device drives an LED string in normal operation. With enough voltage drop across SUPPLY and OUT, the device is able to drive the output in constant-current mode.

7.4.3 Low-Voltage Dropout

When the device drives an LED string in low-dropout mode, if the voltage drop is less than open-circuit detection threshold, the device may report a false open-circuit fault. Set the DIAGEN threshold higher than LED string voltage to avoid a false open-circuit detection.

7.4.4 Fault Mode

When the device detects an open circuit or a shorted LED, the device tries to pull down the FAULT pin with a constant current. If the FAULT bus is pulled down, the device switches to fault mode and consumes a fault current of $I_{\text{FAULT}}$. 
8 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information
In automotive lighting applications, thermal performance and LED diagnostics are always design challenges for linear LED drivers.

The TPS92610-Q1 device is capable of detecting LED open-circuit, LED short-circuit and single-LED short failures. To increase current-driving capability, the TPS92610-Q1 device supports using an external a parallel resistor to help dissipate heat as shown in the following application, Figure 25. This technique provides the low-cost solution of using external resistors to dissipate heat due to high input voltage, and still keeps high accuracy of the total current output. Note that the one-fails–all-fail feature is not supported by this topology.

8.2 Typical Application

8.2.1 Single-Channel LED Driver With Full Diagnostics
The TPS92610-Q1 device is a potential choice for LED driver for applications with diagnostics requirements. In many cases, single-LED short diagnostics are mandatory for applications such as sequential turn indicators.

![Typical Application Diagram](image)

8.2.1.1 Design Requirements
Input voltage ranges from 9 V to 16 V, LED maximum forward voltage \( V_{\text{fmax}} = 2.5 \text{ V} \), minimum forward voltage \( V_{\text{fmin}} = 1.9 \text{ V} \), current \( I_{\text{LED}} = 50 \text{ mA} \).

8.2.1.2 Detailed Design Procedure
Current setting by sense resistor is as described in Equation 1.

\[
R_{\text{(SNS)}} = \frac{R_{\text{CS-REG}}}{I_{\text{(LED)}}} = 1.96 \Omega
\]  

LED-string maximum forward voltage = \( 3 \times 2.5 \text{ V} = 7.5 \text{ V} \).
Typical Application (continued)

With 400-mV headroom reserved for the TPS92610-Q1 device between SUPPLY and OUT, the TPS92610-Q1 device must disable open-circuit detection when the supply voltage is below 7.9 V by using the DIAGEN feature.

\[ V_{IL(DIAG,\text{min})} = \frac{7.9 \times R_3}{R_3 + R_4} \]  

(4)

Set \( R_4 = 10 \, \text{k}\Omega \), \( R_3 = 65.6 \, \text{k}\Omega \).

The single-LED short-detection resistor ratio can be calculated as follows.

\[ \frac{R_2}{R_1} = 2 \]  

(5)

If \( R_1 = 50 \, \text{k}\Omega \), \( R_2 = 100 \, \text{k}\Omega \)

Total device power consumption at worst case is with 16-V input and LEDs at minimal forward voltage.

\[ P_{(\text{Max})} = (V_{(\text{SUPPLY})} - V_{(CS\_REG)} - V_{(OUT)}) \times I_{(\text{LED})} + V_{(\text{SUPPLY})} \times I_{(\text{Quiescent})} \]  

\[ = (16 - 3 \times 1.9 - 0.098) \times 0.05 + 16 \times 0.00025 = 0.5141 \, \text{W} \]  

(6)

8.2.1.3 Application Curve

![Figure 24. Output Current With PWM Input](image)
Typical Application (continued)

8.2.2 Single-Channel LED Driver With Heat Sharing

**Figure 25. Heat Sharing With a Parallel Resistor**

**8.2.2.1 Design Requirements**

Input voltage range is 9 V to 16 V, LED maximum forward voltage $V_{f_{\text{max}}}$ = 2.5 V, minimum forward voltage $V_{f_{\text{min}}}$ = 1.9 V, current $I_{(\text{LED})}$ = 200 mA.

**8.2.2.2 Detailed Design Procedure**

Using parallel resistors, thermal performance can be improved by balancing current between the TPS92610-Q1 device and the external resistors as follows. As the current-sense resistor controls the total LED string current, the LED string current $I_{(\text{LED})}$ is set by $V_{(CS\_REG)} / R_{(SNS)}$, while the TPS92610-Q1 current $I_{(\text{DRIVE})}$ and parallel resistor current $I_{(P)}$ combine to the total current.

Note that the parallel resistor path cannot be shut down by PWM or fault protection. If PWM or one-fails—all-fail feature is required, TI recommends an application circuit as described in Single-Channel LED Driver With Full Diagnostics.

In linear LED driver applications, the input voltage variation contributes to most of the thermal concerns. The resistor current, as indicated by Ohm’s law, depends on the voltage across the external resistors. The TPS92610-Q1 controls the driver current $I_{(\text{DRIVE})}$ to attain the desired total current. If $I_{(P)}$ increases, the TPS92610-Q1 device decreases $I_{(\text{DRIVE})}$ to compensate, and vice versa.

While in low-dropout mode, the voltage across the $R_{(P)}$ resistor may be close to zero, so that almost no current can flow through the external resistor $R_{(P)}$.

When the input voltage is high, the parallel-resistor current $I_{(P)}$ is proportional to the voltage across the parallel resistor $R_{(P)}$. The parallel resistor $R_{(P)}$ takes the majority of the total string current, generating maximum heat. The device must prevent current from draining out to ensure current regulation capability.

In this case, the parallel resistor value must be carefully calculated to ensure that 1) enough output current is achieved in low-dropout mode, 2) thermal dissipation for both the TPS92610-Q1 device and the resistor is within their thermal dissipation limits, and 3) device current in the high-voltage mode is above the minimal output-current requirement.

Current setting by sense resistor is as described in Equation 7.
LED-string maximum forward voltage = 3 × 2.5 V = 7.5 V.

Parallel resistor $R_{(P)}$ is recommended to consume 50% of the total current at maximum supply voltage.

$$R_{(P)} = \frac{V_{(SUPPLY)} - V_{(CS _ {REG})} - V_{(OUT)}}{0.5 \times i_{(LED)}} = \frac{16 - 3 \times 1.9 - 0.098}{0.5 \times 0.2} \approx 100\Omega$$

Total device power consumption is maximum at 16 V input and LED minimal forward voltage.

$$P_{(DEV _ {MAX})} = (V_{(SUPPLY)} - V_{(CS _ {REG})} - V_{(OUT)}) \times \left( i_{(LED)} - \frac{V_{(SUPPLY)} - V_{(CS _ {REG})} - V_{(OUT)}}{R_{(P)}} \right) + V_{(SUPPLY)} \times i_{(Quiescent)}$$

$$= (16 - 3 \times 1.9 - 0.098) \times 0.1 + 16 \times 0.00025 = 1.0242\text{ W}$$

Resistor $R_{(P)}$ maximum power consumption is at 16-V input.

$$P_{(RP _ {MAX})} = \left( \frac{V_{(SUPPLY)} - V_{(CS _ {REG})} - V_{(OUT)}}{R_{(P)}} \right)^2 = \frac{(16 - 3 \times 1.9 - 0.098)^2}{100} = 1.04\text{ W}$$

Users must consider the maximum power of both of the device and the parallel resistor.
Typical Application (continued)

8.2.2.3 Application Curve

![Graph showing typical application curve.]

Ch. 1 = $V_{\text{(SUPPLY)}}$  
Ch. 2 = $V_{\text{(OUT)}}$  
Ch. 3 = $I_{\text{(P)}}$  
Ch. 4 = $I_{\text{(LED)}}$  
$V_{\text{(SUPPLY)}}$ increases from 9 V to 16 V

Figure 26. Constant Output Current With Increasing Supply Voltage
9 Layout

9.1 Layout Guidelines
Thermal dissipation is the primary consideration for TPS92610-Q1 layout. TI recommends good thermal dissipation area connected to thermal pads with thermal vias.

9.2 Layout Example

Figure 27. TPS92610-Q1 Example Layout Diagram
10 Device and Documentation Support

10.1 Documentation Support

10.1.1 Related Documentation
For related documentation see the following:
- **TPS92610-Q1 EVM User's Guide**
- **How to Calculate TPS92630-Q1 Maximum Output Current for Automotive Exterior Lighting Applications**
- **Automotive Linear LED Driver Reference Design for Center High-Mounted Stop Lamp (CHMSL)**
- **User Guides: Automotive Linear LED Driver Reference Design for Center High-Mounted Stop Lamp (CHMSL)**

10.2 Receiving Notification of Documentation Updates
To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on **Alert me** to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

10.3 Community Resources
**TI E2E™ support forums** are an engineer’s go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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10.5 Electrostatic Discharge Caution
This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

**ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.**

10.6 Glossary
**SLYZ022 — TI Glossary.**
This glossary lists and explains terms, acronyms, and definitions.
11 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most-current data available for the designated device. This data is subject to change without notice and without revision of this document. For browser-based versions of this data sheet, see the left-hand navigation pane.
###包装信息

<table>
<thead>
<tr>
<th>订购代码</th>
<th>状态</th>
<th>包装类型</th>
<th>包装</th>
<th>引脚</th>
<th>Eco计划</th>
<th>铅/球形处理</th>
<th>MSL峰值温度</th>
<th>工作温度（°C）</th>
<th>设备标记</th>
<th>样品</th>
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<tr>
<td>TPS92610QPWPRQ1</td>
<td>ACTIVE</td>
<td>HTSSOP</td>
<td>PWP</td>
<td>14</td>
<td>2000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU NIPDAU</td>
<td>Level-3-260C-168 HR</td>
<td>-40至125</td>
<td>TP92610</td>
</tr>
</tbody>
</table>

(1) 订单状态值定义如下：
**ACTIVE**：建议用于新设计的产品。
**LIFEBUY**：TI已宣布该设备将停产，并且进入生命周期购买期。
**NRND**：不建议用于新设计。该设备适用于支持现有客户，但TI不建议在新设计中使用。
**PREVIEW**：已宣布但未生产。样品可能或可能不可用。
**OBSOLETE**：TI已停止生产该设备。

(2) RoHS：TI定义“RoHS”为符合当前欧盟RoHS要求的所有10种RoHS物质，包括RoHS物质的重量不超过0.1%的 homogeneous材料。设计用于在高温下焊接的“RoHS”产品适合用于指定的无铅过程。TI可能根据这些产品的类型将其称为“Pb-Free”。

**RoHS Exempt**：TI定义“RoHS Exempt”为符合RoHS要求的产品。

**Green**：TI定义“Green”为氯（Cl）和溴（Br）的含量低于1000ppm的阈值。三氧化二锑基阻燃剂也必须满足每种阻燃剂均符合≤1000ppm的阈值要求。

(3) MSL，峰值温度。- 湿度敏感度级别根据JEDEC行业标准分类，以及峰值焊料温度。

(4) 可能存在额外标记，这关系到标识，标识码，或环境分类。

(5) 多个设备标记将包含在括号中。只有一个设备标记包含在括号中，用“~”分隔。如果一条内容包含在括号中，则它表示前一条内容的延续。

(6) 铅/球形处理 - 订购的产品可能有多种材料完成选项。完成选项由垂直分隔线隔开。完成选项值可以超两行。完成价值超过最大列宽。

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**TAPE AND REEL INFORMATION**

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0  (mm)</th>
<th>B0  (mm)</th>
<th>K0  (mm)</th>
<th>P1  (mm)</th>
<th>W  (mm)</th>
<th>Pin1 Quadrant</th>
</tr>
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<tbody>
<tr>
<td>TPS92610QPWPRQ1</td>
<td>HTSSOP</td>
<td>PWP</td>
<td>14</td>
<td>2000</td>
<td>330.0</td>
<td>12.4</td>
<td>6.9</td>
<td>5.6</td>
<td>1.6</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
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</table>

*All dimensions are nominal.*
**TAPE AND REEL BOX DIMENSIONS**

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS92610QPWPRQ1</td>
<td>HTSSOP</td>
<td>PWP</td>
<td>14</td>
<td>2000</td>
<td>350.0</td>
<td>350.0</td>
<td>43.0</td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusions. Mold flash and protrusion shall not exceed 0.15 per side.
D. This package is designed to be soldered to a thermal pad on the board. Refer to Technical Brief, PowerPAD Thermally Enhanced Package, Texas Instruments Literature No. SLMA002 for information regarding recommended board layout. This document is available at www.ti.com <http://www.ti.com>.
E. See the additional figure in the Product Data Sheet for details regarding the exposed thermal pad features and dimensions.
F. Falls within JEDEC MO-153

PowerPAD is a trademark of Texas Instruments.
THERMAL INFORMATION

This PowerPAD™ package incorporates an exposed thermal pad that is designed to be attached to a printed circuit board (PCB). The thermal pad must be soldered directly to the PCB. After soldering, the PCB can be used as a heatsink. In addition, through the use of thermal vias, the thermal pad can be attached directly to the appropriate copper plane shown in the electrical schematic for the device, or alternatively, can be attached to a special heatsink structure designed into the PCB. This design optimizes the heat transfer from the integrated circuit (IC).

For additional information on the PowerPAD package and how to take advantage of its heat dissipating abilities, refer to Technical Brief, PowerPAD Thermally Enhanced Package, Texas Instruments Literature No. SLMA002 and Application Brief, PowerPAD Made Easy, Texas Instruments Literature No. SLMA004. Both documents are available at www.ti.com.

The exposed thermal pad dimensions for this package are shown in the following illustration.

NOTE: A. All linear dimensions are in millimeters

PowerPAD is a trademark of Texas Instruments
LAND PATTERN DATA

PWP (R-PDSO-G14)  PowerPAD™ PLASTIC SMALL OUTLINE

Example Board Layout
Via pattern and copper pad size
may vary depending on layout constraints

Solder Mask
Over Copper

3x1,5
6xØ0,33

Example Solder Mask
Defined Pad
(See Note C, D)

Example Non Soldermask Defined Pad

Center Power Pad Solder Stencil Opening

<table>
<thead>
<tr>
<th>Stencil Thickness</th>
<th>X</th>
<th>Y</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.1mm</td>
<td>2.5</td>
<td>2.65</td>
</tr>
<tr>
<td>0.127mm</td>
<td>2.31</td>
<td>2.46</td>
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<tr>
<td>0.152mm</td>
<td>2.15</td>
<td>2.3</td>
</tr>
<tr>
<td>0.178mm</td>
<td>2.05</td>
<td>2.15</td>
</tr>
</tbody>
</table>

NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Customers should place a note on the circuit board fabrication drawing not to alter the center solder mask defined pad.
D. This package is designed to be soldered to a thermal pad on the board. Refer to Technical Brief, PowerPad Thermally Enhanced Package, Texas Instruments Literature No. SLMA002, SLMA004, and also the Product Data Sheets for specific thermal information, via requirements, and recommended board layout. These documents are available at www.ti.com <http://www.ti.com>. Publication IPC-7351 is recommended for alternate designs.
E. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Example stencil design based on a 50% volumetric metal load solder paste. Refer to IPC-7525 for other stencil recommendations.
F. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

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