

# **LM79XX Series 3-Terminal Negative Regulators**

Check for Samples: LM7905, LM7912, LM7915

### **FEATURES**

- Thermal, Short Circuit and Safe Area Protection
- · High Ripple Rejection
- 1.5A Output Current
- 4% Tolerance on Preset Output Voltage

### **DESCRIPTION**

The LM79XX series of 3-terminal regulators is available with fixed output voltages of -5V, -12V, and -15V. These devices need only one external component—a compensation capacitor at the output. The LM79XX series is packaged in the TO-220 power package and is capable of supplying 1.5A of output current.

These regulators employ internal current limiting safe area protection and thermal shutdown for protection against virtually all overload conditions.

Low ground pin current of the LM79XX series allows output voltage to be easily boosted above the preset value with a resistor divider. The low quiescent current drain of these devices with a specified maximum change with line and load ensures good regulation in the voltage boosted mode.

For applications requiring other voltages, see LM137 datasheet.

### **Connection Diagram**

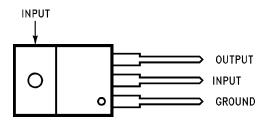
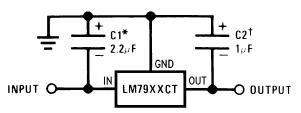


Figure 1. TO-220 Package Front View See Package Number NDE0003B

### **Typical Applications**



\*Required if regulator is separated from filter capacitor by more than 3". For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of 100µF, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

Figure 2. Fixed Regulator

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.





These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

# ABSOLUTE MAXIMUM RATINGS(1)

-25V
a=1/
-35V
25V
30V
Internally Limited
0°C to +125°C
−65°C to +150°C
230°C

<sup>(1)</sup> Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not ensure Specific Performance limits. For ensured specifications and test conditions, see the Electrical Characteristics.

### **ELECTRICAL CHARACTERISTICS**

Conditions unless otherwise noted: I<sub>OUT</sub> = 500mA, C<sub>IN</sub> = 2.2µF, C<sub>OUT</sub> = 1µF, 0°C ≤ T<sub>J</sub> ≤ +125°C, Power Dissipation ≤ 1.5W.

	Par		LM7905C		Units	
	Output Voltage -5V					
	Input Voltage (unle	ess otherwise specified)		-10V		
Symbol	Parameter	Conditions	Min	Тур	Max	
Vo	Output Voltage	$T_J = 25$ °C	-4.8	-5.0	-5.2	V
		$5mA \le I_{OUT} \le 1A$ ,	-4.75		-5.25	V
		P ≤ 15W		$(-20 \le V_{IN} \le -7)$	7)	V
$\Delta V_{O}$	Line Regulation	$T_J = 25^{\circ}C, (1)$		8	50	mV
				$(-25 \le V_{IN} \le -7)$	7)	V
				2	15	mV
				$(-12 \le V_{IN} \le -8$	3)	V
$\Delta V_{O}$	Load Regulation	$T_J = 25^{\circ}C, (1)$				
		5mA ≤ I <sub>OUT</sub> ≤ 1.5A		15	100	mV
		250mA ≤ I <sub>OUT</sub> ≤ 750mA		5	50	mV
IQ	Quiescent Current	$T_J = 25$ °C		1	2	mA
$\Delta I_Q$	Quiescent Current	With Line			0.5	mA
	Change			$(-25 \le V_{IN} \le -7$	7)	V
		With Load, 5mA ≤ I <sub>OUT</sub> ≤ 1A			0.5	mA
V <sub>n</sub>	Output Noise Voltage	T <sub>A</sub> = 25°C, 10Hz ≤ f ≤ 100Hz		125		μV
	Ripple Rejection	f = 120Hz	54	66		dB
				$(-18 \le V_{IN} \le -8)$		
	Dropout Voltage	T <sub>J</sub> = 25°C, I <sub>OUT</sub> = 1A		1.1		V
I <sub>OMAX</sub>	Peak Output Current	$T_J = 25^{\circ}C$		2.2		Α
	Average Temperature	I <sub>OUT</sub> = 5mA,		0.4		mV/°C
	Coefficient of	0 C ≤ T <sub>J</sub> ≤ 100°C				
	Output Voltage					

<sup>(1)</sup> Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

Product Folder Links: LM7905 LM7912 LM7915

<sup>(2)</sup> Refer to DESIGN CONSIDERATIONS for details.



# **ELECTRICAL CHARACTERISTICS**

Conditions unless otherwise noted:  $I_{OUT} = 500 \text{mA}, \ C_{IN} = 2.2 \mu \text{F}, \ C_{OUT} = 1 \mu \text{F}, \ 0^{\circ}\text{C} \leq T_{J} \leq +125^{\circ}\text{C}, \ \text{Power Dissipation} \leq 1.5 \text{W}.$ 

	Part N	I	LM7912	C	I	LM79150	C	Units	
	Output		-12V			−15V			
	Input Voltage (unless	s otherwise specified)		-19V			-23V		
Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	
Vo	Output Voltage	T <sub>J</sub> = 25°C	-11.5	-12.0	-12.5	-14.4	-15.0	-15.6	V
		$5mA \le I_{OUT} \le 1A$ ,	-11.4		-12.6	-14.25		<b>-</b> 15.75	V
		P ≤ 15W	(-27	≤ V <sub>IN</sub> ≤ -	-14.5)	(-30	≤ V <sub>IN</sub> ≤ -	-17.5)	V
$\Delta V_{O}$	Line Regulation	$T_J = 25^{\circ}C, (1)$		5	80		5	100	mV
			$(-30 \le V_{IN} \le -14.5$		-14.5)	$(-30 \le V_{IN} \le -17.5)$			V
				30		3	50	mV	
			(-22	$2 \le V_{IN} \le$	-16)	(-26 ≤ V <sub>IN</sub> ≤-20)		<b>-</b> 20)	V
ΔV <sub>O</sub>	Load Regulation	T <sub>J</sub> = 25°C, <sup>(1)</sup>							
		5mA ≤ I <sub>OUT</sub> ≤ 1.5A		15	200		15	200	mV
		250mA ≤ I <sub>OUT</sub> ≤ 750mA		5	75		5	75	mV
lQ	Quiescent Current	$T_J = 25^{\circ}C$		1.5	3		1.5	3	mA
$\Delta I_Q$	Quiescent Current	With Line		•	0.5		•	0.5	mA
	Change		(-30	≤ V <sub>IN</sub> ≤ -	-14.5)	$(-30 \le V_{IN} \le -17.5)$		·17.5)	V
		With Load, 5mA ≤ I <sub>OUT</sub> ≤ 1A			0.5	0.5			mA
V <sub>n</sub>	Output Noise Voltage	T <sub>A</sub> = 25°C, 10Hz ≤ f ≤ 100Hz		300			375		μV
	Ripple Rejection	f = 120 Hz	54	70		54	70		dB
			(-25	$5 \le V_{IN} \le$	-15)	(-30	≤ V <sub>IN</sub> ≤ -	·17.5)	V
	Dropout Voltage	$T_{J} = 25^{\circ}C, I_{OUT} = 1A$		1.1			1.1		V
I <sub>OMAX</sub>	Peak Output Current	T <sub>J</sub> = 25°C		2.2			2.2		Α
	Average Temperature	I <sub>OUT</sub> = 5mA,		-0.8			-1.0		mV/°C
	Coefficient of	0 C ≤ T <sub>J</sub> ≤ 100°C							
	Output Voltage								

<sup>(1)</sup> Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

Product Folder Links: LM7905 LM7912 LM7915



#### **DESIGN CONSIDERATIONS**

The LM79XX fixed voltage regulator series has thermal overload protection from excessive power dissipation, internal short circuit protection which limits the circuit's maximum current, and output transistor safe-area compensation for reducing the output current as the voltage across the pass transistor is increased.

Although the internal power dissipation is limited, the junction temperature must be kept below the maximum specified temperature (125°C) in order to meet data sheet specifications. To calculate the maximum junction temperature or heat sink required, the following thermal resistance values should be used:

	Тур	Max	Тур	Max
Package	θ <sub>JC</sub>	θ <sub>JC</sub>	$\theta_{JA}$	$\theta_{JA}$
	°C/W	°C/W	°C/W	°C/W
TO-220	3.0	5.0	60	40

$$P_{D MAX} = \frac{T_{J Max} - T_{A}}{\theta_{JC} + \theta_{CA}} \text{ or } \frac{T_{J Max} T_{A}}{\theta_{JA}}$$

$$\theta_{CA} = \theta_{CS} + \theta_{SA} \text{ (without heat sink)}$$
(1)

Solving for T<sub>J</sub>:

$$T_{J} = T_{A} + P_{D} (\theta_{JC} + \theta_{CA})$$

or

= 
$$T_A + P_D \theta_{JA}$$
 (without heat sink)

#### where

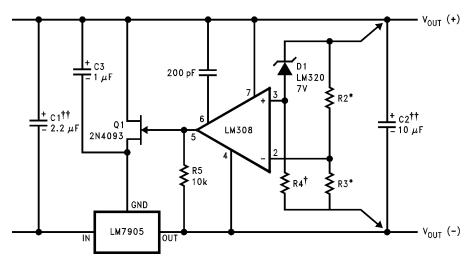
- T<sub>J</sub> = Junction Temperature
- T<sub>A</sub> = Ambient Temperature
- P<sub>D</sub> = Power Dissipation
- $\theta_{JA}$  = Junction-to-Ambient Thermal Resistance
- $\theta_{JC}$  = Junction-to-Case Thermal Resistance
- θ<sub>CA</sub> = Case-to-Ambient Thermal Resistance
- $\theta_{CS}$  = Case-to-Heat Sink Thermal Resistance
- $\theta_{SA}$  = Heat Sink-to-Ambient Thermal Resistance

# **Typical Applications**

Bypass capacitors are necessary for stable operation of the LM79XX series of regulators over the input voltage and output current ranges. Output bypass capacitors will improve the transient response by the regulator.

The bypass capacitors,  $(2.2\mu\text{F} \text{ on the input, } 1.0\mu\text{F} \text{ on the output)}$  should be ceramic or solid tantalum which have good high frequency characteristics. If aluminum electrolytics are used, their values should be  $10\mu\text{F}$  or larger. The bypass capacitors should be mounted with the shortest leads, and if possible, directly across the regulator terminals.



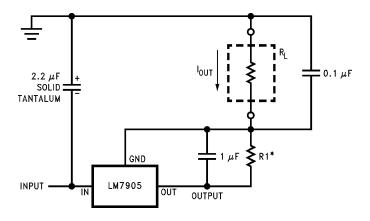


Load and line regulation < 0.01% temperature stability ≤ 0.2%

†Determine Zener current

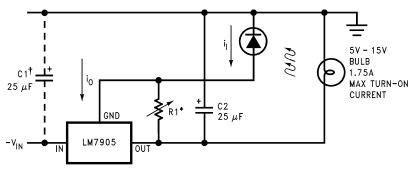
††Solid tantalum

Figure 3. High Stability 1 Amp Regulator



 $*I_{OUT} = 1 \text{ mA} + \frac{5V}{R1}$ 

Figure 4. Current Source



<sup>\*</sup>Lamp brightness increase until  $i_I=i_Q$  ( $\approx 1$  mA) + 5V/R1.

†Necessary only if raw supply filter capacitor is more that 2" from LM7905CT

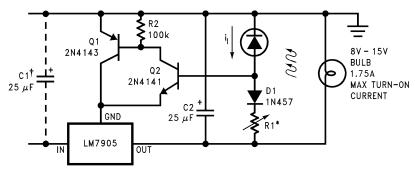
Figure 5. Light Controller Using Silicon Photo Cell

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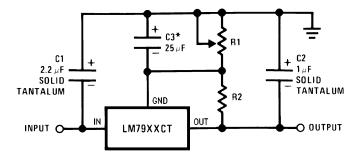
<sup>\*</sup>Select resistors to set output voltage. 2 ppm/°C tracking suggested





\*Lamp brightness increases until  $i_i$  = 5V/R1 ( $I_i$  can be set as low as 1  $\mu$ A) †Necessary only if raw supply filter capacitor is more that 2" from LM7905

Figure 6. High-Sensitivity Light Controller



\*Improves transient response and ripple rejection. Do not increase beyond 50  $\mu$ F.

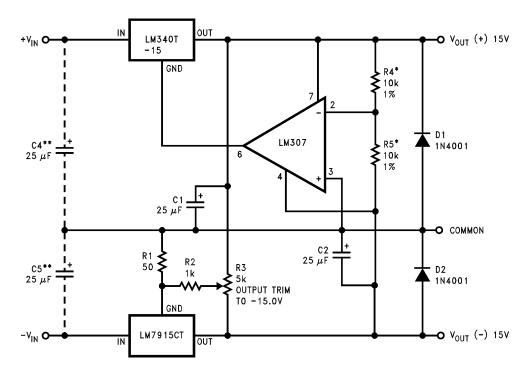
 $V_{OUT} = V_{SET} \left( \frac{R1 + R2}{R2} \right)$ 

Select R2 as follows:

 $\begin{array}{ll} \text{LM7905CT} & 300\Omega \\ \text{LM7912CT} & 750\Omega \\ \text{LM7915CT} & 1\text{k} \end{array}$ 

Figure 7. Variable Output





	(-15)	(+15)
Load Regulation at $\Delta I_L = 1A$	40mV	2mV
Output Ripple, $C_{IN} = 3000\mu\text{F}$ , $I_L = 1\text{A}$	100 μVms	100 μVms
Temperature Stability	50mV	50mV
Output Noise 10Hz ≤ f ≤ 10kHz	150 μVms	150 μVms

<sup>\*</sup>Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs.

Figure 8. ±15V, 1 Amp Tracking Regulators

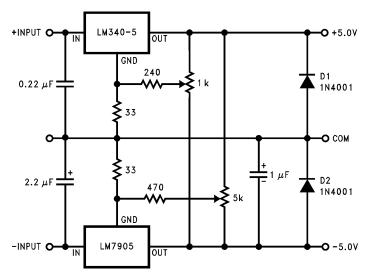


Figure 9. Dual Trimmed Supply

<sup>\*\*</sup>Necessary only if raw supply filter capacitors are more than 3" from regulators.



# **Schematic Diagrams**

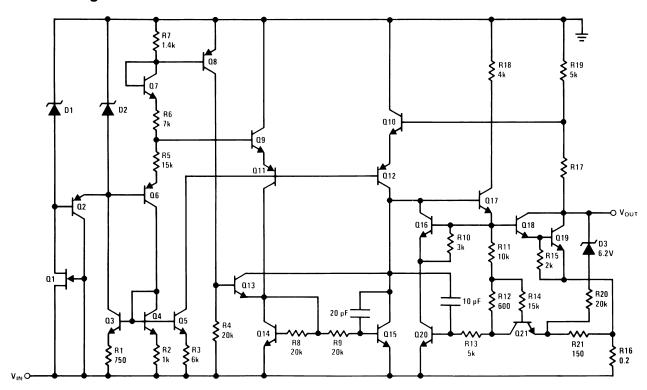


Figure 10. -5V

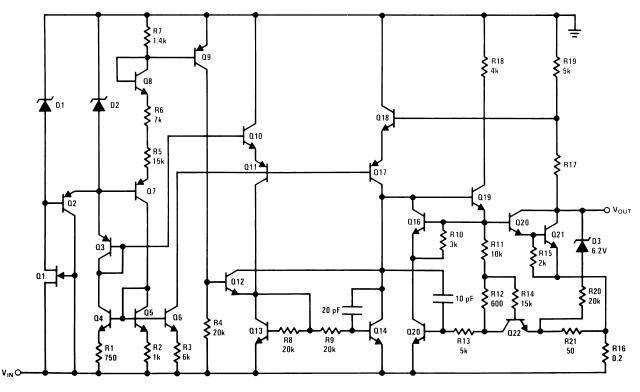


Figure 11. -12V and -15V



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# **REVISION HISTORY**

Cł	nanges from Revision B (May 2013) to Revision C	age
•	Changed layout of National Data Sheet to TI format.	8

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#### PACKAGING INFORMATION

Orderable part number	Status	Material type	Package   Pins	Package qty   Carrier	RoHS	Lead finish/	MSL rating/	Op temp (°C)	Part marking
	(1)	(2)			(3)	Ball material Peak reflow			(6)
						(4)	(5)		
LM7905CT/NOPB	Active	Production	TO-220 (NDE)   3	45   TUBE	Yes	SN	Level-1-NA-UNLIM	0 to 125	LM7905CT
LM7905CT/NOPB.B	Active	Production	TO-220 (NDE)   3	45   TUBE	Yes	SN	Level-1-NA-UNLIM	0 to 125	LM7905CT
LM7912CT/NOPB	Active	Production	TO-220 (NDE)   3	45   TUBE	Yes	SN	Level-1-NA-UNLIM	0 to 125	LM7912CT
LM7912CT/NOPB.B	Active	Production	TO-220 (NDE)   3	45   TUBE	Yes	SN	Level-1-NA-UNLIM	0 to 125	LM7912CT
LM7915CT/NOPB	Active	Production	TO-220 (NDE)   3	45   TUBE	Yes	SN	Level-1-NA-UNLIM	0 to 125	LM7915CT
LM7915CT/NOPB.B	Active	Production	TO-220 (NDE)   3	45   TUBE	Yes	SN	Level-1-NA-UNLIM	0 to 125	LM7915CT

<sup>(1)</sup> Status: For more details on status, see our product life cycle.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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<sup>(5)</sup> MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

<sup>(6)</sup> Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.



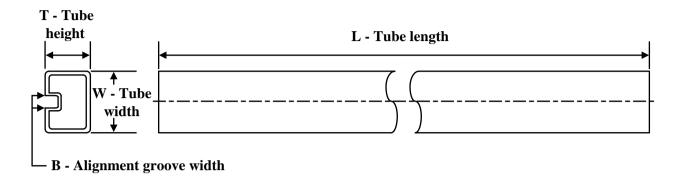
# **PACKAGE OPTION ADDENDUM**

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# **PACKAGE MATERIALS INFORMATION**

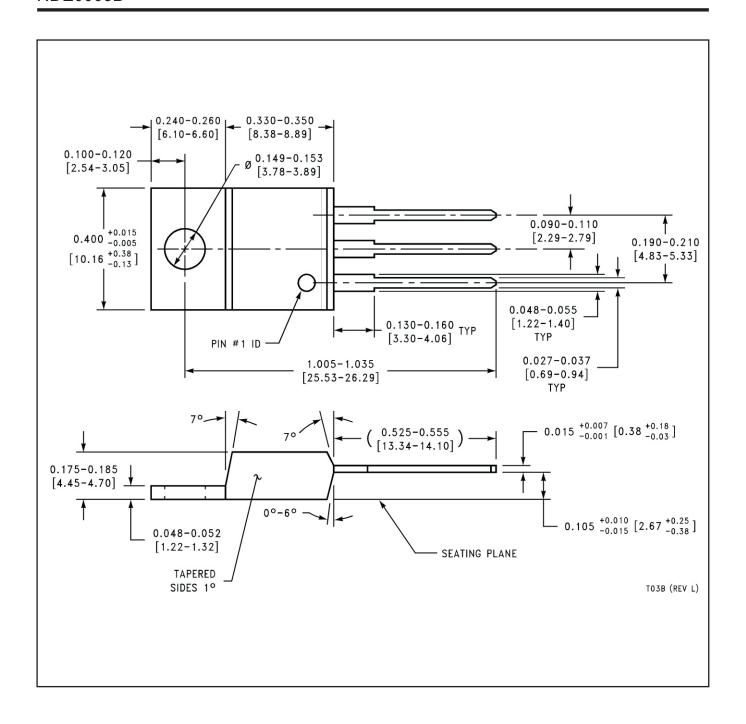
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# **TUBE**



\*All dimensions are nominal

Device	Package Name	Package Type	Pins	SPQ	L (mm)	W (mm)	T (µm)	B (mm)
LM7905CT/NOPB	NDE	TO-220	3	45	502	33	6985	4.06
LM7905CT/NOPB.B	NDE	TO-220	3	45	502	33	6985	4.06
LM7912CT/NOPB	NDE	TO-220	3	45	502	33	6985	4.06
LM7912CT/NOPB.B	NDE	TO-220	3	45	502	33	6985	4.06
LM7915CT/NOPB	NDE	TO-220	3	45	502	33	6985	4.06
LM7915CT/NOPB.B	NDE	TO-220	3	45	502	33	6985	4.06



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