
Tag-it™ HF-I PLUS TRANSPONDER INLAYS LARGE RECTANGLE

FEATURES

- ISO/IEC 15693-2, -3; ISO/IEC 18000-3 Compliant
- 13.56-MHz Operating Frequency
- 2048-Bit User Memory in 64-Bit × 32-Bit Blocks
- User and Factory Lock Per Block
- Application Family Identifier (AFI)
- Data Storage Format Identifier (DSFID)
- Combined Inventory Read Block

APPLICATIONS

- Product Authentication
- Library
- Supply-Chain Management
- Asset Management
- Ticketing/Stored Value

DESCRIPTION

Texas Instruments Tag-it™ HF-I plus transponder inlays consist of 13.56-MHz high-frequency (HF) transponders that are compliant with the ISO/IEC 15693 and ISO/IEC 18000-3 global open standards. These products offer a user-accessible memory of 2048 bits, organized in 64 blocks, and an extensive command set available in six different antenna shapes, with frequency offset for integration into paper, PVC, or other substrates.

The Tag-it HF-I plus transponder inlays are manufactured with TI's patented laser tuning process to provide consistent read performance. Prior to delivery, the transponders undergo complete functional and parametric testing in order to provide the high quality that customers have come to expect from TI.

The Tag-it HF-I plus transponder inlays are well suited for a variety of applications including, but not limited to, product authentication, library, supply-chain management, asset management, and ticketing/stored value applications.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

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SPECIFICATIONS⁽¹⁾

	PART NUMBER	
	RI-I02-112A-03	RI-I02-112B-03
Supported standard	ISO/IEC 15693-2, -3; ISO/IEC 18000-3	
Recommended operating frequency	13.56 MHz	
Passive resonance frequency (at 25°C)	13.86 MHz ± 200 kHz (includes frequency offset to compensate further integration into paper)	14.4 MHz ± 200 kHz (includes frequency offset to compensate PVC lamination)
Typical required activation field strength to read (at 25°C)	94 dBμA/m ⁽²⁾	94 dBμA/m ⁽³⁾
Typical required activation field strength to write (at 25°C)	97 dBμA/m ⁽²⁾	97 dBμA/m ⁽³⁾
Factory-programmed read-only number	64 bits	
Memory (user programmable)	25k bits organized in 64-bit × 32-bit blocks	
Typical programming cycles (at 25°C)	100,000	
Data retention time (at 55°C)	>10 years	
Simultaneous identification of tags	Up to 50 tags per second (reader/antenna dependent)	
Antenna size	45 mm × 76 mm (~1.77 in × ~2.99 in)	
Foil width	48 mm ± 0.5 mm (1.89 in ± 0.02 in)	
Foil pitch	96 mm +0.1 mm/-0.4 mm (~3.78 in)	
Thickness	Chip area: 0.355 mm (~0.014 in) Antenna area: 0.085 mm (~0.0033 in)	
Base material	Substrate: PET (polyethylenetherephtalate); Antenna: aluminum	
Smallest bending radius allowed	18 mm (~0.71 in)	
Operating temperature	-25°C to 70°C	
Storage temperature (single inlay)	-40°C to 85°C (warpage may occur at upper temperature range)	
Storage temperature (on reel)	-40°C to 40°C	
Delivery	Single-row tape wound on cardboard reel with 500-mm diameter Reel outer width: approximately 60 mm (~2.36 in) Reel inner width: approximately 50 mm (~1.97 in) Hub diameter: 76.2 mm (3 in)	
Typical quantity of good units per reel	5,000	

(1) For highest possible read-out coverage, operate readers at a modulation depth of 20% or higher.

(2) After integration into paper

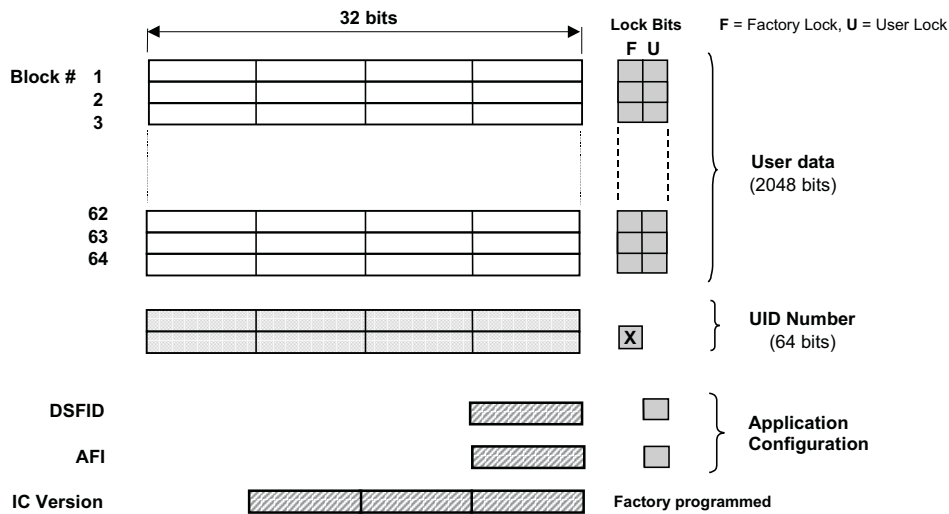
(3) After PVC lamination

SUPPORTED COMMAND SET

REQUEST	REQUEST MODE ⁽¹⁾					
	REQUEST CODE	INVENTORY	ADDRESSED	NON-ADDRESSED	SELECT	AFI
ISO 15693 Mandatory and Optional Commands						
Inventory	0x01	Ü	–	–	–	Ü
Stay Quiet	0x02	–	Ü	–	–	–
Read_Single_Block	0x20	Ü	Ü	Ü	Ü	Ü
Write_Single_Block	0x21	–	Ü	Ü	Ü	–
Lock_Block	0x22	–	Ü	Ü	Ü	–
Read_Multi_Blocks	0x23	Ü	Ü	Ü	Ü	Ü
Write_Multi-Blocks	0x24	–	–	–	–	–
Select Tag	0x25	–	Ü	–	–	–
Reset to Ready	0x26	–	Ü	Ü	Ü	–
Write_AFI	0x27	–	Ü	Ü	Ü	–
Lock_AFI	0x28	–	Ü	Ü	Ü	–
Write DSFID	0x29	–	Ü	Ü	Ü	–
Lock DSFID	0x2A	–	Ü	Ü	Ü	–
Get_System_info	0x2B	Ü	Ü	Ü	Ü	Ü
Get_M_BLK_Sec_St	0x2C	Ü	Ü	Ü	Ü	Ü
TI Custom Commands						
Write_2_Blocks	0xA2	–	Ü	Ü	Ü	–
Lock_2_Blocks	0xA3	–	Ü	Ü	Ü	–

(1) Ü = Implemented, – = Not applicable

MEMORY ORGANIZATION



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