EDERC2014

Embedded Design in Education & Research Conference

11-12 September, 2014

Milano - Italy





TI university program





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Important Dates:

March 24th:

Proposals for special sessions **May 12th:**

Proposals for tutorials/hands-on **May-26th:** June 16th:

Electronic submission of papers **July 14th:**

Notification of acceptance

August 4th:

Submission of camera ready paper **August 11th:**

August 11th:
Early /Author Registration deadline
September 11th - 12th:
EDERC2014 Milan

We are delighted to announce a **Call for Papers** for the 6th **EDERC Conference** (European Embedded Design in Education and Research), to be held at the **Cosmo Hotel Palace, Milan, Italy,** September 11th and 12th 2014. EDERC2014 receives technical co-sponsorship from IEEE Region 8, IEEE Italian Signal Processing Chapter, and the IEEE IAS-PELS-IES North Italy Joint Chapter and the European Association of Signal Processing (EURASIP), in cooperation with Texas Instruments. The event aims to gather **educators and researchers** from around the world to share their experiences in the use of **DSP, microcontrollers** and **analog** technologies for **embedded systems**.

For the first time, **EDERC2014** will include an **analog signal processing systems track** that will incorporate smart sensing solutions including ultra-low power front end signal processing to detect "events" from vibrational, acoustic, magnetic, current, image or other analog sensors. Combining analog pre-processing with digital signal processing for optimum solutions is of particular interest.

Accepted papers will describe critical implementation issues and the latest applications of Texas Instruments embedded processors for a wide range of exciting applications. All papers will be indexed in **IEEE Xplore**. The conference will comprise **key note** addresses from industrial and academic experts, **oral, poster, show & tell demo sessions, and free workshops** (limited spaces) with tools.

Areas of Interest:

Submissions are invited in, but not limited to, the following areas:

DSP/microcontrollers/analog signal processing, smart sensor solutions, embedded processing technology in education and research using any of TI's multicore/DSP embedded processors and TI's analog technologies for:

- Power electronics, power management, digital power and control, motor drives
- Multimedia, 3D-imaging, compressed sensing, security and surveillance systems, smart cameras
- Communications: wired/wireless, SDR, broadband/UWB, internet, VOIP, optical
- Applications including: renewables, smart grid, biomedical, defence, radar and sonar, acoustics, aeronautics, automotive, transport, remote sensing, sensor networks, robotics, distributed collaborative systems, signal estimation and detection

Submission:

Procedures to submit a paper or proposal for special sessions using EDAS can be found at www.ti.com/ederc2014. Submitted papers must be camera-ready, no more than five pages long, and conform to the format specified on www.ti.com/ederc2014.

The early bird registration **cost of 199 Euro** will cover the registration fee, lunches, and refreshments for the two days, the EDERC2014 gala dinner and access to up to two hands-on workshops with tools. Furthermore, **a second person** from the same institution **may register for 50**% of the registration fee up to 11th August 2014. After 11th August 2014, the registration cost will be 249 Euro.

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