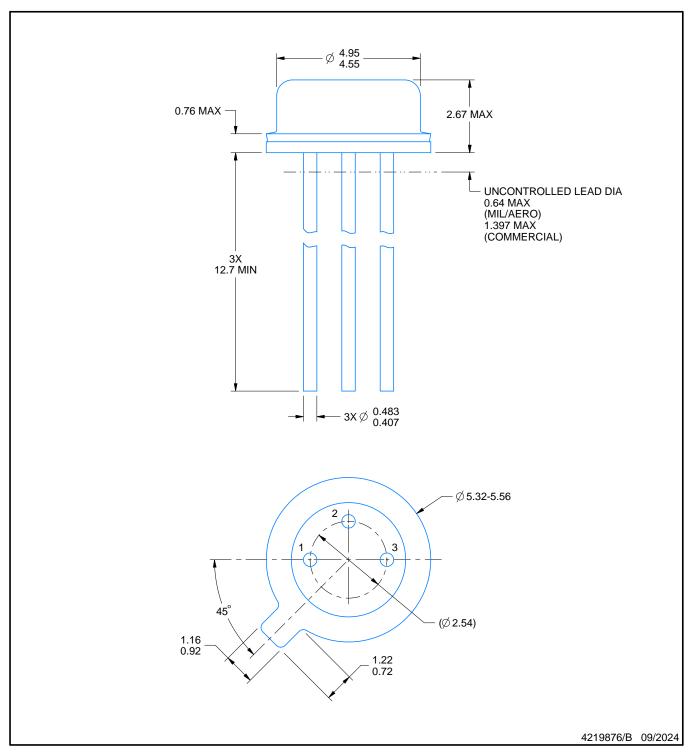


TRANSISTOR OUTLINE

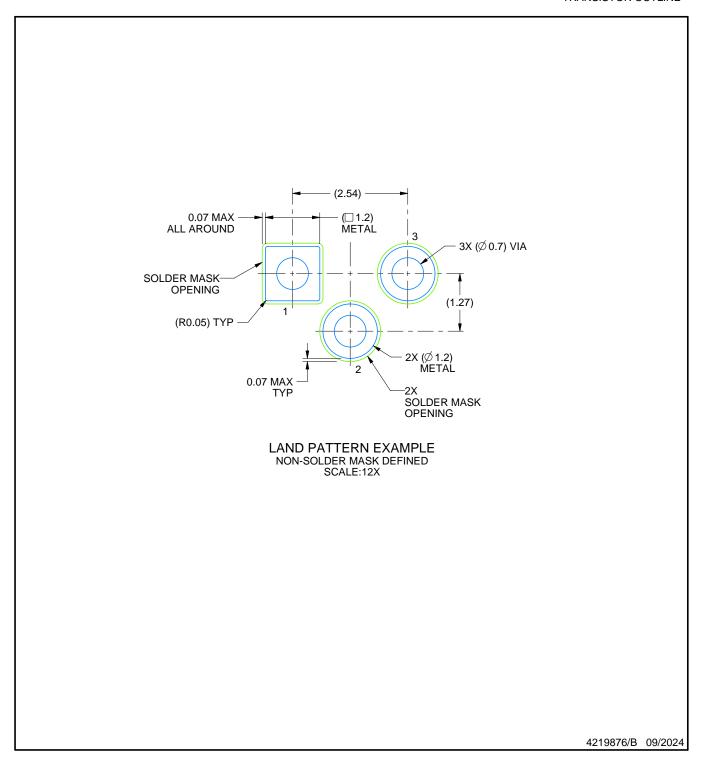


## NOTES:

- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing All linear differsions are in minimeters. Any difference per ASME Y14.5M.
  This drawing is subject to change without notice.
  Reference JEDEC registration TO-46.



TRANSISTOR OUTLINE



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