

## SN65LVDS348 vs SN65LVDS349

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SN65LVDS348 and SN65LVDS349 are high-speed, quadruple differential receivers with a wide, common-mode, input voltage range of -4 V to 5 V. Both these devices meet or exceed the requirements of the ANSI TIA/EIA-644A standard.

SN65LVDS348 and SN65LVDS349 support single-channel signaling rates up to 560 Mbps. Both these devices are in a 16-pin package to match the industry-standard footprint of the DS90LV048. Both offer a flow-through architecture with all inputs on one side and outputs on the other to ease board layout and reduce crosstalk between receivers.

The only difference between the SN65LVDS348 and SN65LVDS349 is that an active fail-safe circuit exists in the SN65LVDS348 but not in the SN65LVDS349.



(One of Four Shown) Figure 1. SN65LVDS348 (One of four Shown; failsafe circuit does not exit in LVDS349)

Figure 2. SN65LVDS349

## Table 1. Comparison of SN65LVDS348 vs SN65LVDS349

	SN65LVDS348	SN65LVDS349	
Standard	ANSI TIA/EIA-644A	ANSI TIA/EIA-644A	
Functionality	QUAD differential receiver	QUAD differential receiver	
Input Common Mode	-4 V to 5 V	-4 V to 5 V	
Data Rate	560 Mbps	560Mbps	
Package	16-pin PW (SOIC)	16-pin PW (SOIC)	
Failsafe	Yes (active fail-safe)	No	
Device behavior in fail-safe mode	The active fail-safe circuit detects when the input differential falls below 80 mV. A 600-ns fail-safe timer filters the window comparator outputs. When fail-safe function is asserted, the fail-safe logic drives the main receiver output to logic high.	The device logic functionality is ensured above $\pm 50$ mV. If the input VID is below $\pm 50$ mV, the output is uncertain.	

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