Quasi-Resonant Current Mode PWM Controller

Lowest Stand-By Power Consumption for High-Power AC/DC Applications





Overview

The LM5023 is a quasi-resonant controller with all of the features needed to implement a highly efficient off-line power supply. It uses the transformer auxiliary winding for demagnetization detection to ensure quasi-resonant operation (valleyswitching) to minimize switching losses. For applications that need to meet ENERGY STAR® low-standby power requirements, the LM5023 features an extremely low IQ

current (346 µA) and skip cycle mode which reduces power consumption at light loads. A feedback signal from the output provides a very accurate output voltage regulation of better than 1%. To reduce overheating and stress during sustained overload conditions, the LM5023 offers a hiccup mode option for over current restart timer to disable the outputs

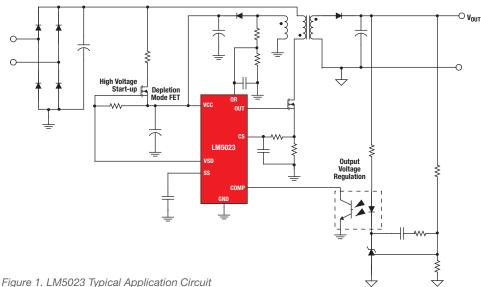
protection and provides a current limit and force a delayed restart.

Benefits

- No cable compensation required
- Skip cycle mode reduces power consumption at light loads for energy conservation applications (ENERGY STAR, CEPCP, etc.)
- Quasi-resonant mode reduces switching losses and improves active efficiency
- Reduces EMI
- Hiccup mode prevents over-heating during extended overload conditions

Key Specifications

- ±1% voltage output regulation over line, load, temp
- Skip mode for low-standby power <10 mW at 230 VAC
- Line current-limit feed-forward
- Critical Conduction Mode (CCM) control
- Peak current mode control when in CV operation
- Integrated 0.7 A peak gate
- OVP protection by sensing the AUX winding voltage



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Line Current-Limit Feed-Forward

To improve the overpower limit accuracy over the full Universal Input Line, the LM5023-2 integrates Line Current-Limit Feed-Forward.

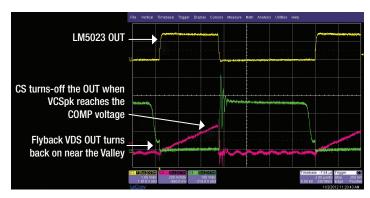


Figure 2. Typical QR switching waveforms

Line Current-Limit Feed-Forward improves the overpower limit by summing a current proportional to the input rectified line into the current sense resistor (Rsense), refer to *Figure 3*. The current proportional to the input line biases up the current sense pin which turns off the Flyback MOSFET earlier at high input line. This feature compensates for the propagation delays creating an overpower protection that is nearly constant over the Universal Input Line.

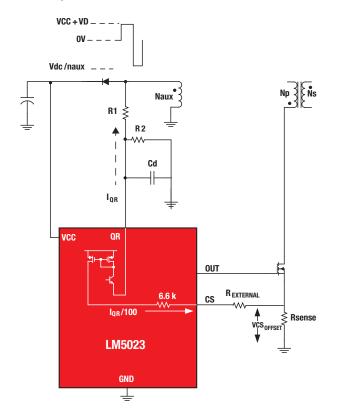


Figure 3. Line Current-Limit Feed-Forward diagram

Hiccup Mode

Hiccup Mode is a method to prevent the power supply from over-heating during an extended overload condition. In an overload fault, the current limit comparator turns off the driver output on pulse-by-pulse basis. This starts the Over Load Detection Timer and after the Over Load Detection Timer (OLDT) times out, the current limit comparator is re-checked. If the power supply is still in an overload condition, the OUT drive is latched-off and VCC is allowed to drop to VCC_{OFF} (7.5 V).

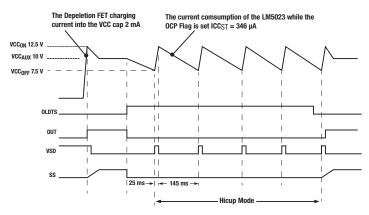


Figure 4. Hiccup Mode operation



Figure 5. Efficiency vs. I_{OUT} , V_{IN} 115 VAC and 230 VAC



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