

Application Report SLVA362-October 2009

# 3.6-V to 6-V Input, 3.3-V Output, Tiny and High Efficiency DC/DC Converter

PMP - DC/DC Low-Power Converters

### ABSTRACT

This design was created to help those desiring to design-in a Stellaris® ARM® Cortex<sup>™</sup>-M3 MCU into a system where the chief concern is minimizing solution size as well as maintaining high efficiency and long battery life. This particular design allows for an input voltage between 3.6V to 6V.

### 1 Features

- 3.6-V to 6-V input voltage range
- The TPS62300 is capable of driving up to 500 mA
- 3-MHz operation allows for small passive components
- High efficiency (over 92%)
- Low quiescent current (20 μA)
- Tiny 8-pin NanoStar<sup>™</sup> (chip scale) and QFN-10 and package

#### 2 Introduction

This reference design is for the Stellaris® ARM® Cortex<sup>™</sup>-M3 MCU devices and accounts for voltage and current, requirements given below. The Stellaris® devices only require a single 3.3V input, so no sequencing is required. The operating input voltage for this reference design is 3.6V to 6V. This design is optimized for small solution size, low part count, and high efficiency.

NanoStar is a trademark of Texas Instruments.



## 3 Requirements

The power requirements for each Stellaris® ARM® Cortex™-M3 MCU family are listed below.

For more information and other reference designs, please visit <u>www.ti.com/processorpower</u>.

Table 1. Stellaris® ARM® Cortex<sup>™</sup>-M3 MCU Family Power Requirements

DEVICE FAMILY	PIN NAME	VOLTAGE (V)	I <sub>MAX</sub> (mA)	TOLERANCE	SEQUENCING ORDER	TIMING DELAY	COMMENTS
LM3S100 series LM3S300 series LM3S600 series LM3S800 series LM3S1000 series LM3S2000 series LM3S3000 series LM3S5000 series	VDD	3.3	170	±10%	_	_	Internal regulator supplies power to device core
LM3S6000 series LM3S8000 series	VDD	3.3	225	±10%	_	_	Internal regulator supplies power to device core
LM3S9000 series	VDD	3.3	150	±10%	_	_	Internal regulator supplies power to device core
LM3S2B93, LM3S2B2793, LM3S5B91, LM3S5791	VDD	3.3	100	±10%	_	_	Internal regulator supplies power to device core
Note: The "Imax" currents listed are worst case expected values.							



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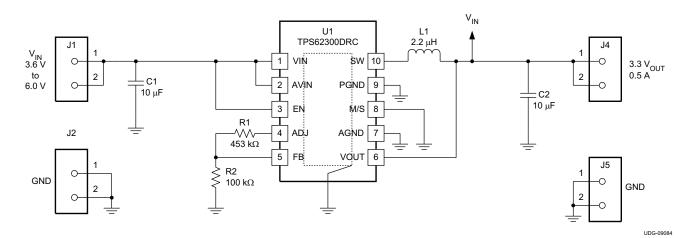


Figure 1. PMP4775 Reference Design Schematic

# 4 List of Materials

REF DES	QTY	VALUE	DESCRIPTION	SIZE	PART NUMBER	MFR
C1, C2	2	10 µF	Capacitor, Ceramic, 6.3 V, X5R, 10%	0603	C1608X5R0J106KT	TDK
L1	1	2.2 µH	Inductor, SMT, 1.5A, 110 mΩ	0.102" x 0.110"	VLF3010AT-2R2M1R0	TDK
R1	1	453 kΩ	Resistor, Chip, 1/16W, 1%	0603	Std	Std
R2	1	100 kΩ	Resistor, Chip, 1/16-W, 1%	0603	Std	Std
U1	1		3-MHz synchronous step-down converter, 500-mA, Adj	QFN-10	TPS62300DRC	ті

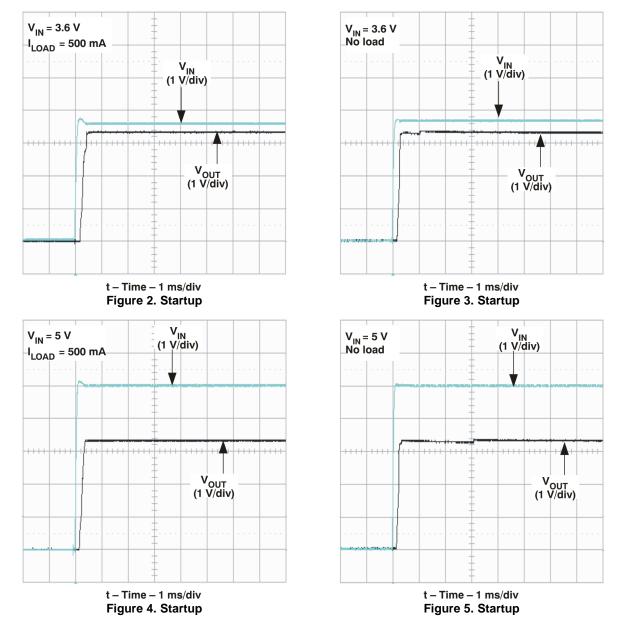
# Table 2. PMP4775 List of Materials



# 5 Test Results

The input and output startup waveforms are shown in Figure 2 through Figure 5. The output ripple voltage is shown in Figure 6. Figure 7 shows the transient response. The switching node waveform is shown in Figure 8.

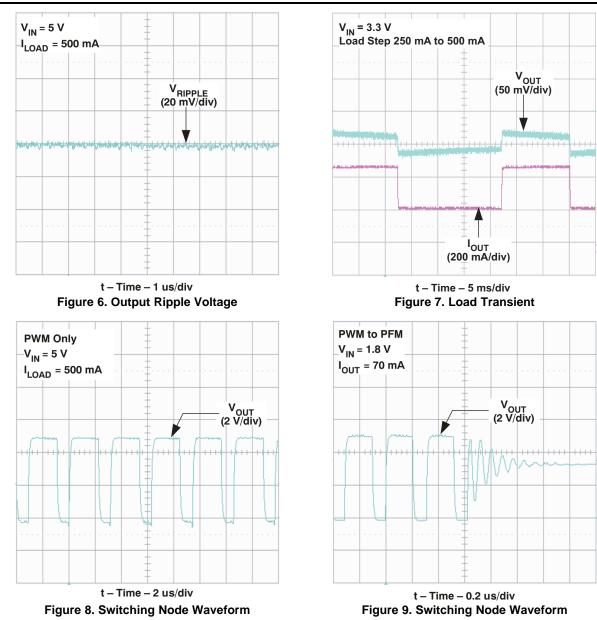
# 5.1 Test Results





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Test Results





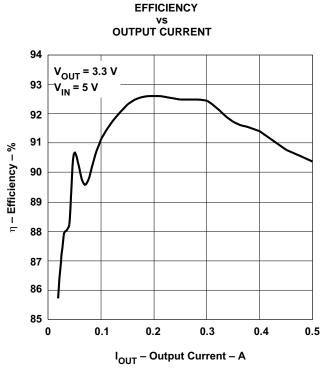


Figure . Figure 10.

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