COP472-3

AN-749 Quadrature Signal Interface to a COP400 Microcontroller

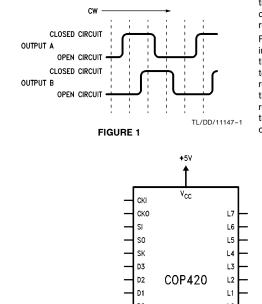


Literature Number: SNOA211

Quadrature Signal Interface to a COP400 Microcontroller

INTRODUCTION

Switches have always been a popular way of getting information into a microcontroller. Two-bit quadrature output devices, also known as two-bit gray code output, use two switches that are mechanically coupled together thru a shaft so that as the shaft is rotated the switches generate two square waves that are 90 degrees out of phase with each other. This is also known as being in quadrature, see *Figure 1*. The reason for doing this is that within the two signals there is the information to detect the direction of rotation, i.e., clockwise (CW) or counterclockwise (CCW). This type of device allows an input variable to be increased or decreased by CW or CCW rotation of the shaft. Additionally, these devices allow continuous rotation in either direction, which lets the span and resolution of the input variable to be a function of the software.



National Semiconductor Application Note 749 Walter Bacharowski February 1991



OPERATION

Figure 2 shows a hardware connection of a quadrature output device to the COP400 microcontroller. Although in this example the G0 and G1 I/O pins are used, any pin that can be used as an input could be used with the appropriate changes in the software.

In this example the output of device QD1 is processed to detect a state change in the quadrature signal and which direction the change was in. A 3-digit BCD variable, which is stored in RAM, is then incremented or decremented. The variable is defined to have a range of 200 to 350 units. The routine allows the variable to saturate at it's upper and lower limits when reached.

Figure 3 displays the two waveforms that are generated by QD1 as it's shaft is rotated from an arbitrary starting position. Each edge represents a change of state. By keeping track of the state that was moved from and the state that currently exists, it can be determined which direction the rotation was in.

Referring to *Figure 3*, there are 4 possible states for a starting position, (00, 01, 11, 10), and they will be referred to as the previous state. There are also 4 possible states to move to, (00, 01, 11, 10), and they will be referred to as the current state. *Figure 4* lists the 8 possible combinations of bits that can be formed by starting from each previous state and rotating CW or CCW to the current state. If the two bits of the previous state and current state are concatenated into one 4-bit value, each value will be unique. The routine



FIGURE 2

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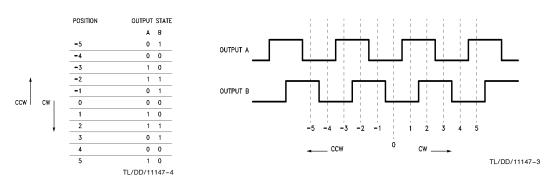


FIGURE 3. Quadrature Signal Output (Gray Code)

CONCLUSION

used in this example assigns the two bits of the current state to the low 2 bits of a 4-bit value, and the 2 bits of the previous state to the high 2 bits of a 4-bit value. This 4-bit value (see the column under the PS/CS heading in *Figure* 4) is then used as a pointer into a jump table which branches to the add or subtract part of the routine. This method takes advantage of the "jump indirect" instruction which implements a multiway branch based on the value of a pointer. The routine to input data from the quadrature device reads the value of G0 and G1 and compares it to the value stored from the previous read operation. If the two values are equal there is no input to process. If the two values are not equal there is an input and the data is processed to determine if

one is to be added to or subtracted from the variable. The flow chart details the operation of the subroutine "QUAD".

In *Figure 4*, only 8 of the possible 16 combinations are used. To account for potential spurious operation if one of the 8 undefined combinations occur, they are ignored by this routine by branching to a return instruction which bypasses any additional processing.

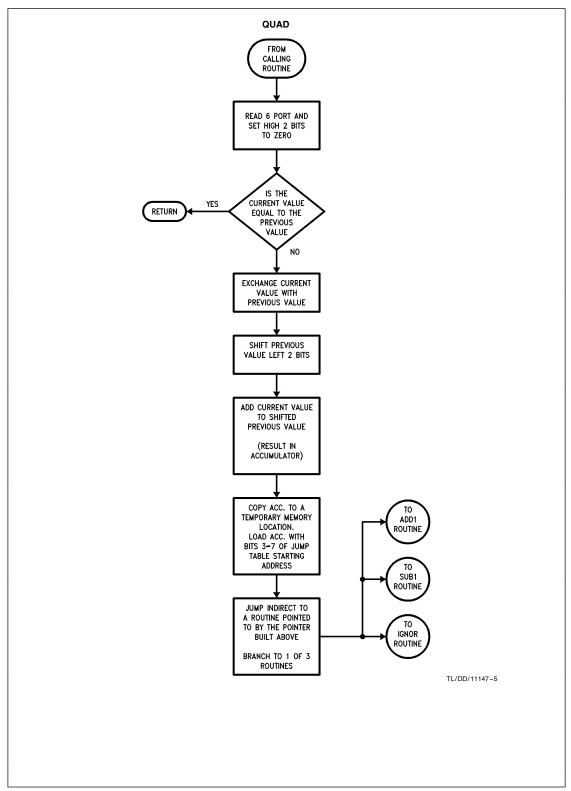
The source listing for this example subroutine, which is named "QUAD", is provided. An initialization routine that is required to set up the starting parameters is also included.

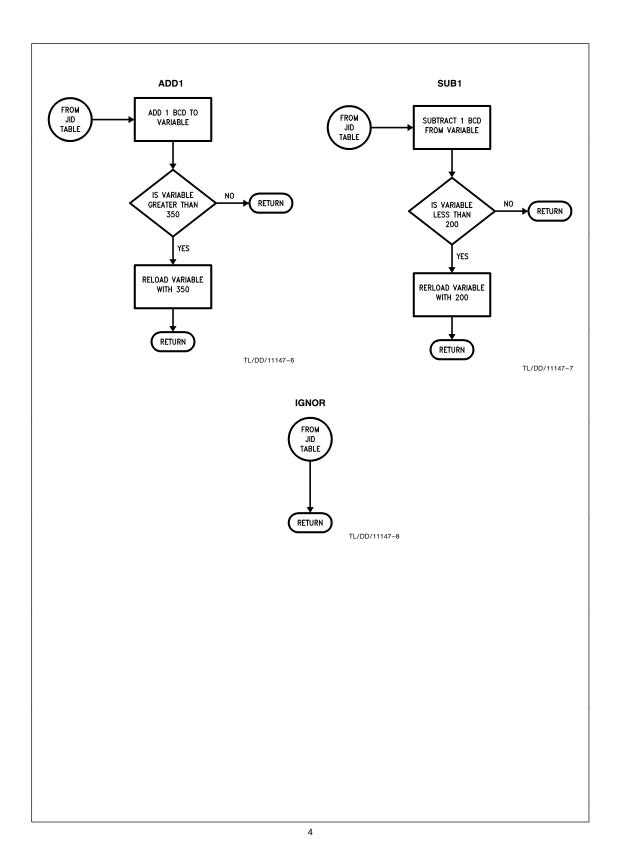
This application note demonstrates the relative ease of interfacing a quadrature device to a COP400 microcontroller. The combination of a low cost microcontroller and input device can provide the basis for a cost effective instrument or appliance design.

Direction	PS/CS	Hex Value	Operation
CW	00 10	2	Add 1
CCW	00 01	1	Subtract 1
CW	01 00	4	Add 1
CCW	01 11	7	Subtract 1
CW	11 01	D	Add 1
CCW	11 10	E	Subtract 1
CW	10 11	B	Add 1
CCW	10 00	8	Subtract 1

PS = Previous State CS = Current State

FIGURE 4





1 2 3 4 5 6			;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;			AUGUST	AC TURE SIGNAL INTERFACE TO THE COP420 1, 1990 BACHAROWSKI	
8 7 8			;		.TITLE .CHIP 4			
9 10				; ;****	***			
11 12				; ;ASSIG	NMENTS			
L3 L4 L5				; ;***** ·	***			
.6 .7 .8 .9 .0 .1		000F 003D 003E 003F 001D 001E 001F		; PV SCR0 SCR1 SCR2 VLD VLD VMD VHD		0,15 3,13 3,14 3,15 1,13 1,14 1,15	;PREVIOUS VALUE REGISTER ;SCRATCH PAD LOCATION ;SCRATCH PAD LOCATION ;SCRATCH PAD LOCATION ;VARIABLE VALUE, LOW DIGIT ;VARIABLE VALUE, MIDLE DIGIT ;VARIABLE VALUE, HIGH DIGIT	
3 4				; ;****	*****			
5 6 7				; ; progr ;	AM START			
.8 9				, ;**** ;	*****			
82 83 84	0003 0004	335F 0E 332A	POR:	CLRA OGI LBI ING	15 PV	;GET I	PORT TO 1'S SO THEY CAN BE USED AS INPUTS. NITIAL SETTING OF QUADRATURE DEVICE TORE IN PREVIOUS VALUE REGISTER	
6	0006 0007 0008	42		X RMB RMB	2 3	; MASK	HIGH 2 BITS	
B 9 0	0009 000A 000B 000C	1C 70 70		; LBI STII STII STII ;	VLD 0 0 2	; LOAD	MINIMIUM VALUE FOR THE VARIABLE	
	000D 000F	6A11 CD	IDLE:	JSR JP ;	QUAD IDLE	;CONTI	NUOUS LOOP TO CHECK THE INPUT	
8 9 0 1		0200		; = ;	X' 200	; FORCE	THE QUAD ROUTINE TO START AT HEX 200	
T				;			TL/DD/	111

54 55 56		;***** ; ;START C ; ;*****	F JUMP	TABLE FOR	PROCESSING INPUTS
57 58 59 0200 10 60 0201 28 61 0202 3D 62 0203 10 63 0204 3D 64 0205 10 65 0206 10 66 0207 28 67 0208 28 68 0209 10 69 020A 10 70 020B 3D 71 020C 10 72 020D 3D 73 020E 28	QUADJT:	; ADDR ADDR ADDR ADDR ADDR ADDR ADDR ADD	IGNOR ADD1 SUB1 IGNOR SUB1 IGNOR ADD1 IGNOR SUB1 IGNOR SUB1 IGNOR	; POINTEF ;0 ;1 ;2 ;3 ;4 ;5 ;6 ;7 ;8 ;9 ;4 ;9 ;2 ;2 ;2 ;2 ;2 ;2	VALUE IN HEX
74 020F 10 75			IGNOR		ND OF JUMP TABLE
76 77 0210 48 78 80 81 82 83	IGNOR:	; ;******	INPUT	TO CHECK	; BYPASS ANY ADDITIONAL PROCESSING FOR A CHANGE OF STATE
84 85 86 0211 3E 87 0212 332A 88 0214 06 90 0215 42 90 0216 43 91 0217 35 92 0218 21 93 0219 DB 94 021A 48 95	QUAD :	ING X RMB	SCR2 2 3 3		;GET CURRENT INPUT STATE ;AND MASK HIGH TWO BITS ;THEN COMPARE PREVOIUS AND CURRENT ;STATE ;COPY MASKED VALUE TO ACCUM. AND POINT ;TO PREVIOUS STATE. CHECK IF EQUAL ;THEY ARE EQUAL SO RETURN
96 97 021B 36 98 021C 06 99 021D 00 00 021E 31 01 021F 31 02 0220 31	QUAD2:		3		;EXCHANGE CURRENT AND PREVIOUS VALUES ;AND POINT TO SCRATCH LOCATION ;DO A LEFT SHIFT OF 2 BITS ;THIS FORMS THE 2 HIGH BITS OF THE ;JUMP POINTER TL/DD/1

03 0221 31 04		ADD		
05 0222 OE 06 0223 31 07		LBI ADD	PV	;NOW ADD THE CURRENT STATE THAT WAS JUST ;PROCESSED. BECOMES THE 2 LOW BITS OF ;THE JUMP POINTER
08 0224 3E 09 0225 06 10 0226 00		LBI X CLRA	SCR2	;SET UP THE POINTER FOR THE ;JUMP INDIRECT POINTER
11 12 0227 FF 13 14		; JID ; ;****	* * * *	;BRANCH TO REQUIRED ROUTINE
15 16 17		; ;ADD ;VALU	I TO THE V	ALUE OF THE VARIABLE AND CHECK FOR ITS MAX
18 19 20		; ;**** ;	****	
21 22 0228 1C 23 0229 22 24	ADD1:	LBI SC	VLD	;POINT TO LEAST SIGNIFICANT DIGIT ;USE CARRY TO ADD 1
25 022A 00 26 022B 56 27 022C 30 28 022D 4A		CLRA AISC ASC	6	;BCD CORRECTION
29 022E 04 30 022F EA		ADT XIS JP	ADD1L	;STORE DIGIT AND POINT TO NEXT DIGIT
31 0230 3C 32 0231 79 33 0232 74 34 0233 76		LBI STII STII STII	SCR0 9 4 6	;STORE VALUE TO CHECK FOR MAX VALUE
35 0234 6A53 36 0236 20		JSR SKC	ADDLIM	; IF CARRY IS SET ON RETURN FROM
37 0237 48 38 0238 1C 39 0239 70 40 023A 75 41 023B 73		RET LBI STII STII STII	VLD 0 5 3	;ADDLIM THEN THE VARIABLE IS LARGER ;THEN ITS MAXIMIUM VALUE ;SO RESET IT TO ITS MAX VALUE
42 023C 48 43 44		RET ; ;****	****	
45 46 47 48		;IT B	EING GREAT	ROM THE VARIABLE AND CHECK FOR FR THEN THE MINIMIUM VALUE.
49 50 51	SUB1:	;****;	****	
51 52 023D 1C 53 023E 32	50B1:	LBI RC	VLD	;SUBTRACT 1 BY FORCING A BORROW
				TL/DD/11

NATIONAL SEMICONDUCTOR CORPORATION COP400 CROSS ASSEMBLER, REV:D,8 MAY 85 QUAD SUB1L: 154 155 023F 00 CLRA 156 0240 10 157 0241 4A 158 0242 04 159 0243 623F 160 161 0245 3C 162 0246 70 163 0247 70 164 0248 78 165 0249 6A53 166 024C CE 169 024D 48 170 171 024E 1C 172 024F 70 173 0250 70 174 0251 72 175 0252 48 176 177 178 179 180 CASC ADT XIS ;BCD CORRECTION ;STORE DIGIT AND POINT TO NEXT DIGIT JMP SUB1L ; LBI STII SCR0 ;STORE VALUE TO CHECK LOWER LIMIT OF THE VARIABLE 0 0 8 STII STII JSR ADDLIM ; SKC JP RET ; IF CARRY IS NOT SET ON RETURN THEN VARIABLE ;LESS THEN IT'S MINIMIUM VALUE SUB2 SUB2: LBI STII STII STII ;FORCE VARIABLE TO ITS MIN VALUE VLD 0 0 2 RET ; ;******* ; ;ADD A VALUE STORED IN SCR0 TO SCR3 TO THE VALUE OF THE ;VARIABLE NONDESTRUCTIVELY. THE STATE OF THE CARRY BIT ;IS USED BY THE CALLING ROUTINE AS A RESULT. 182 183 ; ;****** 184 ; ADDLIM: 185 185 186 0253 1C 187 0254 32 188 189 0255 25 190 0256 56 191 0257 30 192 0258 40 193 0259 24 194 025A D5 195 025B 48 196 LBI VLD RC ADLIM1: ; BCD ADDITION OF 3 DIGITS LD 2 6 AISC ASC ADT ;PROCESS NEXT DIGIT XIS JP RET 2 ADLIM1 196 197 ; .END TL/DD/11147-12

8

NATIONAL SEMICONDUCTOR C COP400 CROSS ASSEMBLER, R QUAD SYMBOL TABLE		PAGE: 5		
ADD1 0228 ADD1L IDLE 000D IGNOR QUAD 0211 QUAD2 SCR1 003E * SCR2 SUB2 024E VHD NO ERROR LINES 108 ROM BYTES USED COP 420 ASSEMBLY SOURCE CHECKSUM = 2177 OBJECT CHECKSUM = 01FF INPUT FILE C:QUAD.MAC LISTING FILE C:QUAD.PRN OBJECT FILE C:QUAD.LM	022A ADDLIM 0210 POR 021B QUADJT 003F SUB1 001F * VLD	0000 * PV	0255 000F 003D 023F 001E *	TL/DD/11147-13

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