

# DM365 Digital Video Recorder Reference Design: DM365DVR-UD1 @ U.S. \$1,195



TI has brought to market a single platform, H.264 reference design based on the TMS320DM365 digital media processor with DaVinci™ technology and the TI TVP5158 multi-channel video decoder for faster development at a reduced cost.

## Hardware features

- TI TMS320DM365 digital media processor based on DaVinci™ technology
- TI TVP5158, new multi-channel video decoder with integrated audio
- Storage of compressed input (SATA and USB)



▲ DM365DVR-UD1 Reference Design available from Texas Instruments

- Streaming of compressed input (Ethernet)
- Local display support up to 800×600 resolution
- Local user interface support
- Pan, tilt and zoom camera support

## Software features

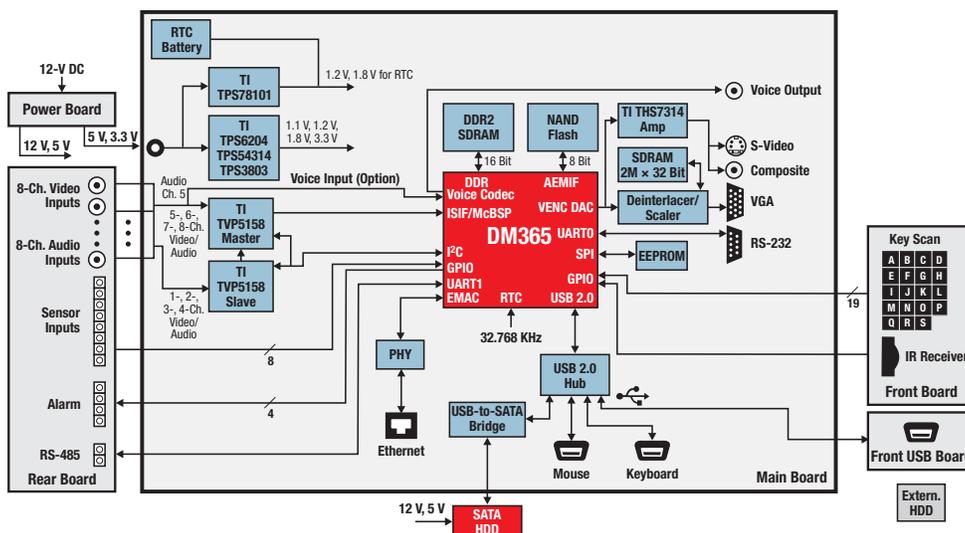
- Multi-codec system allows triple stream per channel (H.264, MPEG-4 and MJPEG) for real-time signal processing
- Simultaneous record (65 fps at D1 or 260 fps at CIF), playback (30 fps), storage, streaming and display
- Audio/video adjustment tools
- Video timestamp support
- Software Development Kit (SDK) provided for easy customization

TI's new TVP5158 multi-channel video decoder provides improved image quality. Features include:

- 4-ch NTSC/PAL video decoder with robust auto detection

- Features for each channel:
  - Independent scalers (half D1/ CIF)
  - One 10-bit ADC with 2× over-sampling
  - Patented 2-D 5-line adaptive comb filter with high-quality video
  - Integrated anti-aliasing filter
  - Advanced features: Noise reduction and auto contrast
- Multiplexed video output (single-sampling clock – byte or line mode)
- Supports 8-bit BT.656, dual/quad 8-bit BT.656, and 16-bit BT.601 compatible interfaces
- Video/Audio cascade connection
- 2 stereo or 4 mono audio ADCs
- TDM (time-division-multiplexed) audio output

For more information about this reference design, go to [www.ti.com/dvr](http://www.ti.com/dvr)



◀ Digital Video Recorder reference design block diagram: DM365DVR-UD1