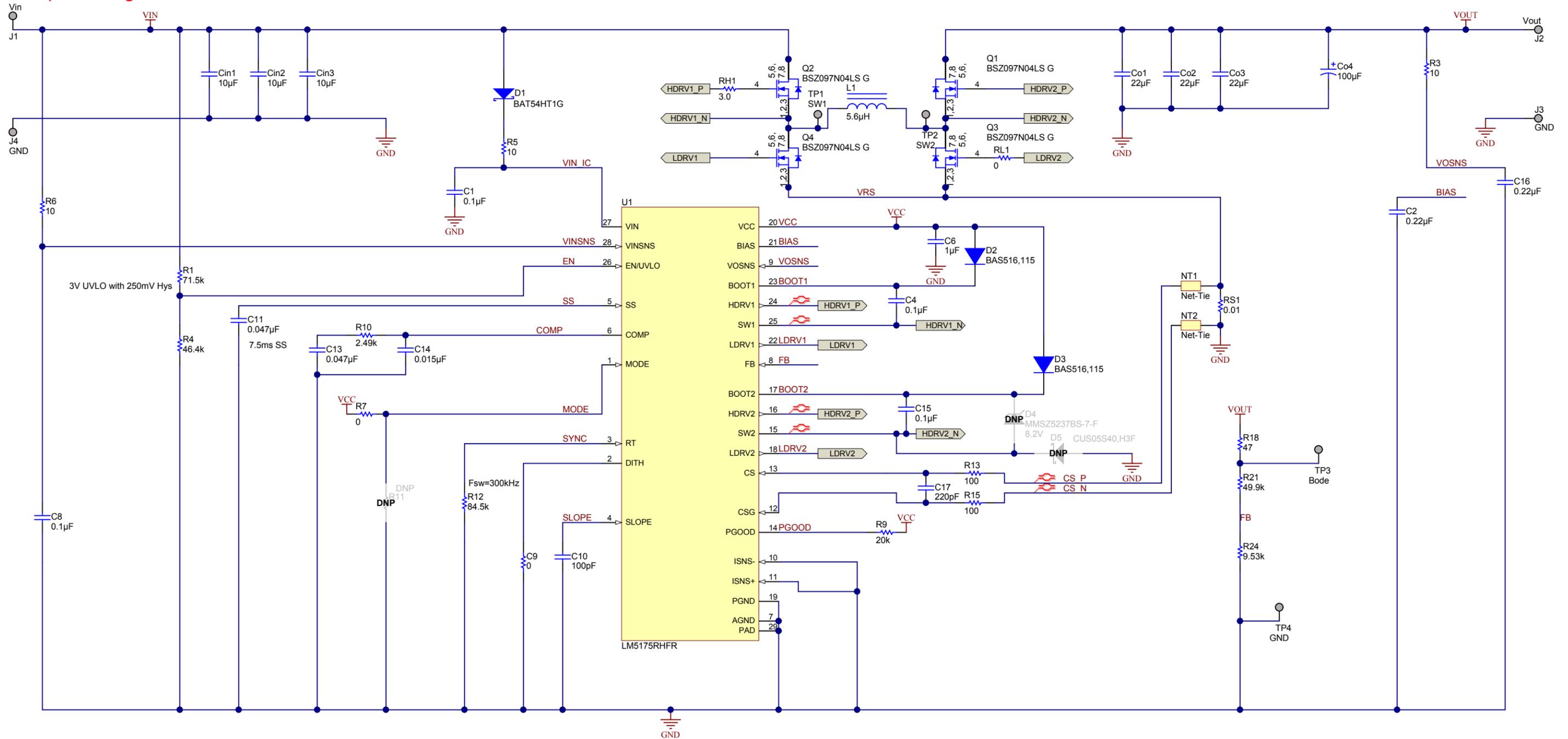
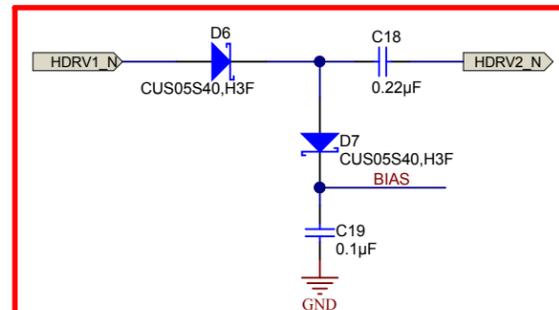


Input Voltage = 3.5V to 30V

Vout = 5V @ 3A



Charge pump voltage doubler for VCC bias



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TID #: PMP20107	Project Title: LM5175 QFN 5V@3A Ultra Small Solution Size	
Number: PMP20107	Rev: A	Sheet Title:
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 2
Drawn By:	File: PMP20107_REVA_SchDoc	Size: B
Engineer: Xinyu Dai	Contact: http://www.ti.com/support	



PCB Number: PMP20107
PCB Rev: A

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Label Table	
Variant	Label Text
001	

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: N/A	Designed for: Public Release	Mod. Date: 4/11/2016	 http://www.ti.com © Texas Instruments 2016
TID #: PMP20107	Project Title: LM5175 QFN 5V@3A Ultra Small Solution Size		
Number: PMP20107	Rev: A	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 2	
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Engineer: Xinyu Dai	Contact: http://www.ti.com/support		

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