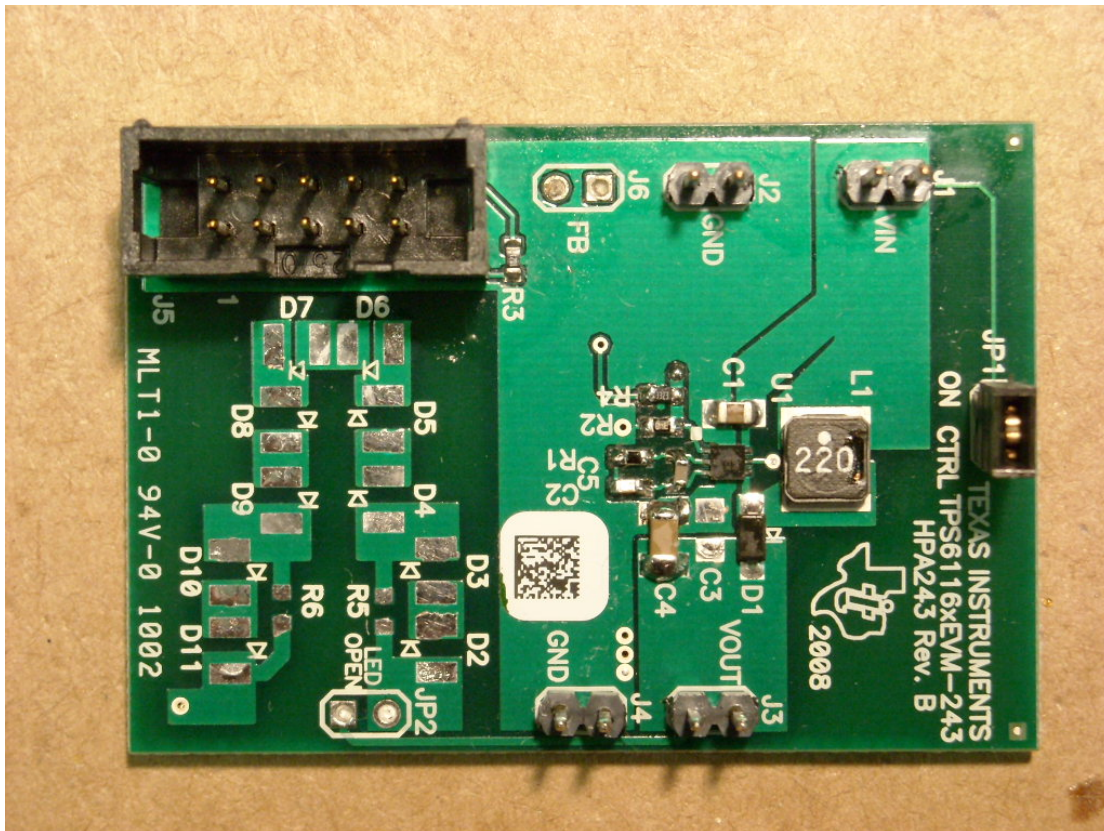


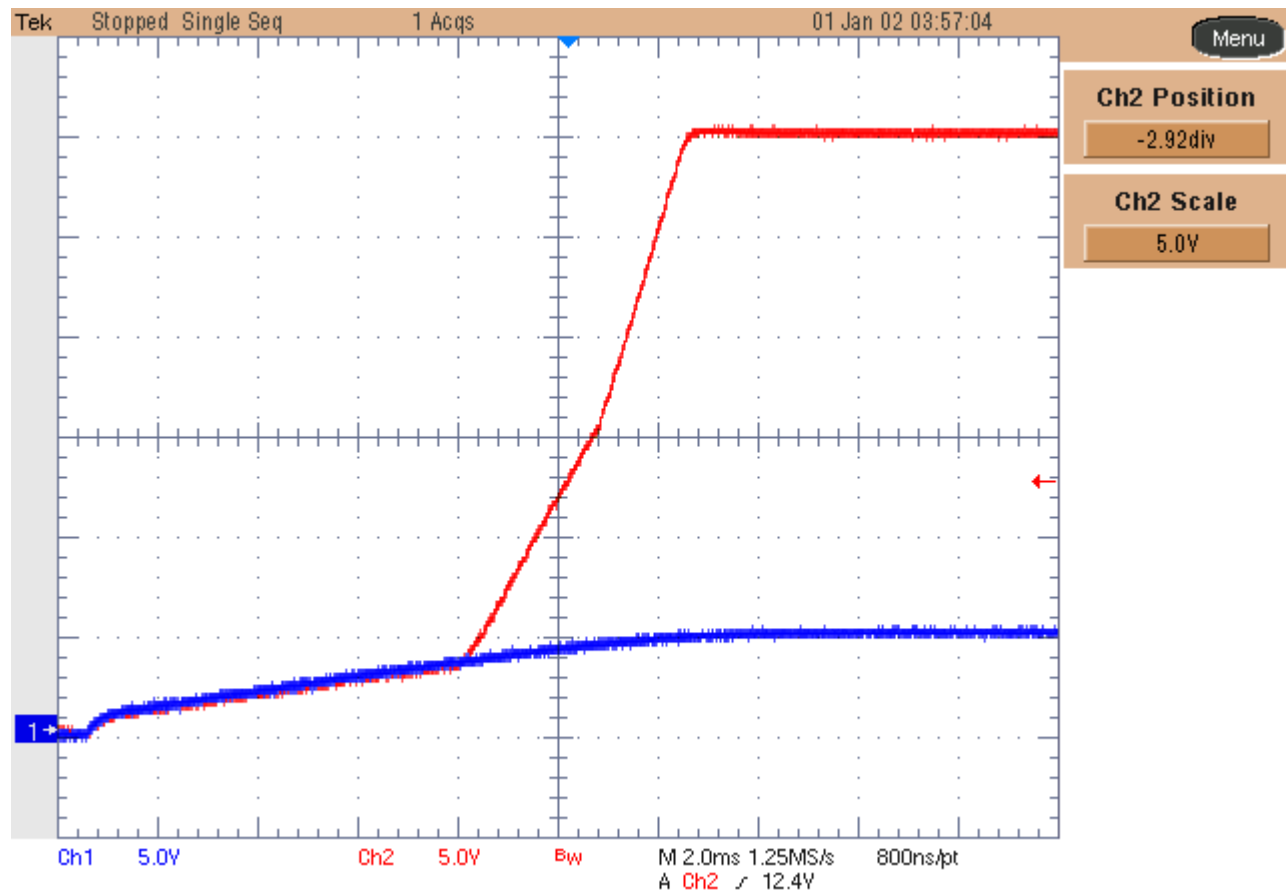
## 1. PHOTO OF THE PROTOTYPE



## 2. Startup

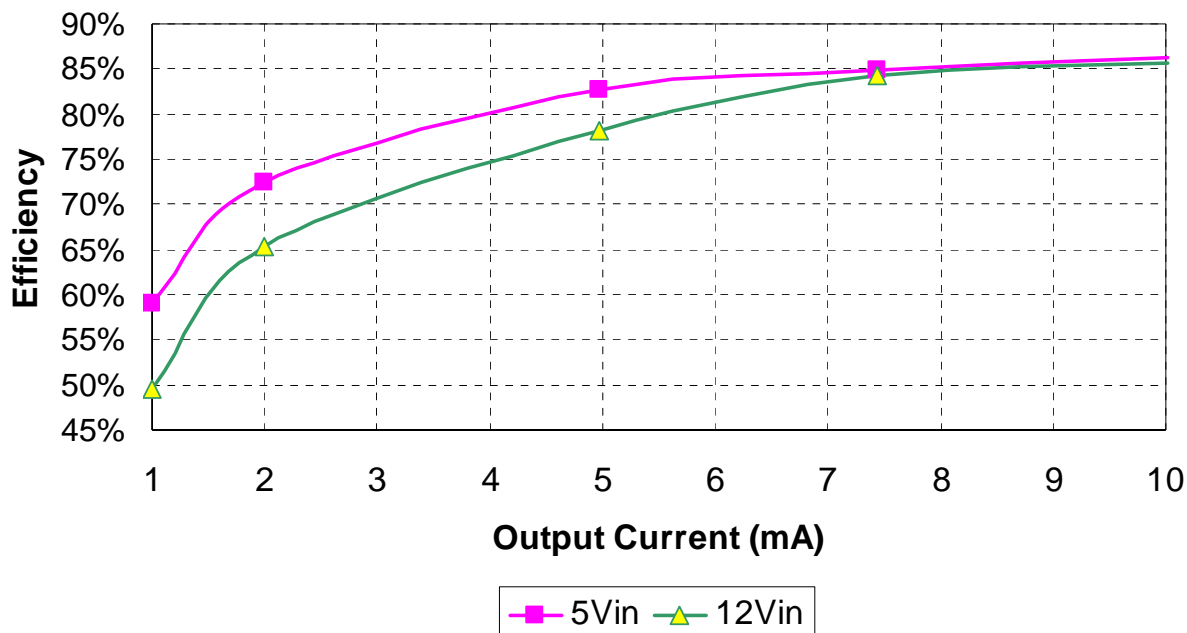
The output voltage at startup is shown in the image below. The input voltage was set to 5V. The load was set to 10mA. There was no difference when the converter was unloaded.

**Ch1: Input Voltage (5 V/div, msec/div), Ch2: Output Voltage (5 V/div, 20MHz BWL)**



### 3. Efficiency

The efficiency data is shown in the tables and graph below. The measurements were taken at 5V and 12V input voltage. The load was a resistor, set to the values shown in the tables. The tables show that the output voltage doesn't change vs. input voltage and load variation.



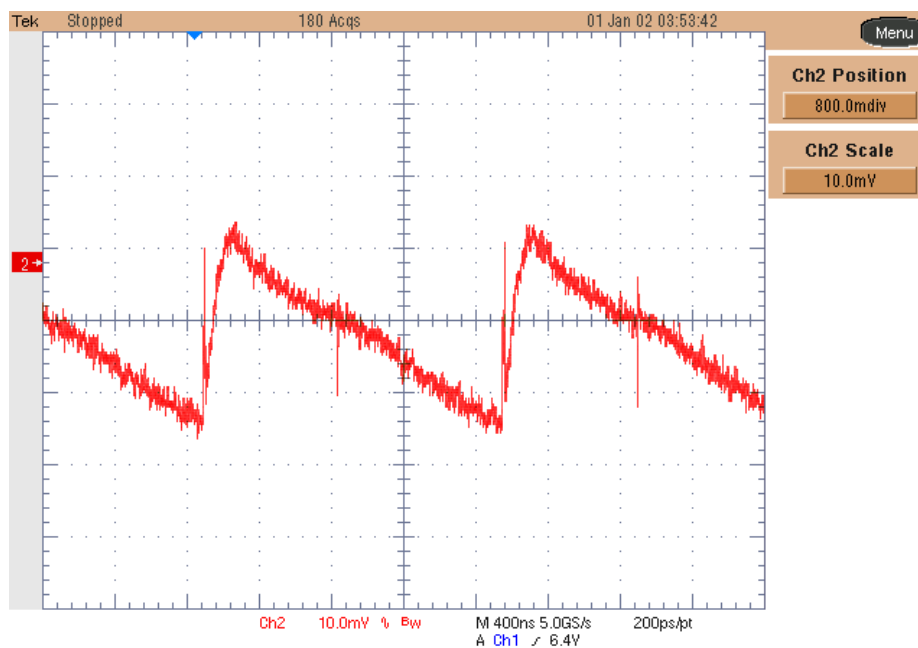
| V <sub>in</sub> (V) | I <sub>in</sub> (mA) | V <sub>out</sub> (V) | I <sub>out</sub> (mA) | R <sub>load</sub> (Kohm) | P <sub>out</sub> (mW) | P <sub>in</sub> (mW) | P <sub>loss</sub> (mW) | Effic. (%) |
|---------------------|----------------------|----------------------|-----------------------|--------------------------|-----------------------|----------------------|------------------------|------------|
| 5.033               | 3.23                 | 29.89                | 0.0                   | 1000                     | 0.0                   | 16.3                 | 16.26                  | 0.0%       |
| 5.025               | 10.03                | 29.89                | 1.00                  | 30.0                     | 29.8                  | 50.4                 | 20.62                  | 59.1%      |
| 5.017               | 16.38                | 29.89                | 1.99                  | 15.0                     | 59.6                  | 82.2                 | 22.62                  | 72.5%      |
| 5.027               | 35.72                | 29.89                | 4.97                  | 6.02                     | 148.4                 | 179.6                | 31.16                  | 82.6%      |
| 5.006               | 52.3                 | 29.89                | 7.44                  | 4.02                     | 222.2                 | 261.8                | 39.57                  | 84.9%      |
| 5.015               | 69.1                 | 29.89                | 10.01                 | 2.986                    | 299.2                 | 346.5                | 47.34                  | 86.3%      |

| V <sub>in</sub> (V) | I <sub>in</sub> (mA) | V <sub>out</sub> (V) | I <sub>out</sub> (mA) | R <sub>load</sub> (Kohm) | P <sub>out</sub> (mW) | P <sub>in</sub> (mW) | P <sub>loss</sub> (mW) | Effic. (%) |
|---------------------|----------------------|----------------------|-----------------------|--------------------------|-----------------------|----------------------|------------------------|------------|
| 12.02               | 1.92                 | 29.89                | 0.0                   | 1000                     | 0.0                   | 23.1                 | 23.08                  | 0.0%       |
| 12.01               | 5.01                 | 29.89                | 1.00                  | 30.0                     | 29.8                  | 60.2                 | 30.39                  | 49.5%      |
| 12.01               | 7.60                 | 29.89                | 1.99                  | 15.0                     | 59.6                  | 91.3                 | 31.72                  | 65.3%      |
| 12.00               | 15.81                | 29.89                | 4.97                  | 6.02                     | 148.4                 | 189.7                | 41.31                  | 78.2%      |
| 12.01               | 21.97                | 29.89                | 7.44                  | 4.02                     | 222.2                 | 263.9                | 41.62                  | 84.2%      |
| 12.01               | 29.08                | 29.89                | 10.01                 | 2.986                    | 299.2                 | 349.3                | 50.05                  | 85.7%      |

#### 4. Output Ripple Voltage

The output ripple voltage waveform measured at the terminal jumpers J3 and J4 is shown in the plot below. The input was set to 5V and the outputs to 10mA. The bandwidth limit has been set to 150MHz.

**Ch2: Output ripple voltage (10 mV/div, 400 nsec/div, AC coupled, 150MHz BW limit).**

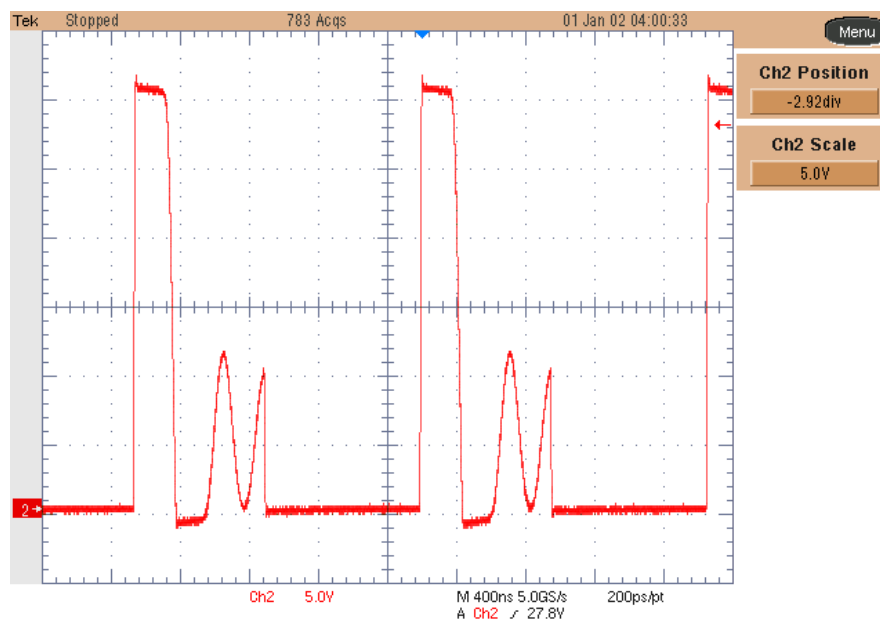


## 5. Switching Waveforms

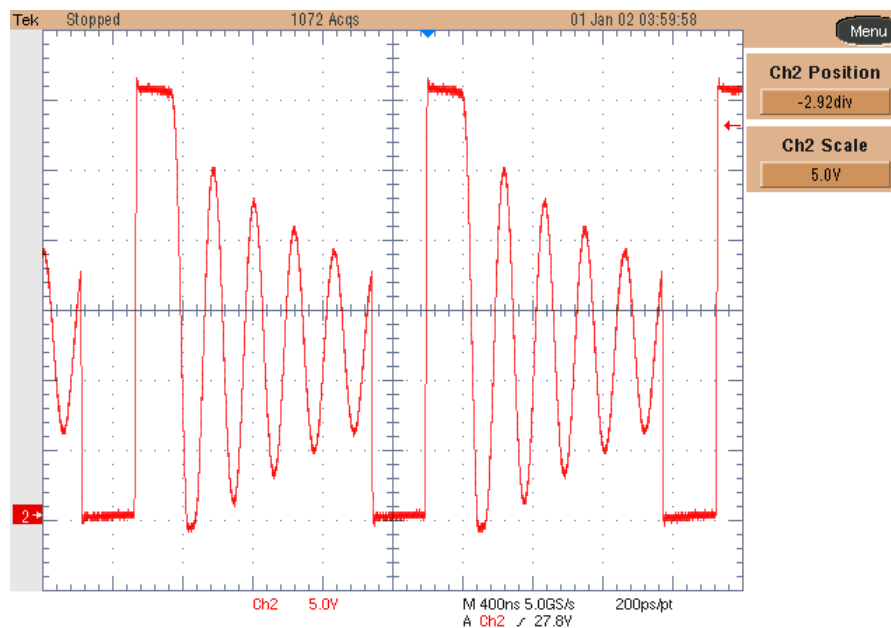
The images below show the waveforms of the SW pin of U1 at full load.

**Ch2: SW node, (5V/div, 400nsec/div), no bandwidth limit**

**5Vin:**



**12Vin:**



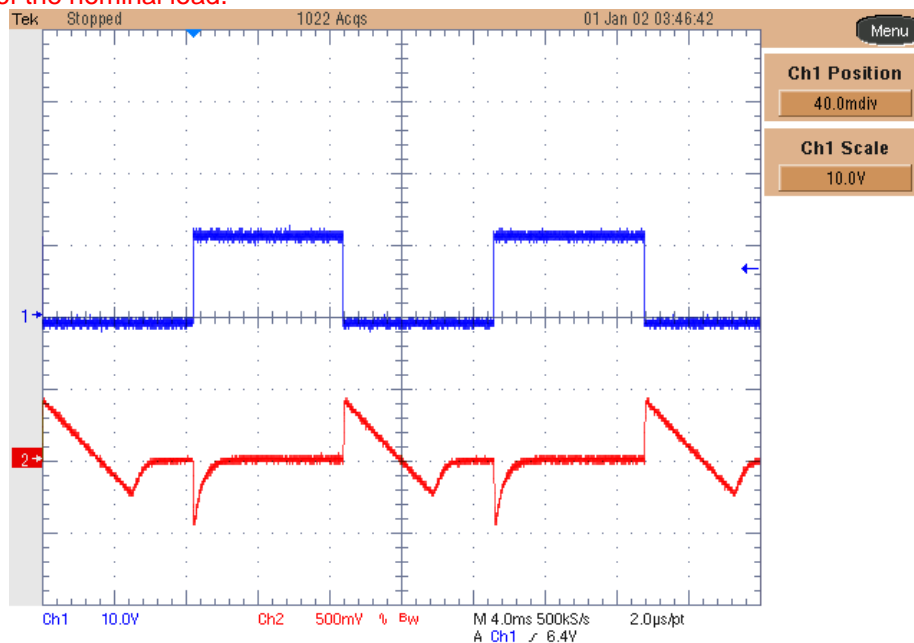
## 6. Transient Response

The images below show the transient response of the converter. The input voltage was set to 5V and the load switched between 0 and 10mA (top image) and 1mA to 11mA (bottom image), by means of a 3KOhm resistor in series with a MOSFET. The control signal is shown as well.

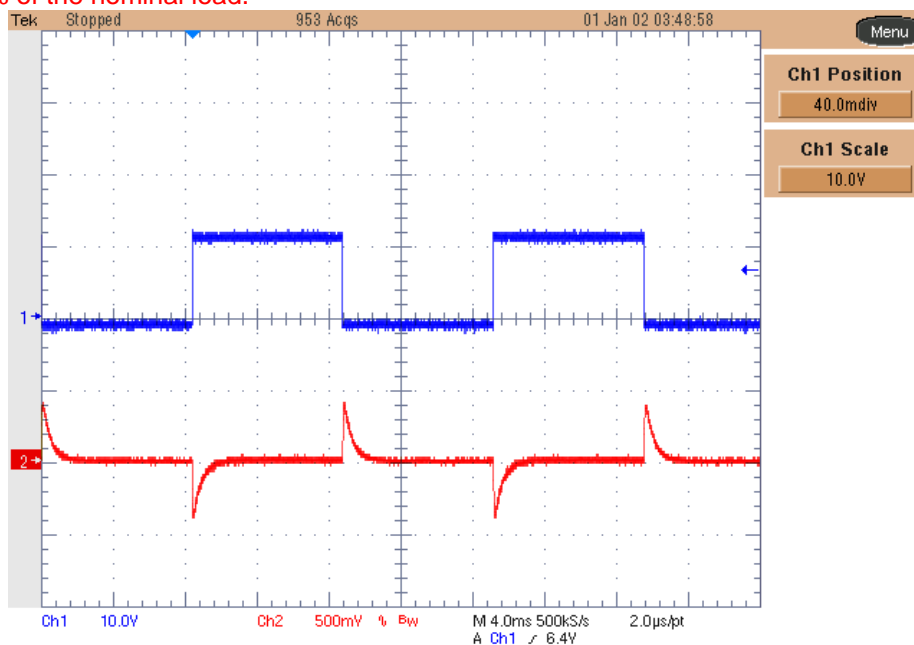
**Channel 1: External Mosfet Gate Voltage (10V/div, 4msec/div)**

**Channel 2: Output Voltage (500mV/div, AC coupled, 20MHz BWL)**

0% to 100% of the nominal load.



10% to 110% of the nominal load.



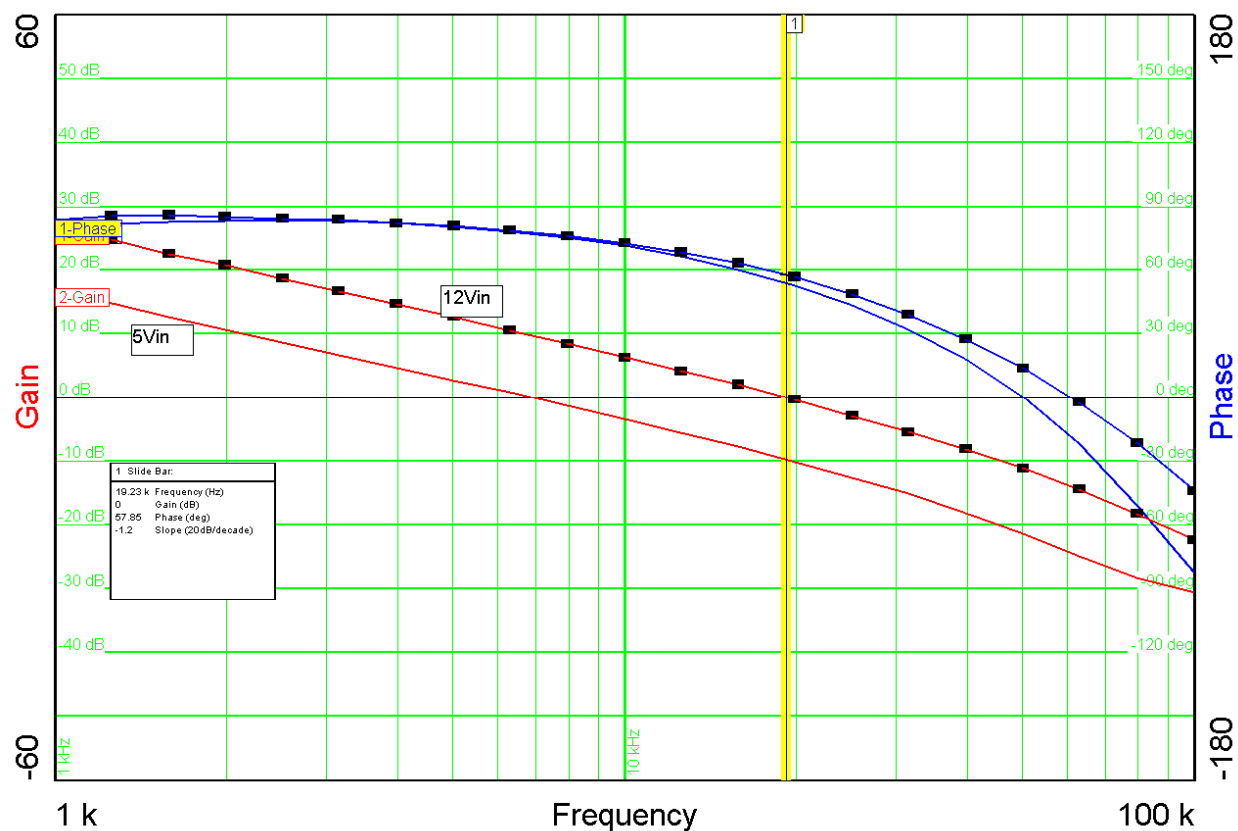
## 7. Loop Analysis

The graph below shows the loop measurement at 5Vin and 12Vin while the load was 10mA.

The crossover frequency and phase margin was:

@ 5Vin: Fco = 6.85 KHz, Phase margin = 77.22 deg.

@ 12Vin: Fco = 19.23 KHz, Phase margin = 57.85 deg.



**For Feasibility Evaluation Only, in Laboratory/Development Environments.** The EVM is not a complete product. It is intended solely for use for preliminary feasibility evaluation in laboratory / development environments by technically qualified electronics experts who are familiar with the dangers and application risks associated with handling electrical / mechanical components, systems and subsystems. It should not be used as all or part of a production unit.

**Your Sole Responsibility and Risk.** You acknowledge, represent and agree that:

1. You have unique knowledge concerning Federal, State and local regulatory requirements (including but not limited to Food and Drug Administration regulations, if applicable) which relate to your products and which relate to your use (and/or that of your employees, affiliates, contractors or designees) of the EVM for evaluation, testing and other purposes.
2. You have full and exclusive responsibility to assure the safety and compliance of your products with all such laws and other applicable regulatory requirements, and also to assure the safety of any activities to be conducted by you and/or your employees, affiliates, contractors or designees, using the EVM. Further, you are responsible to assure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard.
3. Since the EVM is not a completed product, it may not meet all applicable regulatory and safety compliance standards (such as UL, CSA, VDE, CE, RoHS and WEEE) which may normally be associated with similar items. You assume full responsibility to determine and/or assure compliance with any such standards and related certifications as may be applicable. You will employ reasonable safeguards to ensure that your use of the EVM will not result in any property damage, injury or death, even if the EVM should fail to perform as described or expected.

**Certain Instructions.** Exceeding the specified EVM ratings (including but not limited to input and output voltage, current, power, and environmental ranges) may cause property damage, personal injury or death. If there are questions concerning these ratings please contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, some circuit components may have case temperatures greater than 60°C as long as the input and output ranges are maintained at nominal ambient operating temperature. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors which can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during normal operation, please be aware that these devices may be very warm to the touch.

**Agreement to Defend, Indemnify and Hold Harmless.** You agree to defend, indemnify and hold TI, its licensors and their representatives harmless from and against any and all claims, damages, losses, expenses, costs and liabilities (collectively, "Claims") arising out of or in connection with any use of the EVM that is not in accordance with the terms of this agreement. This obligation shall apply whether Claims arise under the law of tort or contract or any other legal theory, and even if the EVM fails to perform as described or expected.

**Safety-Critical or Life-Critical Applications.** If you intend to evaluate TI components for possible use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, such as devices which are classified as FDA Class III or similar classification, then you must specifically notify TI of such intent and enter into a separate Assurance and Indemnity Agreement.



## IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (<https://www.ti.com/legal/termsofsale.html>) or other applicable terms available either on [ti.com](https://www.ti.com) or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265  
Copyright © 2021, Texas Instruments Incorporated