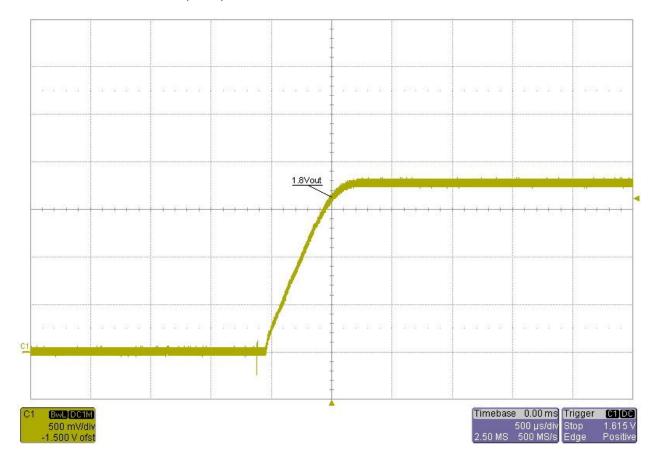


1 Startup

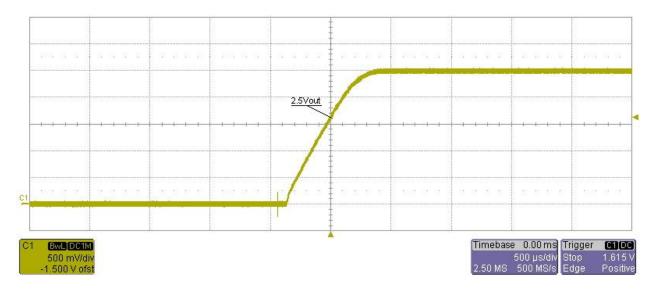
1.1 1.8Vout:

Input voltage = 5VDC



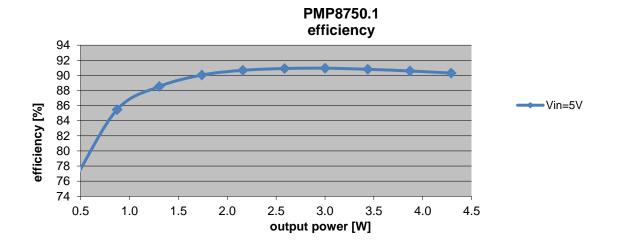


Input voltage = 5VDC



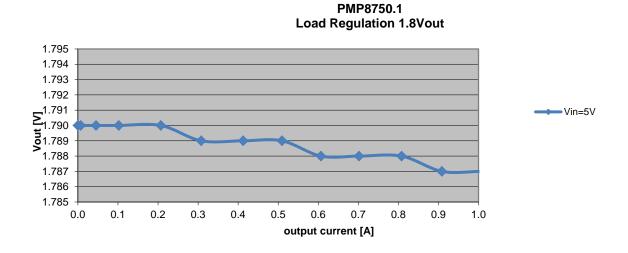


2 Efficiency



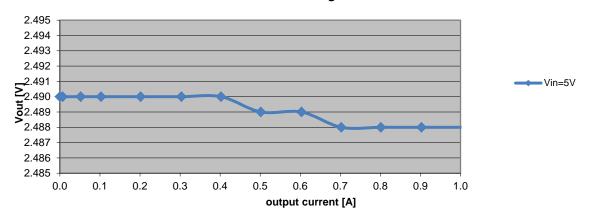
3 Load regulation

3.1 1.8Vout:





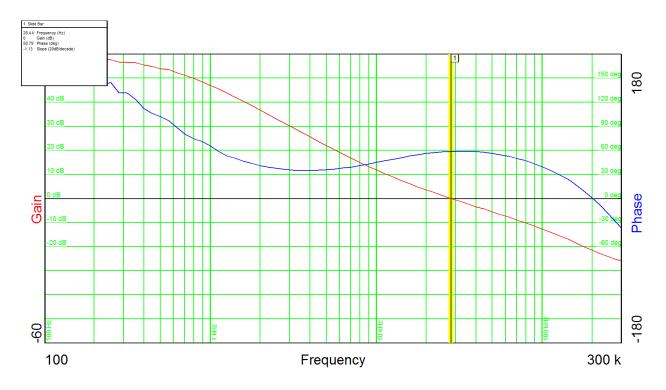
PMP8750 Load Regulation 2.5Vout





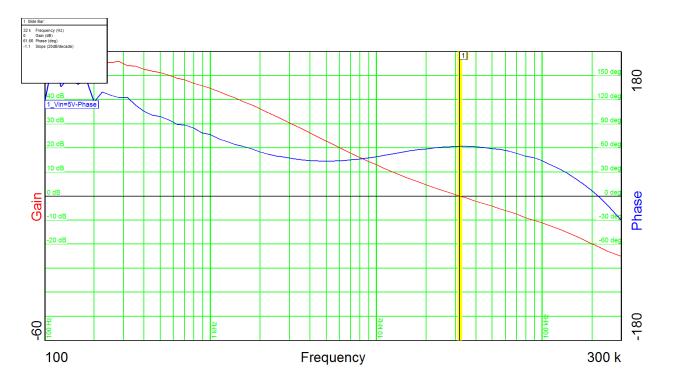
4 Control Loop Frequency Response

4.1 1.8Vout:



Output power= 1.8V@1.0AInput voltage= 5VDCPhase margin $= 59^{\circ}$ Bandwidth= 28.4kHz





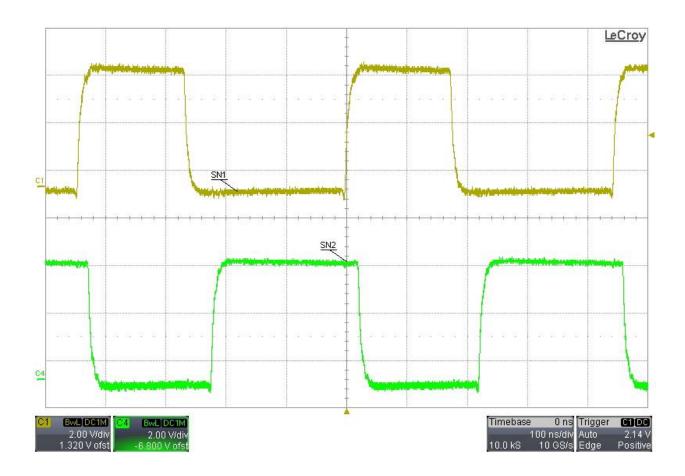
Output power = 2.5V@1.0AInput voltage = 5VDCPhase margin $= 62^{\circ}$ Bandwidth = 32.0kHz



5 Switch Nodes

Input voltage = 5VDC

Load current = full load (1.8V@1.0A / 2.5V@1.0A)

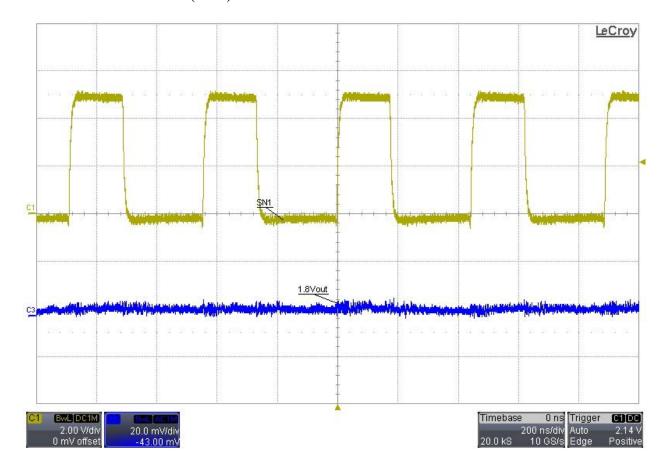




6 Output ripple voltage

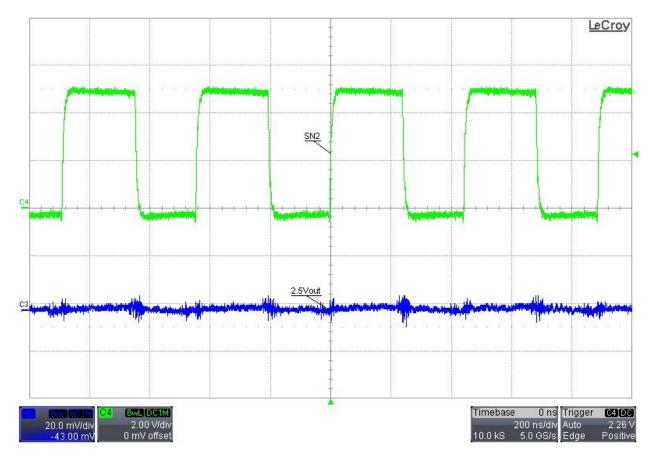
6.1 1.8Vout:

Input voltage = 5VDC





Input voltage = 5VDC



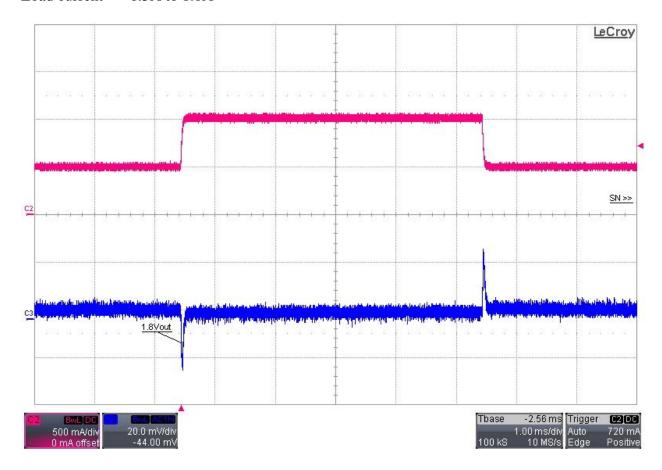


7 Load Transients

7.1 1.8Vout:

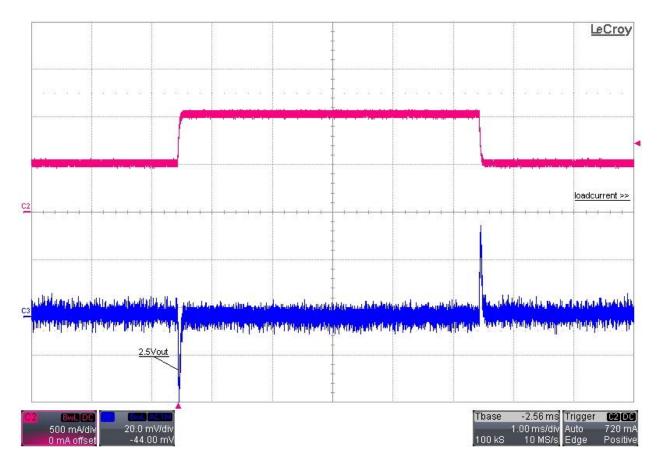
Input voltage = 5VDC

Load current = 0.5A to 1.0A





Input voltage = 5VDC Load current = 0.5A to 1.0A



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