### Power, Interface and Clock Solutions for the TED Spartan-6 FPGA Consumer Video Kit 2.0

**Device** | **Description**
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CDCLVD1204 | Low-jitter, 2-input selectable 1:4 universal-to-LVDS buffer
CDCLVD2102 | Low-jitter, dual 1:2 universal-to-LVDS buffer
CDCM61001 | 1:1 ultra-low-jitter crystal-in clock generator
SN65LVDS25Q | 2.0-Gbps 4x4 crosspoint switch
TLV70033 | 200-mA, low IQ, low dropout regulator for portables
TPS3860000000 | Quad supply voltage supervisors with programmable delay and watchdog timer
TPS51200 | Sink/source DDR termination regulator
TPS51315 | Low input voltage 3-A synchronous buck converter with 2.5-V output
TPS54425 | 4.5-V to 18-V input, 4-A synchronous step-down SWIFT™ converter with D-CAP2™ mode
TPS715A01 | Single-output LDO, 80-mA, adj. (1.2 to 15 V), high input voltage, low quiescent current
TPS74201 | Single-output LDO, 1.5-A, adj. (0.8 to 3.3 V), any or no cap, programmable softstart
TUSB3410 | RS232/IrDA serial-to-USB converter

**Japanese**

| Device | Description |
---|---|
CDCLVD1204 | 低ジッタ、2入力、選択可能1:4 LVDS バッファ |
CDCLVD2102 | 低ジッタ、デュアル1:2 LVDS バッファ |
CDCM61001 | 1:1、超低ジッタ、クリスタル入力、クロック・ジェネレータ |
SN65LVDS25Q | 2.0-Gbps 4x4 クロスポイント・スイッチ |
TLV70033 | 200-mA、低IQ、低ドロップアウト・レギュレータ、ポータブル向け |
TPS3860000000 | 4 Ch 電源電圧監視IC、プログラムマップ遅延、ウォッチ・ドッグ・タイマ付 |
TPS51315 | 低入力電圧3A同期整流降圧型コンバータ、2.5V出力内蔵 |
TPS54425 | 4.5V～18V入力、4A、同期整流、降圧SWIFT™コンバータ、DCAP2™モード付 |
TPS715A01 | シングル出力LDO、80mA、可変(1.2～15V)、高入力電圧、低静止電流 |
TPS74201 | シングル出力LDO、1.5A、可変(0.8～3.6V)、各種コンデンサまたは不要、プログラムマップソフトスタート |
TUSB3410 | RS232/IrDA シリアル/USB コンバータ |

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**Diagram**

[Diagram of the CVK2.0 board with various components labeled and connected.]

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**Website**

www.ti.com/xilinxfpga

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Sample Now!
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