

TI Designs – Precision: Verified Design ±100 A Closed-Loop Current Sensor Reference Design using Bi-Polar Supplies



TI Designs – Precision

TI Designs – Precision are analog solutions created by TI's analog experts. Verified Designs offer the theory, component selection, simulation, complete PCB schematic & layout, bill of materials, and measured performance of useful circuits. Circuit modifications that help to meet alternate design goals are also discussed.

Circuit Description

This dual-supply closed loop current sensor solution is designed to accurately measure dc, ac and pulsed currents to ±100 A with galvanic isolation between the primary and secondary circuits. The linear range of the output is ±2.5 V using a supply voltage of ±6 V to ±15 V.

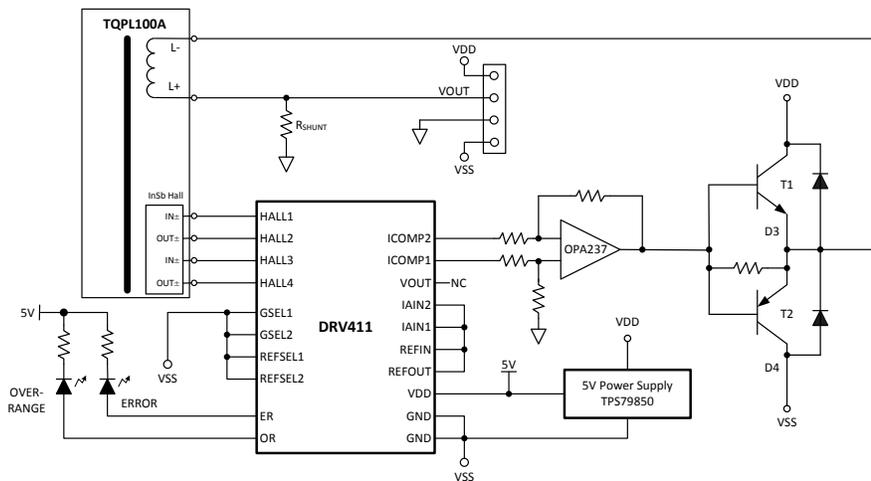
Design Resources

- [Archive](#)
- [TINA-TI™](#)
- [DRV411](#)
- [OPA237](#)
- [TPS79850](#)

- All Design files
- SPICE Simulator
- Product Folder
- Product Folder
- Product Folder



[Ask The Analog Experts](#)
[WEBENCH® Design Center](#)
[TI Designs – Precision Library](#)



An IMPORTANT NOTICE at the end of this TI reference design addresses authorized use, intellectual property matters and other important disclaimers and information.

TINA-TI is a trademark of Texas Instruments
WEBENCH is a registered trademark of Texas Instruments

1 Design Summary

The design requirements are as follows:

- Supply Voltage: $\pm 6\text{ V}$ to $\pm 15\text{ V}$
- Input: $\pm 100\text{ A}$
- Conversion Ratio 1:2000
- Output: $-2.5\text{ V} - +2.5\text{ V}$
- Maximum Shunt Voltage: 2.5 V

The design goals and performance are summarized in Table 1. Figure 1 depicts the measured transfer function of the design.

Table 1. Comparison of Design Goals, Calculations, and Measured Performance

	Goal	Calculated	Measured
Error (%FSR)	$\pm 0.2\%$	0.14%	$\pm 0.07\%$
Offset	$\pm 7\text{ mV}$	$\pm 7.14\text{ mV}$	3.4 mV

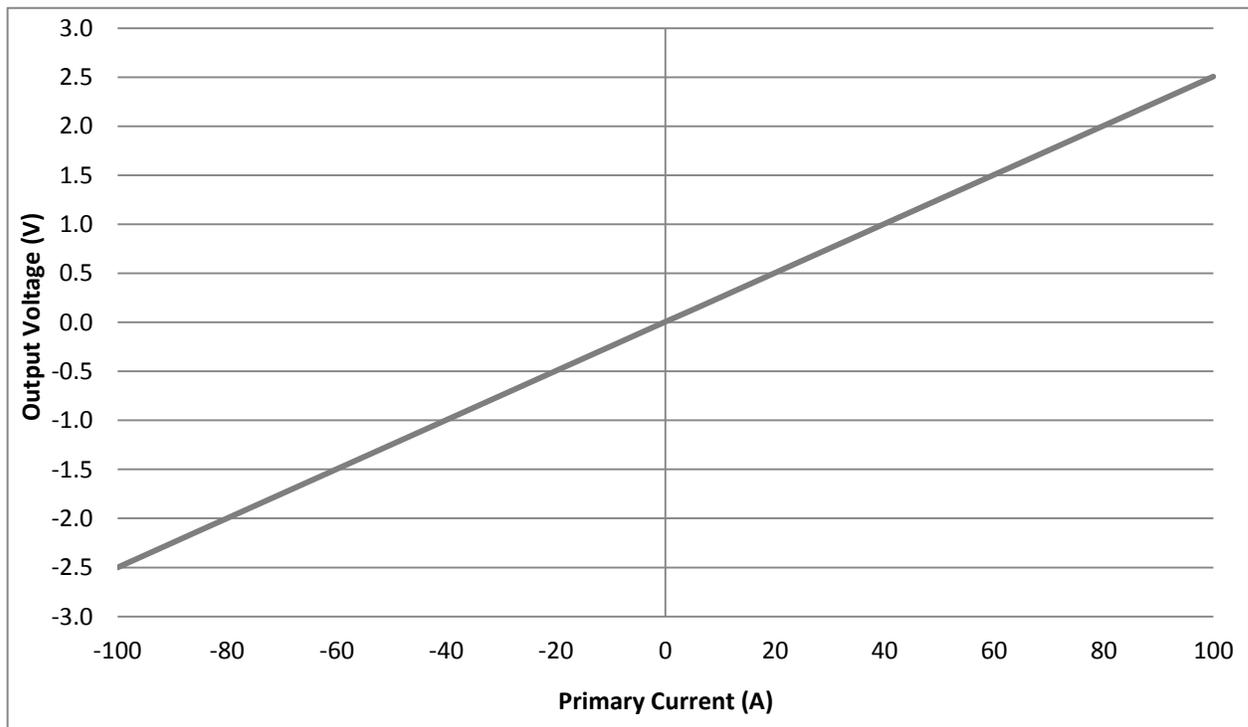


Figure 1: Measured Transfer Function

2 Theory of Operation

Closed-loop current sensors use a Ferromagnetic core with a sensing element or field probe inserted into a gap in the core. The core picks up the magnetic field created by the current flowing through the primary winding. Changes in the magnetic field are measured by the sensing element and passed on to a signal-conditioning stage for filtering and amplification. The external drive stage provides current to the compensation coil, which creates an opposing magnetic field that cancels the effect of the primary current. The resulting voltage output is proportional to the current flowing through the primary winding as shown in the transfer function defined in Equation 1.

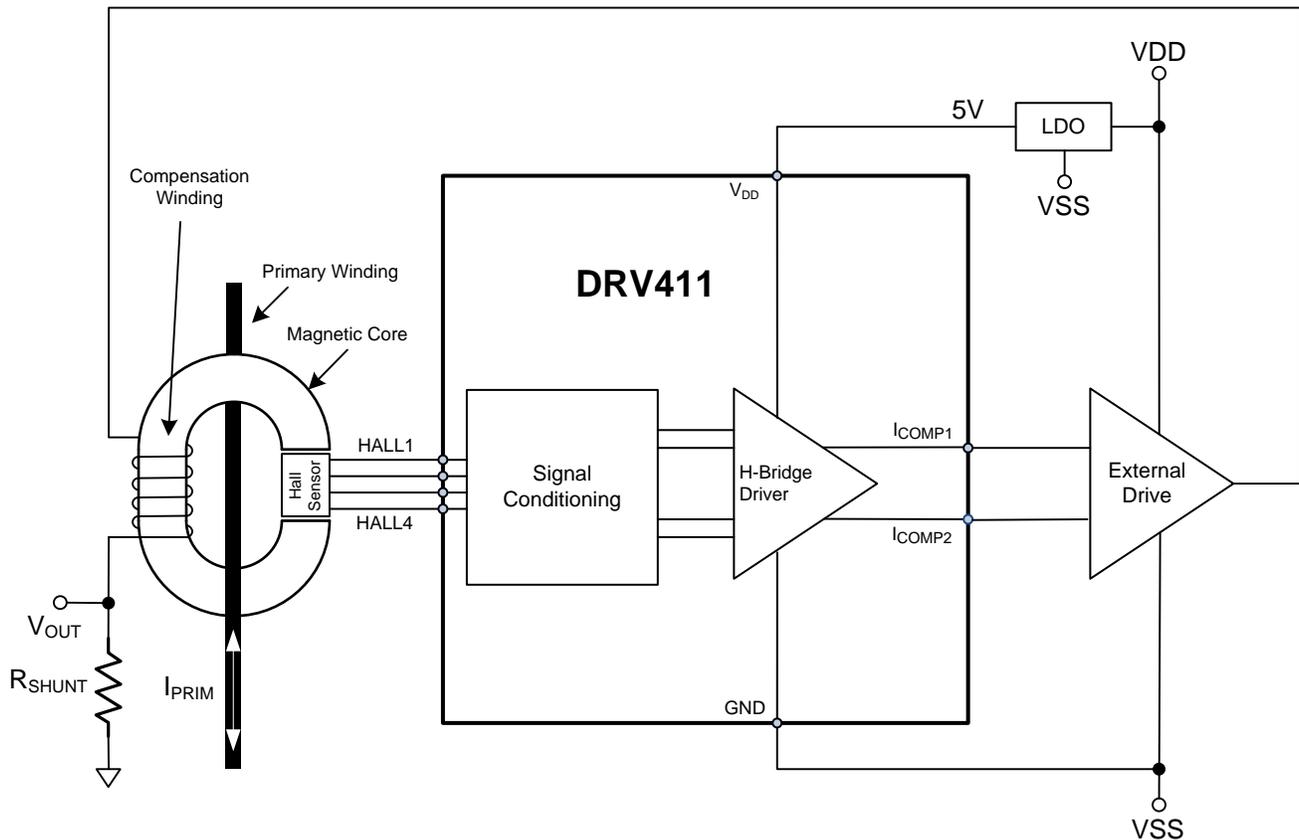


Figure 2: Closed-Loop Sensor Block Diagram

$$V_{OUT} = I_{PRIM} \times \left(\frac{N_P}{N_S} \right) \times R_{SHUNT} \quad (1)$$

2.1 Potential Sources of Output Error

The physical construction of the circuit and the component tolerances will introduce error in the transfer function of the sensor. Several sources of errors are discussed in the following sections.

2.1.1 Hall Effect Sensor

In closed-loop sensors, the Hall sensor voltage is amplified causing a current to flow through the compensation coil wound around the magnetic core. This action generates magnetism with amplitude that is the same magnitude but in the opposite direction to that of the primary current conductor, compensating the flux in the core to zero. The nonlinearity and temperature dependence of the Hall element are therefore compensated, but the offset remains. The offset of a quality indium antimonide (InSb) Hall element is typically ± 7 mV.

2.1.2 External Drive Amplifier

The external drive amplifier's offset voltage (V_{OS}) will be a contributing source of offset error in this design. The magnitude of offset error at the output will equal the product of the amplifier's gain and offset voltage along with the offset voltage from the Hall element. Ideally, the offset error is linear across the entire input range and appears as a vertical shift (up or down) in the transfer function. This vertical shift can be calibrated out at the expense of dynamic range.

2.1.3 Shunt Resistor

The shunt or sense resistor will have an impact on the overall gain of the sensor. To improve the circuit performance, use a good-quality sense resistor properly power sized to reduce the effects of self-heating.

3 Component Selection

3.1 Hall Signal Conditioning

The DRV411 is used in this design to drive the hall element and provide an output voltage that is proportional to the input current to an external bi-polar drive stage. The DRV411 employs a current spinning technique that effectively removes the offset and temperature drift errors inherent in the Hall element. The H-Bridge driver stage of the DRV411 is connected to an OPA237, which is powered from ± 6 V to ± 15 V. The differential amplifier stage provides gain of 4 V/V to a matched pair of bipolar junction transistors that drive the compensation coil.

3.2 5V LDO Selection

The TPS79850-Q1 low-dropout (LDO) linear regulator was chosen for this reference design because of its wide input voltage range, low-dropout voltage, and low quiescent current. The current consumption of the DRV411 is typically 12 mA. With a sensor power supply range of ± 6 V to ± 18 V (MAX), the TPS79850-Q1 can easily provide the power required by the DRV411. In the DGN package, the power dissipated (P_{DISS}) is defined in Equation 2:

$$I_{OUT(MAX)} \times (V_{IN(MAX)} - V_{OUT}) + I_{GND} \times (V_{IN(MAX)}) \quad (2)$$

Where:

$$I_{OUT(MAX)} = 13 \text{ mA}$$

$$V_{IN(MAX)} = 36 \text{ V}$$

$$V_{OUT} = 5 \text{ V}$$

$$I_{GND} \text{ at } (I_{OUT} = 13 \text{ mA}, V_{IN} = 36 \text{ V}) = 1 \text{ mA}$$

Therefore:

$$P_{DISS} = 13 \text{ mA} \times (36 \text{ V} - 5 \text{ V}) + 1 \text{ mA} \times (36 \text{ V}) = 0.0364 \text{ W} \quad (3)$$

The junction temperature rise above ambient will be approximately equal to the following:
 $0.0364 \text{ W} \times 60^\circ \text{ C/W}$ or 2.18° C .

3.3 Sensor Selection

The passive sensor in this design has two components: a compensation coil and a Hall element. A typical compensation winding found in a 100-A current sensor has 2000 turns (N_s). With one turn through the primary (N_p) carrying 100 A (I_p), the resulting current conversion ratio is 1:2000. The resulting secondary current (I_s) is 50 mA as shown in Equation 4.

$$I_s = \frac{I_p \times N_p}{N_s} = \frac{100A \times 1}{2000} = 50mA \quad (4)$$

In precision current sensors, the field probe is often a linear hall device. The TQPL100A from Topstek is a passive sensor that houses a 2000-turn compensation coil and an indium antimonide (InSb) Hall Effect device.

3.4 Shunt Resistor Selection

A shunt resistor placed in series with the compensation winding current path is needed to provide a voltage to the output of the sensor. Selection of an appropriate shunt resistor is dependent on the amount of current flowing through the secondary coil.

With a design target of ± 2.5 V output for ± 100 A of primary current, the maximum voltage across the shunt (V_{shunt}) will be ± 2500 mV. The maximum value of R_{shunt} is calculated in Equation 5.

$$R_{shunt(max)} = \frac{V_{shunt(max)}}{I_{s(max)}} = \frac{2500mV}{50mA} = 50\Omega \quad (5)$$

3.4.1 Output Voltage

The output voltage from the sensor will be directly proportional to the primary current. With ± 100 A of primary current, the output voltage (V_{out}) can be calculated using Equation 6.

$$V_{out(max)} = \pm V_{shunt} = R_{SHUNT} \times I_s = 50\Omega \times \pm 50mA = \pm 2.5V \quad (6)$$

3.5 Compensation Coil Drive Stage

The compensation coil drive stage is comprised of an OPA237 and a pair of matched bi-polar junction transistors. The OPA237 was chosen for this application because of its low cost, wide supply range, and low output offset voltage specification of 250 μ V typical.

The OPA237 is configured as a difference amplifier with a gain of 4 V/V similar to the configuration of the difference amplifier found inside the DRV411.

4 Calculated Performance

The errors from the selected components are listed in Table 2.

Table 2. Typical Error Contributions

Specification	Typical
Input Offset DRV411 (μV)	± 30
Diff Amp Gain Error (%FS)	0.1
OPA237 Offset Error (μV)	250
Shunt Tolerance (%)	0.1

These errors can be referred to the output and compared to the full-scale output voltage to determine the total error as shown in Equations 7 – 10.

$$V_{\text{OS_DRV411}} (\text{V}) = \text{InputOffset} \times 4\text{V/V} = \pm 120\mu\text{V} \quad (7)$$

$$GE_{\text{DA}} (\text{V}) = \frac{\text{GainError}(\% \text{FS})}{100} \times 5\text{V} = \pm 5\text{mV} \quad (8)$$

$$V_{\text{OS_OPA237}} (\text{V}) = \text{Offset} \times 4\text{V/V} = \pm 1\text{mV} \quad (9)$$

$$R_{\text{SH_TOL}} (\text{V}) = \frac{\text{ShuntTolerance}(\%)}{100} \times 5\text{V} = \pm 5\text{mV} \quad (10)$$

Table 3 lists the errors referred to the full-scale output voltage.

Table 3. Typical Error Values

Specification	Typical (mV)
Offset DRV411	0.120
Diff Amp Gain Error	5
OPA237 Offset Error	1
Shunt Tolerance	5

Taking the root of the sum of the squared terms (RSS) provides a probable estimate for the total output error for the system as shown in Equation 11.

$$\text{Error(mV)} = \sqrt{0.12^2 + 5^2 + 1^2 + 5^2} = 7.14 \quad (11)$$

The full-scale error is then calculated using Equation 12.

$$\text{Error(\%FS)} = \frac{\text{Error(V)}}{\text{Vout(FS)}} \times 100 = \frac{0.00714}{5} \times 100 = 0.14\% \quad (12)$$

5 PCB Design

The PCB schematic and bill of materials can be found in the Appendix A.

5.1 PCB Layout

The two-layer printed circuit board (PCB) used in this design measures 1.875" x 0.725" as shown in Figures 3A and 3B. The DRV411 and supporting circuitry occupies the top-copper layer. The bottom-copper layer contains a solid-ground plane that provides a low-impedance path for return currents. U1 (the TPQL100A module) and J1 are mounted from the bottom side.

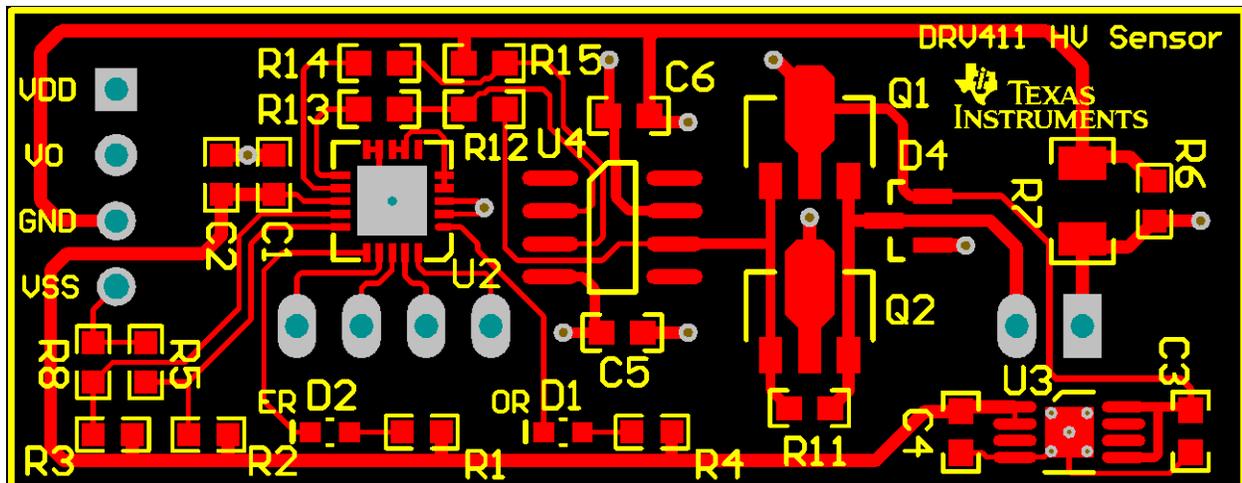


Figure 3A: PWB Top Layer

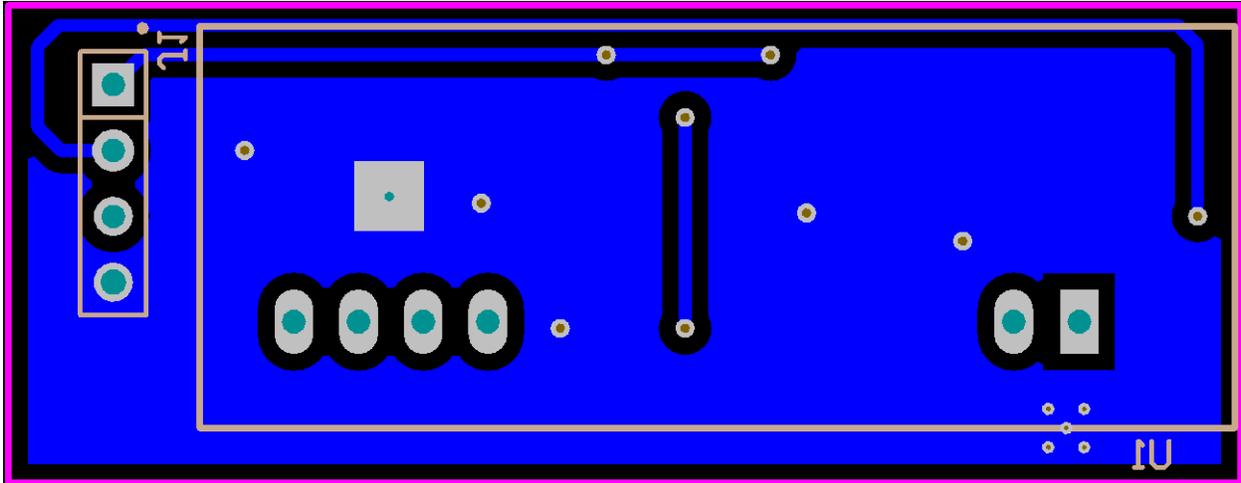


Figure 3B: PWB Bottom Layer

6 Verification & Measured Performance

A fixed ± 15 -V power supply provides power to the sensor circuit. Primary current is provided from a 250-A DC source across the entire input range of ± 100 A. Figure 4 shows the printed wiring board with the sensor and primary winding.

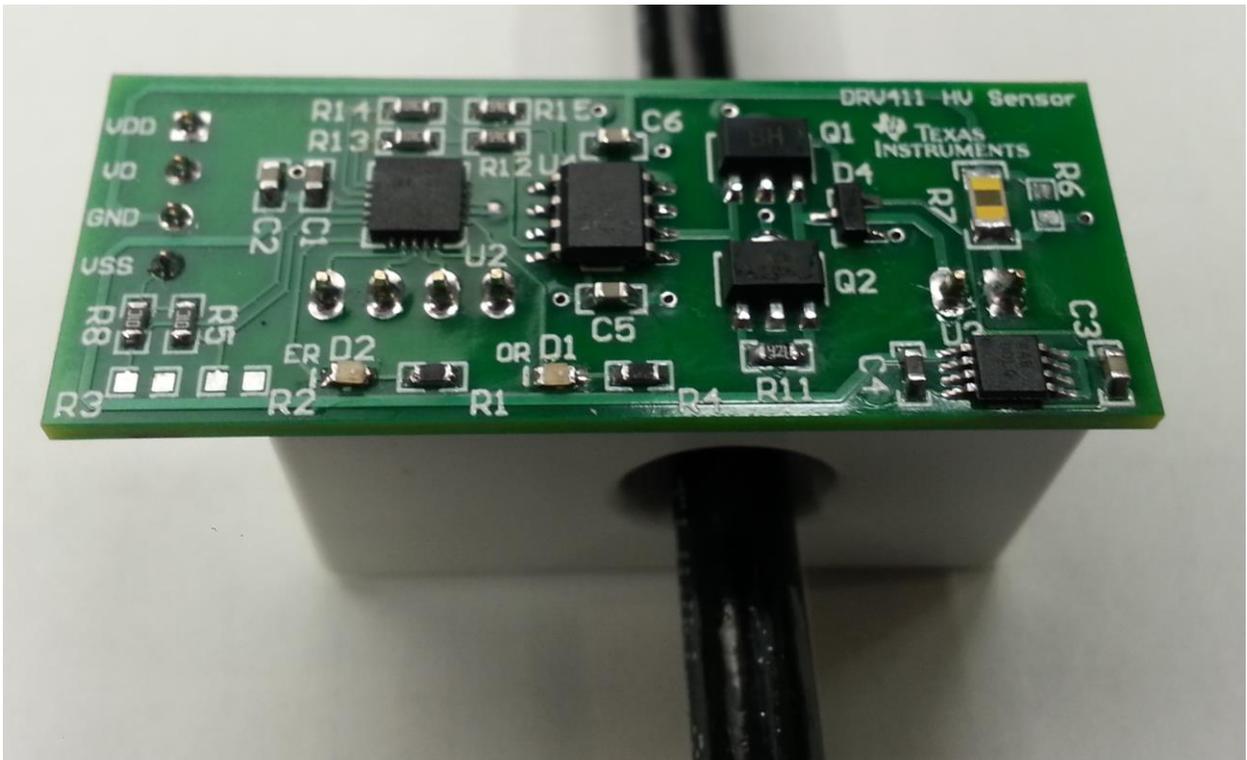


Figure 4: Primary Winding, Sensor and PWB

6.1 Transfer Function

The output voltage was measured at 5-A increments across the full range of current sensing (± 100 A). The measured DC transfer function is shown in Figure 5.

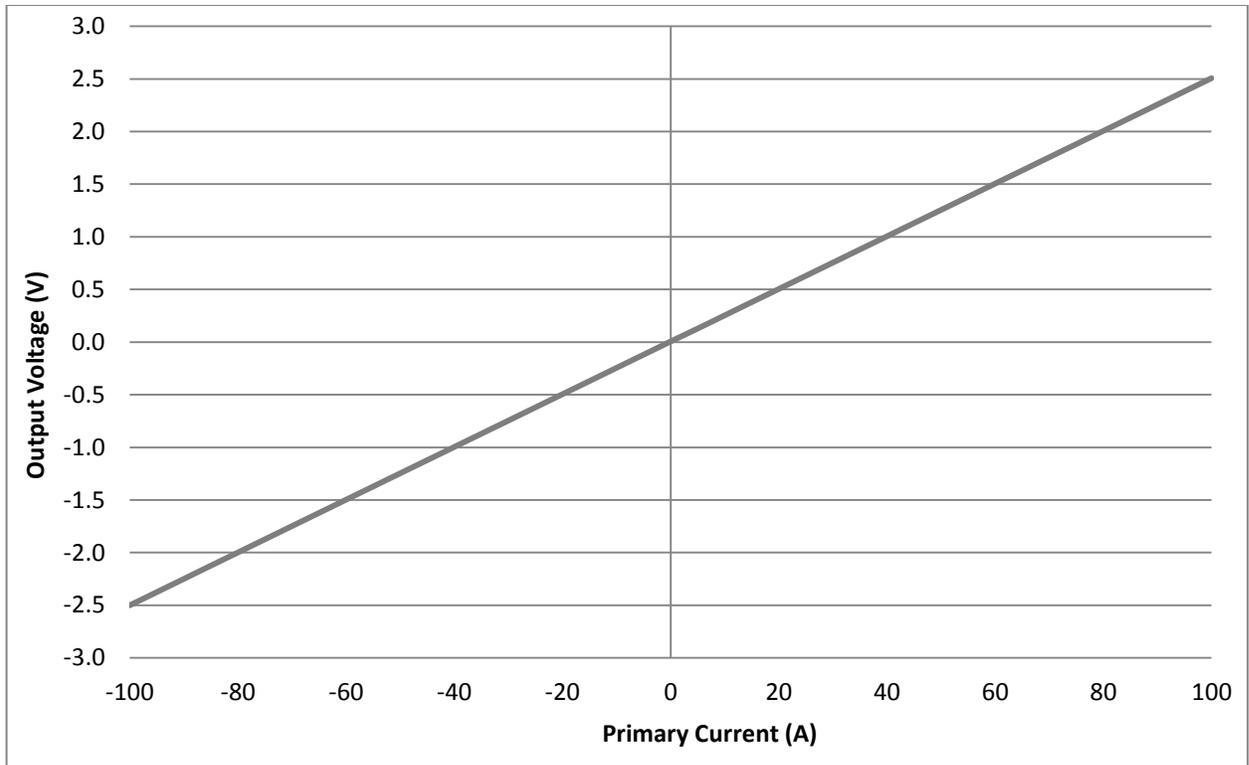


Figure 5: DC Transfer Function (V_{OUT} vs. I_p)

6.2 Full-Scale Error Analysis

The full-scale error (%FSR) of the output is calculated using Equation 13.

$$\text{Full-Scale Error (\%FSR)} = 100 \times \frac{V_{OUT_MEASURED} - V_{OUT_IDEAL}}{V_{OUT_IDEAL_MAX} - V_{OUT_IDEAL_MIN}} \quad (13)$$

The circuit's full-scale range error is plotted over the ± 100 -A input range in Figure 6.

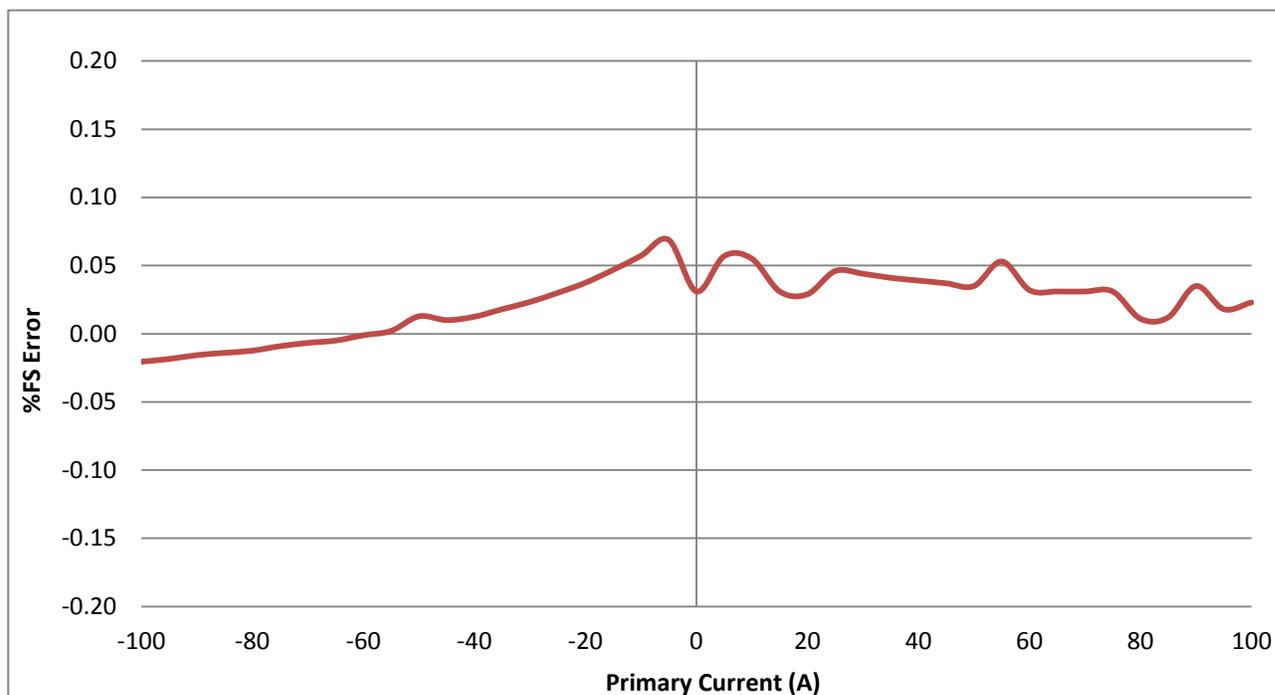


Figure 6: Full-Scale Error vs. Input Current

6.3 Table of Measured Results

The following table provides raw data for several measurement points.

Table 4. Measurement Data

I Primary (A)	V _{OUT} (V)	% FSR Error
100.03	2.502	0.023
75.05	1.878	0.031
50.05	1.253	0.035
25.02	0.628	0.046
0	0.002	0.031
-25.01	-0.624	0.030
-50.02	-1.249	0.013
-75.03	-1.876	-0.009
-100.03	-2.502	-0.020

7 Modifications

The reference board includes two LEDs as indicators for over range and error conditions. These components are not a critical need for the design and can be left off for cost savings. The printed wiring board could also be made in such a way that it is incorporated into the housing with the coil and Hall effect device.

The OPA237 package used in this design is the MSOP8. Single OPA237 devices are available in a SOT23-5 package, which would make the overall footprint of the sensor smaller. Tighter tolerance on the gain resistors around the OPA237 would also provide more accuracy to the sensor module.

The shunt resistor can be sized for the desired output voltage if the power limitations of the resistor are considered along with its thermal drift characteristics.

Appendix A.

A.1 Electrical Schematic

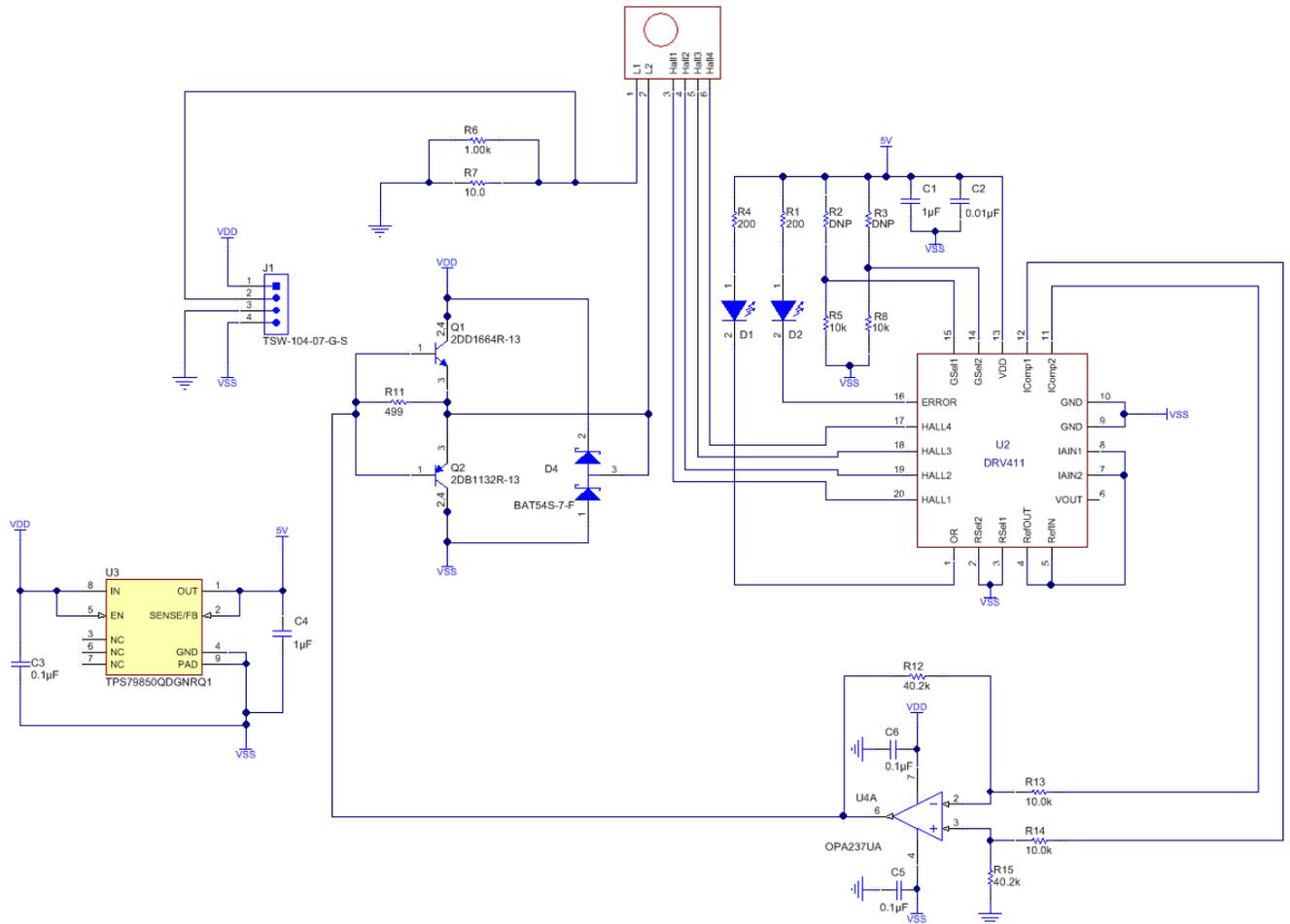


Figure A-1: Electrical Schematic

A.2 Bill of Materials

Item #	Qty	Designator	Description	Manufacturer	Part Number
1	1	N/A	Printed Wiring Board	Texas Instruments	N/A
2	2	C1 C4	1.0uF, 0603, Ceramic, X7R, 10V, 10%	Kemet	C0603C105K8RACTU
3	4	C2 C3 C5 C6	0.1uF, 0603, Ceramic, X7R, 50V, 10%	Kemet	C0603C104K5RACTU
4	2	D1 D2	Yellow LED	Lite-On	LTST-C191KSKT
5	1	D4	BAT54S, Dual Schottkey	Vishay	BAT54S-E3-08
6	1	Q1	NPN	Diodes Inc	BCX56TA
7	1	Q2	PNP	Diodes Inc	BCX53TA
8	2	R1 R4	200 ohm, 0603, 5%, .1W Resistor	Yageo America	RC0603JR-07200RL
9	2	R5 R8	10K ohm, 0603, 1%, .1W Resistor	Yageo America	RC0603FR-0710KL
10	2	R13 R14	10K ohm, 0603, 0.1%, .1W Resistor	Yageo America	RT0603BRD0710KL
11	1	R7	49.9 ohm, 1206, 0.1%, 1W Resistor	Vishay	PHP01206E49R9BST5
12	1	R11	470 ohm, 0603, 5%, .1W Resistor	Yageo America	RC0603JR-07470RL
13	2	R12 R15	40.2K ohm, 0603, 0.1%, .1W Resistor	Yageo America	RT0603BRD0740K2L
15	1	J1	4-pin, Single Row vertical mount male header	Samtec	TSW-104-14-T-S
16	1	U1	100 A Passive Current Sensor	Topstek	TQPL50A
17	1	U2	DRV411	Texas Instruments	DRV411AIRGPT
18	1	U3	TPS78950Q	Texas Instruments	TPS78950QDGNRQ1
19	1	U4	OPA237	Texas Instruments	OPA237UA

Figure A-2: Bill of Materials

IMPORTANT NOTICE FOR TI REFERENCE DESIGNS

Texas Instruments Incorporated ("TI") reference designs are solely intended to assist designers ("Buyers") who are developing systems that incorporate TI semiconductor products (also referred to herein as "components"). Buyer understands and agrees that Buyer remains responsible for using its independent analysis, evaluation and judgment in designing Buyer's systems and products.

TI reference designs have been created using standard laboratory conditions and engineering practices. **TI has not conducted any testing other than that specifically described in the published documentation for a particular reference design.** TI may make corrections, enhancements, improvements and other changes to its reference designs.

Buyers are authorized to use TI reference designs with the TI component(s) identified in each particular reference design and to modify the reference design in the development of their end products. HOWEVER, NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY THIRD PARTY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT, IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI REFERENCE DESIGNS ARE PROVIDED "AS IS". TI MAKES NO WARRANTIES OR REPRESENTATIONS WITH REGARD TO THE REFERENCE DESIGNS OR USE OF THE REFERENCE DESIGNS, EXPRESS, IMPLIED OR STATUTORY, INCLUDING ACCURACY OR COMPLETENESS. TI DISCLAIMS ANY WARRANTY OF TITLE AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, QUIET ENJOYMENT, QUIET POSSESSION, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS WITH REGARD TO TI REFERENCE DESIGNS OR USE THEREOF. TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY BUYERS AGAINST ANY THIRD PARTY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON A COMBINATION OF COMPONENTS PROVIDED IN A TI REFERENCE DESIGN. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, SPECIAL, INCIDENTAL, CONSEQUENTIAL OR INDIRECT DAMAGES, HOWEVER CAUSED, ON ANY THEORY OF LIABILITY AND WHETHER OR NOT TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES, ARISING IN ANY WAY OUT OF TI REFERENCE DESIGNS OR BUYER'S USE OF TI REFERENCE DESIGNS.

TI reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques for TI components are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

Reproduction of significant portions of TI information in TI data books, data sheets or reference designs is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards that anticipate dangerous failures, monitor failures and their consequences, lessen the likelihood of dangerous failures and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in Buyer's safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed an agreement specifically governing such use.

Only those TI components that TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components that have **not** been so designated is solely at Buyer's risk, and Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.