**TI Designs**

**Zero Standby Power NFC for Flow Meter Designs**

---

**Design Features**

- Provide NFC Function for Flow Meter Designs
- Zero Standby Power for NFC Function
- NFC Reader Provides Power to NFC Chip for Communication
- Minimize Impact on Power Consumption to Host

**Featured Applications**

- Gas Meter
- Heat Meter
- Flow Meter

---

**Design Resources**

- **TIDM-NFC-WATERMTR** Design Folder
- **MSP430FR6989** Product Folder
- **RF430CL330H** Product Folder
- **TPS782** Product Folder
- **EVM430-FR6989** Tools Folder
- **RF430CL330HTB** Tools Folder

---

**An IMPORTANT NOTICE** at the end of this TI reference design addresses authorized use, intellectual property matters and other important disclaimers and information.
1 System Description

Many smart flow meters are battery-powered. The amount of the energy is limited by the battery capacity. Adding extra functions to the system consumes more energy, which reduces the battery life. Therefore, minimizing the power consumption of the extra functions is one of the key factors to prolong the system lifetime.

This TI Design adds near field communications (NFC) capability to flow meters. The NFC Interface Transponder RF430CL330H provides data exchange capability between flow meters and NFC readers and is compliant to ISO14443B. The RF430CL330H transponder consumes zero power while waiting to be energized by the RF field generated by the NFC reader. In addition, the RF430CL330H transponder can be powered by harvesting the energy from the RF field generated by the NFC readers. These two approaches minimize the impact of power consumption to flow meters.

For testing, this TI Design uses an EVM430-FR6989 evaluation kit and an RF430CL330HTB target board. The EVM430-FR6989 evaluation kit simulates the flow meter operation using LC sensors for rotation detection. The RF430CL330HTB target board is plugged in to the EVM430-FR6989 main board to provide NFC function.

1.1 MSP430FR6989

The MSP430FR6989 is an MSP430™ ultra-low-power (ULP) FRAM platform that combines uniquely embedded FRAM and a holistic ULP system architecture, allowing innovators to increase performance at lowered energy budgets. FRAM technology combines the speed, flexibility, and endurance of SRAM with the stability and reliability of flash at much lower power.
1.2 **EVM430-FR6989**

The EVM430-FR6989 evaluation kit is used to get the test result for this TI Design. This water meter reference design kit is an easy-to-use evaluation module for the MSP430FR698x family of microcontrollers. The kit consists of three boards: the main board, the sensor board, and the motor board.

The main board of the EVM consists of the MSP430FR6989 with different user interfaces such as LCD, buttons, and LEDs. The built-in eZ-FET enables direct programming to the MCU without extra FET tools. The eZ-FET also supports EnergyTrace™ technology for monitoring power consumption of the system. The MSP430FR6989 also supports EnergyTrace++™ to monitor the usage of different modules inside the MCU.

Designed for flow meter applications, the sensor board is a daughter board consisting of two LC sensors. The sensors are connected to the ESI module of the MSP430FR6989. Users can design their own sensor boards for any specific applications that use ESI. In this TI Design, this LC board is not used and replaced by the TMR sensor board.

The motor board drives the rotor disc to simulate water or gas flow. The buttons control the rotating direction of the disc while the variable resistor controls the rotating speed. Visit [http://www.ti.com/tool/EVM430-FR6989](http://www.ti.com/tool/EVM430-FR6989) for a detailed description.
1.3 **RF430CL330H**

The Dynamic NFC Interface Transponder RF430CL330H is an NFC Tag Type 4 device that combines a wireless NFC interface and a wired SPI or I²C interface to connect the device to a host. The NDEF message in the SRAM can be written and read from the integrated SPI or I²C serial communication interface and can also be accessed and updated wirelessly through the integrated ISO14443B-compliant RF interface that supports up to 848 kbps. As a general NFC interface, the RF430CL330H enables end equipment to communicate with the fast-growing infrastructure of NFC-enabled smartphones, tablets, and notebooks.

1.4 **RF430CL330HTB**

The TI RF430CL330H target board demonstrates the capabilities of the RF430CL330H and help aid in the development process by providing a working hardware or firmware reference example for NFC operations. Visit [http://www.ti.com/tool/rf430cl330htb](http://www.ti.com/tool/rf430cl330htb) for a detailed description.
2 System Design Theory

This design uses two approaches to provide NFC function with minimal power consumption of the RF430CL330H that draw from the host, which is the MSP430FR6989 mixed signal microcontroller.

2.1 Power Off RF430CL330H While Waiting to be Energized by RF Field

This approach is based on the application note Using RF430CL330H VCORE Pin to Wake Up Host MCU via a Presented NFC/RFID High Frequency Magnetic Field (SLOA200).

When a 13.56-MHz field is applied, the RF430CL330H VCORE pin (pin 13 of the RF430CL330H) is raised to 1.5 V. A simple NPN transistor switch circuit is added. The VCORE drives the base of the transistor to provide an active low interrupt to the connected host indicating that a 13.56-MHz RF field was detected. The RF430CL330H should be powered and configured so it can respond to the commands.

When the RF field is not detected, the host switches off the RF430CL330H so that device consumes zero power.

2.2 Minimize Power Consumption When RF Field is Detected

When the RF430CL330H detects an RF field, the device triggers the host and the host powers up the RF430CL330H. Data exchange is performed by accessing the SRAM of the RF430CL330H. The RF430CL330H acts as an NFC Tag Type 4. The NFC reader access the SRAM by communicating with the RF430CL330H using ISO14443B-Compliant 13.56-MHz RF interface. The RF430CL330H acknowledge the read or write activities to the host by sending End of Read (EOR) or End of Write (EOW) interrupts. The host accesses the SRAM of the RF430CL330H through SPI or I2C interface. The data stored in the SRAM is in NDEF message structure. The host powers down the RF430CL330H after finishing the data exchange process.

The RF430CL330H consumes 40 µA from the host when the supply voltage is above 3 V. If the supply voltage is below 3 V, the RF430CL330H obtains power from the RF field and consumes 0 µA from the host.
3 Software Description

The rotation detection function of this software is based on the TI Design TIDM-2LC-WATERMTR. This code is simplified by keeping the most basic functions to operate the system.

When the system starts, the hardware modules, such as the clock system and the ESI, are initialized. After that, the system starts TSM calibration to find the optimum time delay for measuring the oscillation of the LC sensor. The LCD shows the number "8888" when the optimum delay is found. Then the calibration starts to find the suitable reference voltage for rotation detection. The calibration requires the wheel of the motor board to rotate to find the maximum and the minimum oscillating level of the LC sensors. After the calibration is completed, the interrupts are set up. The system goes to low power mode. The ESI interrupts are set to show the updated ESI counter value on the LCD. The navigation button toggles the LCD on or off regardless the direction of the button is pressed.

When an NFC reader such as an NFC-enabled smartphone is near the RF430CL330H target board, the target board triggers the EVM430-FR6989 main board. The main board powers up the target board and writes texts and the value of the ESICNT1 to the RF430CL330H SRAM. The NFC reader reads the data inside the SRAM. When the NFC reader moves away from the target board, the target board generates an EOR interrupt. The main board powers down the target board and returns to low power mode.

![Software Flow Diagram](image-url)
4 Test Setup

4.1 RF430CL330HTB Modification

The target board RF430CL330HTB is not fully pin-compatible with the EVM430-FR6989 main board. Modify the target board RF430CL330HTB as follows:

1. Remove R9 on the RF430CL330HTB to disconnect power being supplied to the RF430CL330H from the system power.
2. Remove R12 to prevent back powering.
3. Add Q1. This can be any general purpose NPN such as 2N2222 or 2N3904 in SOT-23 package.
4. Add R10 and R11 (1 MΩ, 0402 package).
5. Remove R8 (The test uses SPI instead of I²C, DNP for SPI).
6. Remove R1 and R2 (Remove the I²C pullup resistors to prevent leakage).
7. Modify the connection of the RF connectors of the RF430CL330HTB target board as shown in Table 1 and Figure 6 to make it compatible with the EVM430-FR6989 main board.

Table 1. Modifications to RF Connector of RF430CL330HTB Target Board

<table>
<thead>
<tr>
<th>RF430CL330HTB</th>
<th>EVM430-FR6989</th>
</tr>
</thead>
<tbody>
<tr>
<td>PIN</td>
<td>FUNCTION</td>
</tr>
<tr>
<td>RF1 (pin 6)</td>
<td>CS</td>
</tr>
<tr>
<td>RF1 (pin 16)</td>
<td>SCK</td>
</tr>
<tr>
<td>RF1 (pin 18)</td>
<td>MOSI</td>
</tr>
<tr>
<td>RF1 (pin 20)</td>
<td>MISO</td>
</tr>
<tr>
<td>RF2 (pin 13)</td>
<td>INT0</td>
</tr>
<tr>
<td>RF2 (pin 15)</td>
<td></td>
</tr>
<tr>
<td>RF2 (pin 19)</td>
<td>VIN_3.6V</td>
</tr>
<tr>
<td>RF2 (pin 20)</td>
<td>RF2_RST</td>
</tr>
</tbody>
</table>

Figure 5. RF430CL330HTB Bottom View

Figure 6. Wire Connection of RF430CL330HTB Target Board (Bottom View)
### 4.2 Test Procedure

Follow this testing procedure by using the EVM430-FR6989 and Code Composer Studio™ (CCS) version 6.1.

1. Prepare:
   
   (a) Modify the RF430CL330H target board as described in Section 4.1.
   
   (b) Attach the RF430CL330H target board to the RF connector of the EVM430-FR6989 main board as shown in Figure 7.
   
   (c) Attach the LC sensor board to the ESI port of the EVM430-FR6989 main board as shown in Figure 7.

![EVM430-FR6989 Main Board With RF430CL330HTB Target Board Attached](image)

**Figure 7. EVM430-FR6989 Main Board With RF430CL330HTB Target Board Attached**
(d) Connect the jumpers of the EVM430-FR6989 main board as shown in Figure 8.

(e) Connect the EVM430-FR6989 main board to the PC with an USB cable.
(f) Place the motor board to the location that the wheel of the motor board is about 5 mm away to the LC sensors as shown in Figure 1.
(g) Switch on the motor board.

2. Start the test:
(a) Launch CCS and load the test project.
(b) Start and run the debug session of the CCS.
(c) The main board shows "0" on the LCD. Make sure the wheel of the motor board is not rotating.
(d) While calibrating, "8888" is shown on the LCD of the main board.
(e) Start rotating the wheel by pressing the BUT1 or BUT2 of the motor board. Adjust the variable resistor to change the rotation speed.
(f) After the calibration is completed, the counter number of the ESI is shown on the LCD.
(g) Press the navigation button to toggle off the LCD.
3. During the test:
   (a) Pause the debugging session.
   (b) Set the energy trace measurement duration to 30 seconds.
   (c) Start free run mode (Menu → Run → Free run).
   (d) During the measurement period, place the NFC reader near the RF430CL330H target board.
   (e) The NFC reader shows the following text:
       "Device ID: 1234
       ESICNT1 = xxxxx"
       "xxxxx" is the value of the ESICNT1, which is the same value shows on the LCD.

   (f) Move the NFC reader away from the target board.
   (g) After the measurement period, the power consumption profile of that measurement period is generated on the "Power" tab by the EnergyTrace function.

Figure 9. Screenshot of NFC-Enabled Smartphone Reading Data of the RF430CL330HTB Target Board
Written by the EVM430FR6989
5 Test Data

The following figures show the power profile measured for 30 seconds after the ESI calibration is finished and the LCD is toggled off. This simulates the normal operation of the flow meters in detecting rotation of the propellers.

The flow of the power measurement is done as follows:
1. At T = 0 seconds: Start measuring.
2. At T = 10 seconds: Place the NFC reader.
3. At T = 20 seconds: Remove the NFC reader.
4. At T = 30 seconds: Finish measuring.

5.1 Test Result at VCC = 3.3 V

Figure 10 shows the power profile measured for 30 seconds when the system is powered at 3.3 V. When the RF field is not present, the system base current is measured at 8.18 µA (0.027 mW / 3.3 V). When the RF is detected, a burst is observed. The MSP430FR6989 writes text data to the RF430CL330H during that period. After that, the power became steady at about 204 mW, which translates to 61.8 µA.

![Figure 10. Power Profile of 30-Second Measurement at 3.3-V Power Supply](image)
5.2 Test Result at VCC = 2.5 V

The same test is also conducted with the power supply voltage reduced to 2.5 V. Voltage regulator TPS78225 is added to the EVM430-FR6989 main board to lower the supply voltage to 2.5 V. The connection is shown in Figure 11. Note that the jumpers on the J401 should be removed.

Figure 11. Voltage Regulator Connection on the EVM430-FR6989 Main Board
The test result shown in Figure 12 indicates that at a 2.5-V power supply, when the RF is present, the power consumption is reduced compared with a 3.3-V power supply. The current is 10.6 uA (0.035 mW / 3.3 V). The system base current is still 8.18 uA (0.02 mW/3.3 V). Note that the power is measured before the voltage regulator. The voltage used in the calculation is still 3.3 V.

As mentioned in Section 2.2, the RF430CL330H obtains power from the RF field and consumes 0 µA from the host. The additional 2.42 µA is contributed by the wake circuit \((2.5 \text{ V} - \text{VCE}) / 1 \text{ MΩ}\), where VCE is the voltage between the collector and the emitter of the transistor.)
6 Design Files

6.1 Schematics
To download the schematics, see the design files for the following:

- EVM430-FR6989
- TIDM-LC-WATERMTR
- RF430CL330HTB

6.2 Bill of Materials
To download the bill of materials (BOM), see the design files for the following:

- EVM430-FR6989
- TIDM-LC-WATERMTR
- RF430CL330HTB

6.3 Layer Plots
To download the layer plots, see the design files for the following:

- EVM430-FR6989
- TIDM-LC-WATERMTR
- RF430CL330HTB

6.4 Gerber Files
To download the Gerber files, see the design files for the following:

- EVM430-FR6989
- TIDM-LC-WATERMTR
- RF430CL330HTB

7 Software Files
To download the software files, see the design files at TIDM-NFC-WATERMTR.

8 About the Author
ZACK MAK is a system application engineer at Texas Instruments where he is responsible for developing reference design solutions for the industrial segment. Zack earned his bachelor of electronic and communication engineering from City University of Hong Kong.
IMPORTANT NOTICE FOR TI REFERENCE DESIGNS

Texas Instruments Incorporated ("TI") reference designs are solely intended to assist designers ("Buyers") who are developing systems that incorporate TI semiconductor products (also referred to herein as "components"). Buyer understands and agrees that Buyer remains responsible for using its independent analysis, evaluation and judgment in designing Buyer’s systems and products.

TI reference designs have been created using standard laboratory conditions and engineering practices. TI has not conducted any testing other than that specifically described in the published documentation for a particular reference design. TI may make corrections, enhancements, improvements and other changes to its reference designs.

Buyers are authorized to use TI reference designs with the TI component(s) identified in each particular reference design and to modify the reference design in the development of their end products. HOWEVER, NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY THIRD PARTY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT, IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI REFERENCE DESIGNS ARE PROVIDED "AS IS". TI MAKES NO WARRANTIES OR REPRESENTATIONS WITH REGARD TO THE REFERENCE DESIGNS OR USE OF THE REFERENCE DESIGNS, EXPRESS, IMPLIED OR STATUTORY, INCLUDING ACCURACY OR COMPLETENESS. TI DISCLAIMS ANY WARRANTY OF TITLE AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, QUIET ENJOYMENT, QUIET POSSESSION, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS WITH REGARD TO TI REFERENCE DESIGNS OR USE THEREOF. TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY BUYERS AGAINST ANY THIRD PARTY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON A COMBINATION OF COMPONENTS PROVIDED IN A TI REFERENCE DESIGN. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, SPECIAL, INCIDENTAL, CONSEQUENTIAL OR INDIRECT DAMAGES, HOWEVER CAUSED, ON ANY THEORY OF LIABILITY AND WHETHER OR NOT TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES, ARISING IN ANY WAY OUT OF TI REFERENCE DESIGNS OR BUYER’S USE OF TI REFERENCE DESIGNS.

TI reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products are sold subject to TI’s terms and conditions of sale supplied at the time of order acknowledgment. TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI’s terms and conditions of sale of semiconductor products. Testing and other quality control techniques for TI components are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers’ products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers’ products and applications, Buyers should provide adequate design and operating safeguards. Reproduction of significant portions of TI information in TI data books, data sheets or reference designs is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards that anticipate dangerous failures, monitor failures and their consequences, lessen the likelihood of dangerous failures and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in Buyer’s safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI’s goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed an agreement specifically governing such use. Only those TI components that TI has specifically designated as military grade or “enhanced plastic” are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components that have not been so designated is solely at Buyer’s risk, and Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2015, Texas Instruments Incorporated