TI Designs

85Vac – 440Vac input, UCC28722 based, 15V, 6.5W Snubber-less Flyback Converter using BJT primary switch

Design Resources
TIDA-00628
UCC28722

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Block Diagram

Design Features
15V, 6.5W non-isolated power supply design which can tolerate input voltage range from low line (85Vac) to peak of 3 phase line-line (440Vac) with minimum component count and system cost

- Flyback for low power applications
- Output Voltage : 15V-dc
- Output Current : 0.433A
- Constant Output current Limit : 0.5A
- Output Over Voltage protection
- Input voltage up to 440Vrms
- No snubber - reduces system cost and PCB area
- BJT switch to reduce system cost
- Non Isolated output reduces transformer complexity
- Conducted Emissions: CISPR 22, Class B
- Operating temperature : -40°C to 85°C

Featured Applications
- Industrial power supplies
- Bias supplies in 3 phase systems

Board Image
1 Key System Specifications

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input voltage</td>
<td>85–220–440 Vrms</td>
</tr>
<tr>
<td>Input Line Frequency</td>
<td>47–50–63 Hz</td>
</tr>
<tr>
<td>Output Voltage</td>
<td>14.5–15–15.5 V</td>
</tr>
<tr>
<td>Output Over Voltage</td>
<td>17 V</td>
</tr>
<tr>
<td>Output Under Voltage</td>
<td>12 V</td>
</tr>
<tr>
<td>Output Current</td>
<td>0.043–0.433 A</td>
</tr>
<tr>
<td>Output ripple &amp; noise</td>
<td>150 mVpp</td>
</tr>
<tr>
<td>Startup Time</td>
<td>2 s</td>
</tr>
<tr>
<td>Load transient</td>
<td></td>
</tr>
<tr>
<td>Output Voltage deviation (Min - Max % load step)</td>
<td>0.5 V</td>
</tr>
<tr>
<td>EMC Compliance</td>
<td>CISPR22 Class B</td>
</tr>
</tbody>
</table>

Table 1

2 System Description

In 3 phase systems where there is the potential for the netural connection to drift due to unbalanced loading on the phases, or for equipment to be wrongly connected from phase to phase rather than phase to neutral, voltages of circa 440–500Vac can be seen at the input to the unit.

This input voltage range significantly exceeds the 264Vrms maximum voltage specification of most universal input and hence requires changes to the input stage and possibly the primary switch.

In this design a TVS and High voltage MosFET are used at the input to clamp the bulk capacitor voltage below 450V, which allows only one bulk capacitor to be used, even in the presence of input voltages higher than the normal universal input voltage range.

A high voltage BJT is used as the main primary switch which reduces the system cost as HV BJTs are cheaper than high voltage MosFETs and also the higher voltage rating of the BJT means that a snubber is not required, further reducing the system cost.

This BJT is driven by the UCC28722 controller. The UCC28722 is a primary side regulated flyback controller with a current source output designed for driving BJT’s as the flyback main switch. This design is a single output flyback with a nominal output voltage of 15V.

Because the output is not isolated from the input, the output winding can be used to power the IC and also used as the sense winding for output voltage control. Thus no bias winding is required and the transformer complexity is reduced.
2.1 **UCC28722**

The UCC28722 is a Primary side regulated flyback controller, but as mentioned above, because this is a non-isolated application the feedback can be fed back directly from the output, also increasing the regulation accuracy and reducing the complexity of the system.

The controller provides constant current regulation and overvoltage protection as well as output voltage regulation.

The controller operates in discontinuous conduction mode with valley-switching to minimize switching losses. The modulation scheme is a combination of frequency and primary peak current modulation to provide high conversion efficiency across the load range.

The controller also offers output over-voltage protection and input brown out protection features.

2.1.1 **UCC28722 Features**

- Dynamic BJT Drive information from the primary power switch and flyback winding for precise control of the output
- 80-kHz Maximum Switching support high efficiency operation at all load levels without sacrificing output transient
- Quasi-Resonant Valley-Switching Operation for Highest Overall Efficiency
- Wide VDD Range Allows Small Bias Capacitor
- Output Overvoltage, Low-Line, and Overcurrent Protection Functions
- SOT23-6 Package

![UCC28722 Functional Block Diagram](image)

Figure 1 : UCC28722 Functional Block Diagram
3 System Design

3.1 Input stage

Resistor R1 is a fusible resistor. It limits the inrush current and provides protection in case of component failure causing a short circuit downstream of the fuse. Excessive current causes the fusible resistor to fail open circuit. It also provides some attenuation during surge voltage events at the input to the converter.

A position is also provided for a MOV to be fitted for further line voltage surge attenuation.

D1 provides half wave rectification of the input sine wave. D7 is added to improve the conducted emissions from the unit. If EMI filtering is not required D7 can be removed.

An input voltage range of 440Vrms (~622Vdc) is specified which means that the standard approach of using a 400Vdc rated bulk cap to support the output during ac peaks and during the holdup period is not possible.

One possible approach is to use stack two 400V bulk caps in series to double the voltage rating. However since putting capacitors in series reduces their effective capacitance, both capacitors must have twice the required capacitance, so four times the required bulk capacitance are needed to increase the voltage rating in this way.

Another approach is to use a TVS or some other voltage clamp device to clamp the voltage before rectifying it onto the bulk capacitor as shown below. This circuit requires a high voltage MosFET and TVS or MOV but removes the need for two bulk caps in series. One 450Vdc bulk cap can now be used.

A differential mode filter is inserted between the clamp and the bulk capacitor to filter the voltage developed across the bulk capacitor ESR by the switch current and the voltage swing across the transformer interwinding capacitance. Values are based on testing for particular transformer construction, PCB layout etc.

For a half wave rectified input the required bulk capacitance can be estimated from the formula.
Where

$\text{RCT} = 1$ for a half wave rectified input (shown as a solid grey line in the figure below), and

$\text{RCT} = 2$ for a full wave rectified input (shown as a dashed grey line in the figure below)

The bulk cap voltage is shown in black

![Graph showing input voltage and bulk cap voltage](image)

**Figure 3 : Input Voltage and Bulk Cap Voltage**

And

- $P_{\text{in}} = \frac{V_{\text{out}} \cdot I_{\text{out}}}{\eta}$
- $V_{\text{bulk(min)}} = 75V$
- $V_{\text{in(min)}} = 85V$

A bulk capacitor of 33uF was used in this design.
### 3.2 Transformer Turns Ratio

The first consideration for the transformer is what reflected voltage to chose. The reflected voltage and max on and off duty cycles will dictate the minimum bulk voltage at which the load can be supplied.

The maximum secondary diode conduction duty cycle is 42.5%. From equation 14 of the datasheet

\[
D_{\text{max}} = 1 - \left( \frac{t_R}{2f_{\text{max}}} \right) = D_{\text{MAGCC}}
\]

(and targeting a maximum switching frequency of 65kHz), this gives a maximum duty cycle of ~50%.

Therefore the minimum bulk voltage which can support the load is

\[
V_{\text{bulk, min}} = \frac{V_{\text{reflected}} \cdot D_{\text{MAGCC}}}{D_{\text{max}}}
\]

A reflected voltage of ~70-71V was chosen as it is close to the minimum bulk voltage at low line, giving a minimum bulk voltage to maintain regulation of ~ 60V.

This results in a turns ratio of 4.54 to the 15V winding, assuming a schottky diode drop of 0.5V on the output.

\[
N = \frac{V_{\text{reflected}}}{V_{\text{out, 15}} + V_{\text{rect}}} = 4.5
\]

As will be seen later, the eventual turns ratio is 4.47

### 3.3 Current Sense Resistance

A current limit of 120% of the maximum load current is chosen to allow some margin before entering constant current limit operation. Above this level the output voltage will decrease with increasing load.

\[
P_{\text{max}} = 6.5W, \text{ therefore } P_{\text{limit}} = 7.8W. \text{ This is equivalent to a current of 0.506A on the 15V output.}
\]

From equation 16 in the datasheet the current sense resistor is therefore

\[
R_{\text{cs}} = \frac{V_{\text{crr}} \cdot N \cdot \sqrt{\eta_{\text{trafo}}}}{2 \cdot I_{\text{limit}}} = 1.351\Omega
\]

Choosing an Rs of 1.35Ω gives a peak primary current of

\[
I_{\text{pk, pri}} = \frac{V_{\text{cs, max}}}{R_{\text{cs}}} = 0.577A
\]

And an inductance of

\[
L = 2 \cdot \frac{P_{\text{limit}}}{\eta_{\text{trafo}} \cdot \left( \frac{R_{\text{cs}}}{V_{\text{cs, max}}} \right)^2} \cdot \frac{1}{I_{\text{sw}}} = 881 \mu H
\]
3.4 Transformer Design

An E16/8/5 is chosen as the transformer core size. A small transformer may be sufficient to transfer the power but the higher number of turns and the narrower winding window would result in a higher layer count, which would reduce the efficiency significantly.

Copper losses are reduced by
1) The reduced length of copper in the winding
2) The increased strand diameter allowed by the larger winding window
3) The reduction in AC resistance with the reduced number of layers.

Reducing the number of layers also has the significant effect of reducing the leakage inductance, which
1) Increasing the efficiency
2) Reducing the magnitude of the leakage inductance ring, which reduces the electromagnetic emissions from the unit.
3) Providing more margin to the maximum switch rated voltage.

With this core size and targeting a peak flux density of $\sim 333$ mT, the following numbers of turns were chosen,

$$N_{\text{pri}} = 76$$
$$N_{\text{sec}} = 17$$

3.5 Output capacitor

The output capacitor must be sized to handle the output rms ripple current. The rms ripple current can be calculated as

$$I_{\text{out,ripple}} = \sqrt{I_{\text{sec, rms}}^2 - I_{\text{out, max}}^2}$$

$$I_{\text{sec, rms}} = I_{pk, \text{pri}} \cdot N \cdot \frac{D_{\text{MAGCC}}}{3} = 0.931 \text{A}$$

The ripple current rating of the capacitor is given in the datasheet.

For stability, it is also recommended that the output capacitance is greater than

$$C_{\text{out}} > \frac{400 \cdot I_{\text{limit}}}{V_{\text{out}} \cdot f_{\text{SW, max}}} \approx 270 \mu\text{F}$$
3.6 Output Voltage Ripple

The output voltage ripple is equal to

\[ V_{\text{out,ripple}} = I_{\text{pk, pri}} \cdot N \cdot Z_{\text{cout}} \]

Where \( Z_{\text{cout}} \) is the complex impedance of the output capacitance. \( Z_{\text{cout}} \) is approximately equal to

\[ Z_{\text{cout}} = \sqrt{\frac{D_{\text{MAGCC}}}{2 \cdot \pi \cdot f_{\text{sw}} \cdot C_{\text{out}}} + \text{ESR}_{\text{cout}}} \]

This equation assumes that the ESL of the capacitor is negligible at the switching frequency.

It may be necessary to place some ceramic capacitance across the output to filter out higher order harmonics of the switching frequency, where the ESL of the electrolytic is not necessarily negligible.

This ceramic capacitance also affects the complex impedance of the output capacitance network. This effect can be accounted for in the calculation but the equation becomes significantly more complicated. The impact can be neglected to a first order.

If the output ripple is greater than desired, extra output capacitance may be required or in some cases an output filter.

3.7 Transient Response

The transient response of the unit is given by

\[ \Delta V_{\text{out}} = \frac{I_{\text{tran}}}{C_{\text{out}}} \left( \frac{1}{f_{\text{SW,pre_trans}}} + 150 \mu s \right) \]

Where

\[ I_{\text{tran}} = \] the maximum transient load step
\[ f_{\text{SW,pre_trans}} = \] the minimum switching frequency before the load step occurs. This will be set by the minimum load present before the load step and has a big impact on the transient performance and required output capacitance.

If the output transient deviation is too large, more output capacitance may be required.

Two 150uF capacitors were used in this design.
3.8 **Vdd Capacitance**

The Vdd capacitance is sized, as described in the datasheet, according to the formula.

\[
C_{DD} = \frac{[I_{run} + I_{DRS(max)} \cdot D_{Max}]}{V_{dd_on} - V_{dd_off}} \left( \frac{C_{out} \cdot V_{out_{UV}}}{I_{limit} - I_{load}} \right)
\]

Where

- \(I_{run}\) = the current consumption of the IC
- \(I_{DRS(max)}\) = the maximum switch drive current. Using this figure gives some margin on the choice of Vdd capacitance and the current drawn will actually equal the average drive current, not the maximum drive current.

\[
C_{DD} = \frac{[3mA + 42mA \cdot 0.575] \cdot (300\mu F \cdot 10V)}{19 - 8.3} = 12.8\mu F
\]

A 22\(\mu\)F aluminum electrolytic capacitor was used in this design. This will allow the unit to start into ~50% load.

Note; if using ceramic capacitors as the bias capacitors that the capacitance of ceramic caps decreases with bias voltage. The decrease varies from part to part but for some parts the capacitance can be below 50% of it’s nominal value when it has half it’s rated voltage across it.

3.9 **Startup resistor**

The startup resistor is chosen based on the required startup time according to the equation;

\[
R_{\text{startup}} = \frac{V_{accp}}{I_{dd_{\text{start}}} + \frac{V_{dd_{on}} \cdot C_{Vdd}}{T_{\text{start}}}}
\]

For a 2s startup time at Vacpk_min this gives

\[
R_{\text{startup}} = \frac{127V}{1mA + \frac{19V \cdot 22\mu F}{2s}} = 572k\Omega
\]

Two 270k\(\Omega\) resistors in series were used to maintain high voltage spacing from the bulk cap. This gives a startup time of 1s at 220Vac.
3.10 Output voltage Sense Resistors

The output voltage sense resistors are also chosen per the datasheet equations.

\[
R_{S1} = \frac{\sqrt{2} \cdot V_{\text{In min}}}{N_{PA} \cdot I_{VSL\_run}}
\]

\[
R_{S2} = \frac{R_{S1} \cdot V_{\text{SR}}}{N_{AS} \cdot (V_{\text{out}} + V_F) - V_{\text{SR}}}
\]

Where

- \(I_{VSL\_run}\) = The Vs line-sense run current
- \(V_{\text{SR}}\) = The regulation voltage at the Vs pin
- \(V_F\) = The forward drop of the output regulator
- \(N_{PA}\) = The primary to auxiliary turns ratio. If the secondary and auxiliary have the same number of turns, as in this design, this is equal to \(N\).
- \(N_{AS}\) = The secondary to auxiliary turns ratio. If the secondary and auxiliary have the same number of turns, as in this design, this is 1.

\[
R_{S1} = \frac{\sqrt{2} \cdot 85}{4.47 \cdot 225\text{mA}} = 119.5\text{k}\Omega
\]

A 120kΩ resistor was used.

\[
R_{S2} = \frac{120\text{k}\Omega \cdot 4.05V}{(15V + 0.5V) - 4.05V} = 42.27\text{k}\Omega
\]

A 42.2kΩ resistor was used.

3.11 Line Feed-forward Resistor

\[
R_{LC} = \frac{K_{LC} \cdot R_{S1} \cdot R_{CS} \cdot t_D \cdot N_{PA}}{L_P}
\]

\(t_D\) = current-sense delay including NPN transistor turn-off delay, add ~50 ns to transistor delay

\(K_{LC}\) = a current-scaling constant given in the electrical characteristics section of the datasheet.

\[
R_{LC} = \frac{25 \cdot 120\text{k}\Omega \cdot 1.35\Omega \cdot 150\text{ns} \cdot 4.47}{881\text{mH}} = 3.07\text{k}\Omega
\]

A 3.09kΩ resistor was used.
4 Getting Started Hardware

4.1 Hardware Overview.

The design hardware is shown in Figure 4. A standard figure-of-8 AC receptacle is used on the input. The load is connected to terminal block J2. J3 on the BoM is the mating part into which the load terminal wires may be screwed. The board should always be operated within the conditions specified in Table 1 above.
5 Test Data

The results shown below are taken at 220Vac input except where specified otherwise.

5.1 Efficiency

<table>
<thead>
<tr>
<th>(V)</th>
<th>(Hz)</th>
<th>Pin (W)</th>
<th>(V)</th>
<th>(A)</th>
<th>Pout (W)</th>
<th>Eff (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>230</td>
<td>50</td>
<td>1.043</td>
<td>15.085</td>
<td>0.043</td>
<td>0.648</td>
<td>62.07</td>
</tr>
<tr>
<td></td>
<td></td>
<td>1.898</td>
<td>15.073</td>
<td>0.092</td>
<td>1.384</td>
<td>72.92</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2.768</td>
<td>15.074</td>
<td>0.141</td>
<td>2.121</td>
<td>76.61</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.653</td>
<td>15.085</td>
<td>0.190</td>
<td>2.860</td>
<td>78.28</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.547</td>
<td>15.095</td>
<td>0.238</td>
<td>3.600</td>
<td>79.16</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.447</td>
<td>15.102</td>
<td>0.287</td>
<td>4.340</td>
<td>79.67</td>
</tr>
<tr>
<td></td>
<td></td>
<td>6.342</td>
<td>15.107</td>
<td>0.336</td>
<td>5.079</td>
<td>80.09</td>
</tr>
<tr>
<td></td>
<td></td>
<td>7.243</td>
<td>15.116</td>
<td>0.385</td>
<td>5.822</td>
<td>80.38</td>
</tr>
<tr>
<td></td>
<td></td>
<td>8.148</td>
<td>15.133</td>
<td>0.434</td>
<td>6.568</td>
<td>80.61</td>
</tr>
<tr>
<td></td>
<td></td>
<td>9.055</td>
<td>15.153</td>
<td>0.483</td>
<td>7.317</td>
<td>80.81</td>
</tr>
</tbody>
</table>
5.2 **Startup and Risetime**

The plots below show Vbias in Pin and Vout in blue.

* Risetime is independent of input voltage.
5.3 Output Ripple and Noise

220Vac : Vout ripple @ Minimum Load

220Vac : Vout ripple @ Full Load

5.4 Transient Response

220Vac : Full load to Minimum Load step

220Vac : Minimum load to Full Load step
5.5 Output current and voltage characteristic.

220Vac : Output voltage v Load current
5.6 **Bulk voltage and Switch node voltage at 630Vdc in, full load**

- **C1**: Vdc input: 630V
- **C2**: Bulk voltage after input voltage clamp: 429.5V
- **C3**: Vout
- **C4**: Switch node voltage, peak voltage = 640V, BJT switch rating = 1050V.
5.7 EMI Measurement

Conducted Emissions, 220Vac in, 6.5W Load, L1 (Live)
Conducted Emissions, 220Vac in, 6.5W Load, L2 (Neutral)
6  Design Files

6.1  Schematic

To download the schematic for the board, see the design files at [http://www.ti.com/tool/TIDA_00628](http://www.ti.com/tool/TIDA_00628)
6.2 **Bill of Materials**

To download the Bill of Materials for each board, see the design files at [http://www.ti.com/tool/TIDA_00628](http://www.ti.com/tool/TIDA_00628)

### TIDA_00628
REV E1 Bill of Materials

<table>
<thead>
<tr>
<th>Designator</th>
<th>Description</th>
<th>Manufacturer</th>
<th>PartNumber</th>
<th>Quantit y</th>
</tr>
</thead>
<tbody>
<tr>
<td>IPCB</td>
<td>Printed Circuit Board</td>
<td>Any</td>
<td></td>
<td>1</td>
</tr>
<tr>
<td>15.5V, Line</td>
<td>Test Point, TH, Miniature, Red</td>
<td>Keystone</td>
<td>5000</td>
<td>2</td>
</tr>
<tr>
<td>C3</td>
<td>CAP, AL, 33 µF, 400 V, +/- 20%, TH</td>
<td>Rubycon</td>
<td>450BXW33MEFC12.5X25</td>
<td>1</td>
</tr>
<tr>
<td>C4, C5</td>
<td>CAP ALUM 150UF 20% 25V RADIAL</td>
<td>United Chemi-Con</td>
<td>EKYB250ELL151MF11D</td>
<td>2</td>
</tr>
<tr>
<td>C6</td>
<td>CAP, CERM, 10uF, 50V, +/-10%, X7R, 1210</td>
<td>MuRata</td>
<td>GRM32ER71H106KA12L</td>
<td>1</td>
</tr>
<tr>
<td>C10</td>
<td>CAP, CERM, 0.1µF, 50V, +/-10%, X7R, 0603</td>
<td>AVX</td>
<td>06035C104KAT2A</td>
<td>1</td>
</tr>
<tr>
<td>C11</td>
<td>CAP, AL, 22 µF, 35 V, +/- 20%, 0.76 ohm, SMD</td>
<td>Nichicon</td>
<td>UUD1V220MCL1GS</td>
<td>1</td>
</tr>
<tr>
<td>C17</td>
<td>CAP, CERM, 100 pF, 25 V, +/- 10%, X7R, 0603</td>
<td>AVX</td>
<td>06033C101KAT2A</td>
<td>1</td>
</tr>
<tr>
<td>D1, D2</td>
<td>Diode, P-N, 1000V, 1A, TH</td>
<td>Fairchild Semiconductor</td>
<td>1N4007</td>
<td>2</td>
</tr>
<tr>
<td>D3</td>
<td>Diode, Schottky, 150V, 1A, SMA</td>
<td>ST Microelectronics</td>
<td>STPS150A</td>
<td>1</td>
</tr>
<tr>
<td>D5</td>
<td>DIODE SWITCH 200V 200MA SOD323</td>
<td>Vishay</td>
<td>BAS20-V</td>
<td>1</td>
</tr>
<tr>
<td>GND, Neutral</td>
<td>Test Point, TH, Miniature, Black</td>
<td>Keystone</td>
<td>5001</td>
<td>2</td>
</tr>
<tr>
<td>H1, H2, H3, H4</td>
<td>Standoff, Hex, 1&quot;L #6-32 Nylon, M-F</td>
<td>Keystone</td>
<td>4820</td>
<td>4</td>
</tr>
<tr>
<td>H5, H6, H7, H8</td>
<td>Standoff, Hex, 1&quot;L #6-32 Nylon</td>
<td>Keystone</td>
<td>1903E</td>
<td>4</td>
</tr>
<tr>
<td>J1</td>
<td>AC Receptacle, 2.5A, R/A, TH</td>
<td>Qualtek Electronics Corporation</td>
<td>770W-X2/10</td>
<td>1</td>
</tr>
<tr>
<td>J2</td>
<td>Terminal Block, 2x1, 5.08mm, TH</td>
<td>FCI</td>
<td>20020110-H021A01LF</td>
<td>1</td>
</tr>
<tr>
<td>J3</td>
<td>Term Block Plug 2 Pos 5.08MM</td>
<td>FCI</td>
<td>20020006-H021B01LF</td>
<td>1</td>
</tr>
<tr>
<td>J4</td>
<td>Jumper Wire, 500mil spacing, Green, pkg of 200</td>
<td>3M</td>
<td>923345-05-C</td>
<td>1</td>
</tr>
<tr>
<td>L2</td>
<td>Inductor, Wirewound, Ferrite, 1000uH, 0.42A, 2.5 ohm, TH</td>
<td>Wurth Elektron eiSos</td>
<td>744743102</td>
<td>1</td>
</tr>
<tr>
<td>Q1</td>
<td>MOSFET N-CH 800V 1.5A TO-220F</td>
<td>Fairchild Semiconductor</td>
<td>FQP2N80</td>
<td>1</td>
</tr>
<tr>
<td>Q2</td>
<td>Transistor, NPN, xxV, 4A, TO-220F</td>
<td>NXP Semiconductors</td>
<td>BUJ302AX</td>
<td>1</td>
</tr>
<tr>
<td>Component</td>
<td>Description</td>
<td>Manufacturer</td>
<td>Part Number</td>
<td>Quantity</td>
</tr>
<tr>
<td>-----------</td>
<td>-------------</td>
<td>--------------</td>
<td>-------------</td>
<td>----------</td>
</tr>
<tr>
<td>R1</td>
<td>Thermistor NTC, 10 ohm, 20%, Disc_11.5mmx6mm</td>
<td>EPCOS Inc</td>
<td>B57236S0100M000</td>
<td>1</td>
</tr>
<tr>
<td>R2</td>
<td>RES, 100 k, 5%, 0.75 W, 2010</td>
<td>Vishay-Dale</td>
<td>CRCW2010100KJNEF</td>
<td>1</td>
</tr>
<tr>
<td>R3</td>
<td>RES, 6.8 k, 5%, 0.75 W, 2010</td>
<td>Vishay-Dale</td>
<td>CRCW20106K80JNEF</td>
<td>1</td>
</tr>
<tr>
<td>R4, R5</td>
<td>RES, 270 k, 5%, 0.25 W, 1206</td>
<td>Vishay-Dale</td>
<td>CRCW1206270KJNEA</td>
<td>2</td>
</tr>
<tr>
<td>R6</td>
<td>RES, 0, 5%, 0.25 W, 1206</td>
<td>Vishay-Dale</td>
<td>CRCW12060000Z0EA</td>
<td>1</td>
</tr>
<tr>
<td>R8</td>
<td>RES, xxx ohm, x%, xW, [PackageReference]</td>
<td>Panasonic Electronic Components</td>
<td>ERJ-P6WF1203V</td>
<td>1</td>
</tr>
<tr>
<td>R9</td>
<td>RES, 42.2 k, 1%, 0.1 W, 0603</td>
<td>Yageo America</td>
<td>RC0603FR-0742K2L</td>
<td>1</td>
</tr>
<tr>
<td>R10</td>
<td>RES, 3.09 k, 1%, 0.1 W, 0603</td>
<td>Vishay-Dale</td>
<td>CRCW06033K09FKEA</td>
<td>1</td>
</tr>
<tr>
<td>R11, R12</td>
<td>RES, 2.70, 1%, 0.25 W, 1206</td>
<td>Yageo America</td>
<td>RC1206FR-072R7L</td>
<td>2</td>
</tr>
<tr>
<td>T1</td>
<td>1 Input 4 Output XFMR</td>
<td>Used in BOM report</td>
<td>Used in BOM report</td>
<td>1</td>
</tr>
<tr>
<td>U1</td>
<td>Constant-Voltage, Constant-Current Controller</td>
<td>Texas Instruments</td>
<td>UCC28722DBV</td>
<td>1</td>
</tr>
<tr>
<td>V2</td>
<td>TVS DIODE 342VWM 706VC DO15</td>
<td>STMicroelectronics</td>
<td>P6KE400CARL</td>
<td>1</td>
</tr>
<tr>
<td>Vbulk, Vdd</td>
<td>Test Point, Miniature, Yellow, TH</td>
<td>Keystone</td>
<td>5004</td>
<td>2</td>
</tr>
</tbody>
</table>
6.3 PCB Layout Recommendations

Recommend not routing CS track close to Switch node as noise from switching edge can couple onto CS pin. R10 and C17 should be placed close to the CS pin and C17 should grounded close to GND pin of IC.

7 About the Author

Billy Long is a Systems Engineer at Texas Instruments, where he is responsible for developing reference design solutions for the High Voltage power supply market. Billy brings to this role his extensive experience in power supply design and system development. Billy earned his Bachelor of Electrical and Electronic Engineering (BEE) from University Collage Cork, Ireland.
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