Test data for this design was taken using the optical digital audio input. A supply voltage of 36V was provided to the board. The TPA3251D2 was setup in 2 channel BTL mode with 4ohm loads. The PCM5242 was set to a gain of 0dB with no filters.

The TAS1020B USB interface is used for configuring the PCM5242 in an evaluation setting. With both I2S and I2C from the TAS1020B, digital audio can be evaluated from a host computer as well as register configuration of the PCM5242. In an end system, I2C commands would be written to the PCM5424 by the system processor. A secondary I2C source can be connect to the reference design board via the SCL and SDA test points if needed. USB audio can be used by any device that converts USB audio data into an I2S format.

The audio performance was then measured as show in the plots below.

![THD + N Vs Output Power 4ohms](image)
This board achieves 175W into 4ohms at 10% THD + N
THD + N <0.004% at 10W
FFT -60dB Reference to 175W (10% THD + N) 4ohms

Frequency Response 1W 4ohms (Measurement setup uses 20KHz cutoff)
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