Design Guide: TiDEP-01012
Imaging Radar Using Cascaded mmWave Sensor Reference Design

Description
This reference design provides a foundation for a Cascaded Imaging Radar RF system. Cascaded Radar devices can support long-range radar (LRR) beam-forming applications as well as medium-range (MRR) and short-range radar (SRR) MIMO applications with enhanced angle resolution performance.

The AWR1243P Cascade Radar RF development kit has been used to estimate and track the position (in the azimuthal plane) beyond 350 meters with a multi-device, beam-forming configuration. Additionally, this system has demonstrated azimuth angular resolutions as small as 1.4 degrees in a TDMA-MIMO configuration.

Features
- Two or four-chip FMCW radar sensor for LRR, MRR and SRR applications
- Detect objects (for example, cars and trucks) at a distance beyond 350-m with range resolution of 35 cm; human RCS objects detectable at a distance of 150-m
- Antenna field of view ±70° with angular resolution of approximately 1.4 degrees
- MATLAB MIMO and beamforming example code provided
- AWR1243P based demonstration design
- Cascaded imaging radar front end beamforming and MIMO configuration fully explained

Application
- Long range radar
- Imaging radar
- Traffic monitoring camera

Resources
- TiDEP-01012 Design Folder
- AWR1243 Product Folder
- LP87524P-Q1 Product Folder
- TMP112 Product Folder

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1 System Description

ADAS control of a vehicle provides quality-of-life and safety benefits in addition to making the relatively mundane act of driving safer and less difficult. The quality-of-life features include the ability of a vehicle to park itself, or to determine whether a lane change is possible, and provide features like automatic cruise control—where a vehicle maintains a constant distance with respect to the car ahead of it, essentially, tracking the velocity of the car in front of it. Autonomous braking and collision avoidance are safety features that prevent accidents caused by driver inattention. These features work by observing the area in front of a car and alerting the ADAS subsystems if obstacles are observed that are likely to hit the car. Implementing these technologies requires a variety of sensors to detect obstacles in the environment and track their velocities and positions over time.

1.1 Why Cascade Radar?

Frequency-modulated continuous-wave (FMCW) radars allow the accurate measurement of range and relative velocity of obstacles and other vehicles; therefore, radars are useful for autonomous vehicular applications (such as parking assist and lane change assist) and car safety applications (autonomous breaking and collision avoidance). An important advantage of radars over camera and light-detection-and-ranging (LIDAR)-based systems is that radars are relatively immune to environmental conditions (such as the effects of rain, dust, and smoke). FMCW radars can work in complete darkness and also bright daylight (radars are not affected by glare) because they transmit and receive electromagnetic waves. When compared with ultrasound, radars typically have a much longer range and much faster time of transit for their signals.

Despite the many advantages of radar technology, in many cases, automotive manufacturers today still use camera sensors as the primary sensor technology used to make final safety decisions in the system. The radar sensor is being used as the secondary sensor; meaning, the vehicle system receives the Radar warning, but decides to take an action only upon the camera sensor verification. The main reason is limitation in radar angular resolution. The radar sensors deployed today in most vehicles lack the ability to distinguish between static objects with the same range and same relative velocity.

Today, a typical front radar sensor has about a 5-degree angular resolution that corresponds to the ability of the sensor to distinguish between objects that are 8.5 m apart at 100 m. Objects that are closer than 8.5 m appear as one object. For example, a vehicle stopped in the right lane, might look like a shoulder road street lamp for example, and therefore would be ignored by the safety system.

This is about to change with the introduction of the Imaging Radar solution from Texas Instruments (TI). The TI Imaging Radar is a four-chip cascade solution, that acts like a single-chip sensor but achieves $20\log_{10}(N_{TX})$ SNR gain in TX beamforming mode and $360/(N\pi)$ angular resolution ($N$ is the number of virtual antennas in a MIMO configuration).

The TI Imaging Radar solution, we can distinguish between static objects 0.6 degrees apart with all antennae placed in single dimension linearly, and reach a 350-m object detecting range(angular resolution is dependent on the antenna configuration and the number of TX/RX antennae).

This performance enables TI Imaging Radar to become the primary sensor in the vehicle and enhance safety across weather and visibility conditions by providing a high-resolution image for both static and moving objects.

1.2 TI Cascade Radar Design

TIDEP-01012 is an introductory application that demonstrates both a long-range beam-forming configuration, and a shorter range, high-angular resolution MIMO configuration. This reference design can be used as a starting point to design a standalone sensor for a variety of long range and imaging radar applications. The TI Cascade RF reference design has demonstrated automobile target detection in excess of 350 m along with 1.4 degree angular resolution.
The flexible chirp and frame timing engine available on the AWR1243P device (similar to other AWR family mmWave sensors) allows the system to function as a multi-mode radar, interleaving beam-forming and MIMO configurations on a per frame basis. This enables the sensor designer to achieve best range and best angular resolution across the array of Cascaded AWR1243P devices as the scene dynamics requires.

Beamforming antenna across multiple, cascaded, AWR1243P devices provide sensor designers with higher-output power and therefore lower-detectable target RCS, or increased range detection, or both. Applications requiring detection of automobile, motorcycle, pedestrian, signage, bridges, and other roadway objects and barriers at or beyond 350-m range can use this mode of operation.

In medium-range applications (150 m ranges), creating MIMO antenna arrays across multiple, cascaded, AWR1243P devices allows the sensor designer to maximize the number of active antenna enabling substantially improved angular resolution. This enables sub 1 degree resolution: true imaging radar capability.

1.3 Key System Specifications

This reference design has two sets of specifications because the radar is used as a multi-mode radar. MIMO is the first specification. TX beamforming (TXBF) is the second specification,
### Table 1. Key System Specifications

<table>
<thead>
<tr>
<th>PARAMETERS</th>
<th>SPECIFICATIONS (MIMO)</th>
<th>SPECIFICATIONS (TXBF)</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum Range</td>
<td>150 m</td>
<td>350 m</td>
<td>This represents the maximum distance that the radar can detect an object representing an RCS of approximately 10 m².</td>
</tr>
<tr>
<td>Range Resolution</td>
<td>60 cm</td>
<td>150 cm</td>
<td>Range resolution is the ability of a radar system to distinguish between two or more targets on the same bearing but at different ranges. The resolution is configurable, so the provided number is just an example.</td>
</tr>
<tr>
<td>Azimuth Angle Resolution</td>
<td>1.4 degrees</td>
<td>1.4 degrees (with multiple beam steering)</td>
<td>Angle resolution is the ability of a radar system to distinguish between two or more targets with the same range and velocity but different angles. The resolution is equivalent in both applications.</td>
</tr>
<tr>
<td>Elevation Angle Resolution</td>
<td>18 degrees</td>
<td>n/a</td>
<td>Elevation angle resolution is only available for MIMO application given the antenna design on the TI cascade EVM board.</td>
</tr>
<tr>
<td>Maximum Velocity</td>
<td>133 kmph</td>
<td>133 kmph</td>
<td>This is the native maximum velocity obtained using a two-dimensional FFT on the frame data. For TDM MIMO case, velocity compensation algorithm is applied to recover the native maximum velocity. This specification will be improved over time by showing how higher-level algorithms can extend the maximum measurable velocity beyond this limit.</td>
</tr>
<tr>
<td>Velocity Resolution</td>
<td>0.53 kmph</td>
<td>0.53 kmph</td>
<td>This parameter represents the capability of the radar sensor to distinguish between two or more objects at the same range but moving with different velocities.</td>
</tr>
</tbody>
</table>
2 System Overview

2.1 Block Diagram

Figure 2 shows the block diagram of the cascade RF board.

Figure 2. Cascade Radar RF Board System Block Diagram
2.2 **Highlighted Products**

The AWR1243P is an integrated single-chip, frequency modulated continuous wave (FMCW) sensor capable of operation in the 76 to 81 GHz frequency band. The device is built with TI’s low-power, 45-nm RFCMOS processor and enables unprecedented levels of analog and digital integration in an extremely small form factor. The device has four receivers and three transmitters with a closed-loop phase-locked loop (PLL) for precise and linear chirp synthesis.

Each transmitter includes a programmable 6-bit phase shifter (5.625 degree step) to enable beam-forming applications. Each devices also includes two 20-GHz local oscillator (LO) output and two 20-GHz LO input paths for sharing the VCO output with neighboring devices. This enables a cost-effective, totally passive, cascaded radar architecture.

The sensor includes a built-in radio processor (BIST) for RF calibration and safety monitoring. Based on complex baseband architecture, the sensor device supports an IF bandwidth of 15 MHz with reconfigurable output sampling rates in both complex and real sampling modes. Two separate Arm® Cortex®-R4F based processors run the TI provided radar front-end, calibration, and host processor interface firmware targeting ASIL-B.

2.3 **Design Considerations**

### 2.3.1 AWR1243P Cascade RF Board Features

<table>
<thead>
<tr>
<th>Cascade Radar RF Board</th>
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</thead>
<tbody>
<tr>
<td>4 × AWR1243P 76-81GHz Radar SoC</td>
</tr>
<tr>
<td>Integrated VCO, LO distribution, PA, LNA, ADC, 3 TX and 4 RX Arm MCU R4 Controller</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>AWR RF Peripherals</th>
</tr>
</thead>
<tbody>
<tr>
<td>12 × TX, 16 × RX Antennas</td>
</tr>
<tr>
<td>12 total transmitters across all 4 AWR1243 P devices16 total receivers across all 4 × AWR1243P devices</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Azimuth Array</th>
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</thead>
<tbody>
<tr>
<td>86 element virtual array – enabling 1.4 degree angular resolution</td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>Elevation Array</th>
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</thead>
<tbody>
<tr>
<td>4 element virtual array – enabling 18 degree angular resolution</td>
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</table>

<table>
<thead>
<tr>
<th>Embedded Antenna</th>
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</thead>
<tbody>
<tr>
<td>Rogers RO3003 4-element, series-fed, patch antenna</td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>20 GHz LOStar Distribution</th>
</tr>
</thead>
<tbody>
<tr>
<td>2 × passive Wilkinson Power dividers fed by the Master AWR12x device LO output to Slave AWR12x devices and Master AWR12x device</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>AWR Digital Peripherals</th>
</tr>
</thead>
<tbody>
<tr>
<td>Clock Distribution</td>
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<tr>
<td>LMK00804B low-jitter clock distribution</td>
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<table>
<thead>
<tr>
<th>Digital Sync Distribution</th>
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<tbody>
<tr>
<td>LMK00804B low-jitter clock distribution</td>
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</table>

<table>
<thead>
<tr>
<th>CSI2.0 4-lane</th>
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</thead>
<tbody>
<tr>
<td>600Mbps/Lane, max 2.4Gbps ADC IF data per device</td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>QSPI Flash</th>
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<tbody>
<tr>
<td>16Mbit QSPI flash for AWR firmware updates</td>
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</table>

<table>
<thead>
<tr>
<th>Serial Peripherals</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPI, I2C, UART, GPIO</td>
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</tbody>
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<table>
<thead>
<tr>
<th>System Temperature</th>
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</thead>
<tbody>
<tr>
<td>TMP112 I2C Temperature Sensors</td>
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<table>
<thead>
<tr>
<th>Power</th>
</tr>
</thead>
<tbody>
<tr>
<td>Radar Power Management IC (PMIC) Solution</td>
</tr>
<tr>
<td>2 × LP87524P-Q1 Quad-Channel, Integrated FET, Buck Converters and LC filtering solution</td>
</tr>
</tbody>
</table>

### 2.3.2 AWR1243P Cascade RF Board Architecture

The AWR1243P Cascade RF board consists of four AWR1243P 77-GHz radar devices and their associated power, clocking, synchronization, LO, and RF circuits.

Each AWR1243P RF, RX, and TX port is routed to its own set of etched, patch antenna. Each AWR1243P on the RF board has a 4-port CSI2.0 transmitter that is used for sending radar data to a host processor CSI2.0 receiver set. The entire AWR1243P configuration, control, and reset lines are made available on two host-interface connectors implemented with Hirose FX32 series connectors.
The AWR1243P devices are separated into master and slave devices classes. AWR1243P #1, the master device, uses the AWR1243P architecture built in LO distribution, clock distribution and frame synchronization distribution to provide 40-MHz clock, 20-GHz LO and digital frame synchronization to other three slave devices – AWR1243P #2, #3 and #4. This allows the system to generate and receive coherent FMCW chirps across the 4 AWR1243P device array of transmitters and receivers; enabling beam-forming and MIMO operation across the array of devices.

The 20 GHz LO distribution follows the start-network configuration described in the AWR1243 Cascade Radar Application Note, SWRA574A. With the master AWR1243P #1 feeding a network of two Wilkinson power dividers, that provide synchronous LO for the Master and Slave RF PA and mixer subsystems. All clock distribution, synchronization distribution, and LO distribution requirements are documented in this referenced application note.

The Cascade RF board accepts 5V DC, 8 A (max) power through the host board connectors. The primary 5 V system rail can be converted into the various AWR12x device rails by a pair of LP87524P, quad-channel, monolithic, buck-converters.

### 2.3.3 20 GHz Local Oscillator (LO) Distribution

20-GHz LO distribution to all AWR1243P devices is accomplished through an entirely passive, transmission-line and etched power divider network. By using the dual LO outputs and LO inputs provided on each AWR1243P device, all devices including the master device, receive the same master device generated LO output. This results in all package and die routing delays common across all devices. The PCB designer is left with the task of delay matching only the BGA to BGA delays.

By using both of the 6-dBm (typical) LO output, and minimizing transmission line and power divider losses, no external amplifiers are required to be added to the LO network. Additionally, by outputting 20 GHz LO versus full 77-GHz RF for RF synchronization, the LO PCB link budgets can be more relaxed.

The passive LO distribution network and relaxed link budget results in more PCB design flexibility and a lower overall system cost.

Figure 3 shows the block diagram of the delay matched LO distribution. Figure 4 shows the implemented Wilkinson power dividers in GCPW transmission-line. Fanout from the AWR1243P devices and power dividers is through GCPW lines. The majority of the LO distribution length is carried through an internal Stripline layer.
Figure 3. 20 GHz LO Distribution Block Diagram

Figure 4. 20 GHz LO Distribution Board Implementation
3 System Design Theory

The Cascade RF MIMO and beamforming antenna design, chirp, and frame configuration are system variables that must be co-designed for a particular application. Both the antenna array, MIMO chirp, and beamforming chirp design are presented here.

3.1 Antenna Configuration

Figure 5 shows the antenna array on the Cascade RF board with one master device and three slave devices. Together these devises create a total of 12 TX channels and 16 RX channels. Among the 12 TX channels, the three TXs (TX1/2/3) from the master device are placed in the vertical direction for elevation antenna estimation. The remaining 9 TXs (TX4 through TX12) and all 16 RX channels are placed in the horizontal plane for azimuth angle estimation. The relative distances between TXs and between RXs are shown in Figure 4. The three elevation antennas are placed to form a minimum redundancy array to improve elevation angle resolution. Every two azimuth TXs are spaced 2 wave length apart. RX array A and RX array C are placed 16-wavelengths apart. RX array C and RX array B are placed four-wavelengths apart. The virtual array in MIMO mode is shown in Figure 5, with 86 virtual antennas in azimuth direction. The overlapped antenna in azimuth is not shown in the figure. The elevation angle resolution is equivalent to the resolution achieved with 7 antennas.

The Cascade RF board can also be used in two chip cascade mode with 6 TX and 8 RX channels highlighted in Figure 6. Figure 6 shows the virtual MIMO array in 2-device cascade mode using TX ID of [1 2 3 10 11 12] and RX ID of [4 3 2 1 16 15 14 13], with 23 azimuth antennas and same elevation configuration with 4-device cascade.
This distance is to meet 2-chip cascading requirement (azimuth and elevation)

Scale unit is λ
3.2 **Chirp Configuration**

Chirp configurations are different for MIMO and TXBF use cases.

3.2.1 **Chirp Configuration for MIMO Radar**

For the MIMO application, to achieve the system performance specified above, the chirp configuration in Table 2 is used.

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SPECIFICATIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Idle time (µs)</td>
<td>4</td>
</tr>
<tr>
<td>ADC start time (µs)</td>
<td>5</td>
</tr>
<tr>
<td>Ramp end time (µs)</td>
<td>23</td>
</tr>
<tr>
<td>Number of ADC samples</td>
<td>256</td>
</tr>
<tr>
<td>Frequency slope (MHz/µs)</td>
<td>15</td>
</tr>
<tr>
<td>ADC sampling frequency (kSPS)</td>
<td>15</td>
</tr>
<tr>
<td>Number of chirps per frame</td>
<td>256</td>
</tr>
<tr>
<td>Effective chirp time (µs)</td>
<td>17</td>
</tr>
<tr>
<td>Bandwidth (MHz)</td>
<td>256</td>
</tr>
<tr>
<td>Frame length (ms)</td>
<td>4.4</td>
</tr>
</tbody>
</table>

With this chirp configuration the MIMO performance shown in Table 2 is achieved. The primary goal was to achieve a maximum distance of about 150 m and maximum unambiguous velocity of 133 km/h. See [Programming Chirp Parameters in TI Radar Devices](#) for more details. The choice of the chirp periodicity is a trade-off between range resolution and maximum velocity. The velocity resolution is defined by the number of chirps per frame.
3.2.2 Chirp Configuration for TX Beamforming Radar

For the TX beamforming application, to achieve the system performance specified above, the chirp configuration in Table 3 is used.

### Table 3. Chirp Configuration - TX Beamforming

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SPECIFICATIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Idle time (µs)</td>
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</tr>
<tr>
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<td>23</td>
</tr>
<tr>
<td>Number of ADC samples</td>
<td>256</td>
</tr>
<tr>
<td>Frequency slope (MHz/µs)</td>
<td>15</td>
</tr>
<tr>
<td>ADC sampling frequency (MSPS)</td>
<td>15</td>
</tr>
<tr>
<td>Number of chirps per frame</td>
<td>256</td>
</tr>
<tr>
<td>Effective chirp time (µs)</td>
<td>17</td>
</tr>
<tr>
<td>Bandwidth (MHz)</td>
<td>256</td>
</tr>
<tr>
<td>Frame length (ms)</td>
<td>4.4</td>
</tr>
</tbody>
</table>

With this chirp configuration the TXBF performance shown in Table 3 is achieved. The primary goal was to achieve a maximum distance of about 350 m and maximum velocity around 133 km/h. See Programming Chirp Parameters in TI Radar Devices for more details.

The amount of phase value to program to each TX channel is computed as a function of array factor and target angle. Assuming N TX channels, with TX1 as a reference, the distance between every other antenna and TX1 is the known distance when the antenna array is designed during board development. As shown in Figure 9 for TI cascade EVM, the distance between any two adjacent TX channels is two times the wavelength.

![Figure 9. TX Array for Beamforming](image)

Given the notation in Figure 9, the phase value for each TX channel is calculated as:

\[
\Phi = [\phi_1, \phi_2, \phi_3, \ldots, \phi_N] = [0, 2\pi \frac{d_2}{\lambda} \sin \theta, 2\pi \frac{d_3}{\lambda} \sin \theta, 2\pi \frac{d_4}{\lambda} \sin \theta, \ldots] \tag{1}
\]

The ideal phase value is further quantified by the allowed phase step size of 5.625 degree to calculate the integer value to be programmed to the registers (TX calibration phase value will also be added as shown in the next section).

\[
\Phi_{int} = \left\lfloor \frac{\Phi}{5.625} \right\rfloor \tag{2}
\]

As an example, for TI cascade EVM, 9 azimuth TX antennas can be used for beam steering, with \([d_2 = 2\lambda, d_3 = 4\lambda, \ldots, d_9 = 16\lambda]\). If the desired steering angle is 30 degrees, then the phase vector is as shown in degrees in Equation 3.
The TX beamforming application supports both chirp based beam steering and frame based beam steering. Advanced frame configuration is used for either case.

3.2.2.1 Chirp Based Beam Steering

In the subframe used for TX beamforming, the number of chirps used in this subframe is equal to the number of different desired steering angles. Each chirp configuration is associated with phase values for the TX array, calculated based on the corresponding desired steering angle. Within a burst, all chirp configurations are looped over till the end of the burst. Figure 10 demonstrates the mechanism of chirp based beam steering.

Figure 10. Chirp Based Beam Steering

3.2.2.2 Frame Based Beam Steering

In the subframe used for TX beamforming, the number of chirps used in this subframe is equal to the number of different desired steering angles. Each chirp configuration is associated with phase values for the TX array, calculated based on the corresponding desired steering angle. The number of burst within this subframe is equal to the number of different desired angles (same as number of chirp configurations). Each burst is associated with each chirp configuration, so a burst is equivalent to a frame within which each chirp has the same phase shifter value. Figure 11 demonstrates the mechanism of frame based beam steering.
3.3 Antenna Calibration

The purpose of antenna calibration is to estimate the frequency, phase and amplitude mismatches across one master device and three slave devices. The mismatches can be caused by various reasons, such as path length mismatch, chip to chip variation, antenna coupling, and so on. The frequency mismatch is usually minimized by routing path length match during the board layout stage. The calibration method proposed by TI development kit is a one-time boresight calibration method. It is suggested to perform board specific calibration to achieve best angle performance.

3.3.1 How To Generate Phase/Gain Calibration Matrix

To generate a calibration matrix, it is suggested to put a corner reflector at distance of 5 m and beyond, with a typical corner reflector RCS of 1 m² to approximately 2 m², as shown in Figure 12. The corner reflector should be aligned with the array center of board in both azimuth and elevation direction. A level sensor with laser pointer can be used for alignment.
When the corner reflector is placed at the center position, a few frames of raw ADC data needs to be collected by using the example script provided in the mmWave Studio release. During the data collection, turn on 12 TX in a TDM MIMO mode and all 16 RX channels are on at the same time.

The calibration matrix generation follows the sequence described by Figure 13. After the raw binary data is read and formatted so that data from 192 virtual channels are separated. Range FFT is performed on each channel and the peak corresponding to the corner reflector is identified as the local maximum within \([D - 1, D + 1]\) meters, where \(D\) is the approximate target range provided by user. The FFT peak index from all 192 channels form a matrix size of 12 x 16, which will be used for frequency calibration step. The complex values at the peaks from all 192 channels form a complex calibration matrix of size 12 x 16, which will be used for phase and amplitude calibration. These two matrices are saved to a matlab .mat file to be loaded when applying the calibration results to the ADC data.
3.3.2 Applying Calibration For MIMO Operation

The saved calibration information is applied to the raw ADC data collected in MIMO mode in two steps on the flow shown on Figure 14.

3.3.2.1 Frequency Calibration

The FFT index of TX1/RX1 channel is used as reference that is compared with all the other 191 channels. The FFT index difference and chirp parameters are used to compute the frequency calibration vector using Equation 4.

\[
f = \frac{1}{2\pi} \times \Delta P \times \frac{f_{\text{calib}}}{N \times f_{\text{interp}}} \times \tilde{n}
\]

where
- \(\Delta P\) is the FFT peak index difference for a virtual channel
- \(f_{\text{chirp}}\) is the FMCW chirp frequency slope
- \(f_{\text{calib}}\) is the FMCW chirp frequency slope used for calibration which can be different from \(f_{\text{chirp}}\)
- \(N\) is the number of ADC samples per chirp
- \(n\) is \([0, N-1]\) is the ADC sample index
- \(f_{\text{interp}}\) is the interpolation factor used for frequency calibration that is further explained in the next section
- \(F\) is the computed frequency calibration vector
- \(\exp(-j \times F^*)\) is multiplied with the raw ADC data for frequency calibration

For each virtual channel, \(F^*\) is calculated and applied to the corresponding ADC data.

3.3.2.2 Phase and Amplitude Calibration

After frequency calibration, the phase and amplitude calibration value is calculated based on the complex value of the reference channel

\[C_{\text{ph-am}} = \frac{C_{\text{ref}}}{C_{\text{tx1/rx1}}}
\]

where
- \(C_{\text{ref}}\) is the complex value of the reference channel
- \(C_{\text{tx1/rx1}}\) is the complex value of the other channels
• $C_{\text{ph-am}}$ is the calculated phase and amplitude calibration value

$$C_{\text{ph-am}} = 1$$ for reference channel and different for every other channel

$C_{\text{ph-am}}$ is multiplied with the output from the frequency calibration and the result is the final calibrated ADC data

(5)

Figure 14. Flow to Apply Calibration Matrix to ADC Data

3.3.3 Applying Calibration For TX Beam-forming Operation

The saved calibration information is applied in TX beamforming operation as shown in Figure 15. The phase value of the 12 $\times$ 16 complex value matrix obtained from section “How to generate calibration matrix” is averaged across the 16 RX channels to obtain a phase calibration vector for the 12 TX channels. According to the index of TX channel used in actual TX beamforming mode, the corresponding phase value within this 12 $\times$ 1 vector is used for the phase compensation. The compensation phase value together with the phase value calculated based on the desired steering angle is used to program the phase shift register value before starting beam steering process.

Figure 15. Calibration in TX Beamforming
Similarly, the phase across the 12 TX channels is averaged to obtain a phase calibration vector for the 16 RX channels. This is phase only RX calibration assuming that there is no frequency mismatch across the 16 RX channels. The frequency mismatch across 16 RX channels can be captured by collecting calibration data by following these steps.

1. Put a corner reflector at a known distance, for example, approximately 6 m
2. Configure all TX channels to steer towards zero degrees
3. Check the FFT peak index of all 16 RX channels; the FFT peak index indicates the frequency mismatch, similar to the frequency mismatch described in Section 3.3.2.

The phase calibration value is applied during the RX data post processing stage. More details will be discussed in the signal processing section of TX beamforming.

### 3.4 Cascade Radar Signal Processing Chains

#### 3.4.1 MIMO Signal Processing Chain

The raw signal collected in MIMO mode is processed in the flow shown in *Figure 16*.

#### 3.4.1.1 ADC Data Read and Calibration

After each data collection, a binary data file and the corresponding chirp configuration parameters are saved. These two files are the inputs to the ADC data read and calibration module. The binary data file is read/parsed according to the chirp parameters based on the data format, samples per chirp, chirps per frame, number of TX/RX channels. Then the data is reformatted into a 4D matrix, with the dimension of samples per chirp, chirps per frame, number of RX channels, and number of TX channels. Each of the TX/RX channel is calibrated according the pre-calculated calibration matrix that can be obtained using the procedures as described in Section 3. (reference)

#### 3.4.1.2 Range/Doppler FFT

For each TX/RX channel, range FFT is performed followed by Doppler FFT. The default windowing is a Hanning window. The FFT size is determined as the closest integer that is power of 2.
3.4.1.3 Detection

The output of range/Doppler FFT is sent to the detection module. The first step is non-coherent integration across all the virtual channels. The integration output goes through the CFAR detection step that does 1D range CFAR detection followed by 1D Doppler CFAR detection at the detected range bins. For each cross detected range/Doppler point, the maximum velocity extension algorithm is applied to correct any possible velocity ambiguity caused by TDM MIMO. The recovered velocity is used to correct the phase jump that is again caused by the TDM MIMO for each detected point. The details of maximum velocity extension and phase compensation will be discussed in a separate section.

3.4.1.4 Angle Estimation

For each detected point, angle estimation is performed based on the signal vector with corrected phase. If the configured array is azimuth direction only, only azimuth angle estimation is needed. If a 2D configured array is involved, both azimuth and elevation angle estimation is performed. Note that multiple angles will be detected in the azimuth direction, but only the maximum peak is selected in the elevation direction.

3.4.1.5 GUI

Point cloud and heat map can be sent to GUI for visualization.

3.4.2 TX Beamforming Signal Processing

This section provides an example signal processing chain in TX beamforming mode. Users might have their own preferred signal chain depending on the application. This example signal processing chain is used to generate an azimuth/range heat map by stitching multiple TX beamforming scans together.

Figure 17 shows the block diagram of the example signal processing chain in TX beamforming mode. The desired steering angles are defined as \( \beta_1 \beta_2 \ldots \beta_n \). For each angle \( \beta_i \), the phase shifter value is calculated based on the TX phase calibration vector and \( \beta_i \) with given TX antenna positions (see Section 2.3.3). The received ADC data from each RX channel goes through range and Doppler FFT, and only zero Doppler bin is selected for range/azimuth heat map generation assuming it is a static test scene. For moving test scene, user needs to change accordingly. For each range bin at zero Doppler, after phase compensation using calibration vector, RX beamforming is performed to steering the RX beam towards the same \( \beta_i \) where the TX channels are focusing on. As a result, one range line is generated for each angle \( \beta_i \). By stitching all the lines generated with different \( \beta_i \) angles, a range/azimuth heat map is generated.

![Figure 17. TX Beamforming Signal Processing Chain](image)
Figure 18 shows the example heat map results. Two corner reflectors are separated by around 1.8 degrees at 6 m within the anechoic chamber. The anechoic chamber size is around 6 m in width and 12 m in length. The radar is put around 4 m away from one side. The top left and top right plots are the heat map results (top-down view) running in MIMO mode and stitched TX beamforming mode. The TX beam is steering within [–60 60] with step size of 0.5 degree. The rectangular shape corresponding to the anechoic chamber wall, and the two bright spots in the middle are corresponding to the two corner reflectors. The bottom left and bottom right plots are the heat map results in a 3D view, with the height indicates the reflection intensity. The separation of the two peaks is more visible in the 3D view. It is worth to note that the grading lobes caused by the wide distance of adjacent TX channels are no longer observed because of the cancellation in the RX beamforming process.

This experiment result proves that the receiver angle resolution in these two operation modes are equivalent since the effective aperture size is the same. Further, the signal SNR in the beamforming is much higher due to the coherent gain in TX beamforming mode. The SNR in MIMO mode can be improved by increasing the chirp integration time.
4 Hardware, Software, Testing Requirements, and Test Results

4.1 Required Hardware and Software

4.1.1 Hardware

The tests were performed using the AWR1243P four-device Cascade Radar RF board from Texas Instruments together with TDA2 based capture System.

Figure 20. AWR1243P Four-Device Cascade Radar RF Board with TDA2 Based Capture Board

4.1.2 Software

The cascade board was configured using an updated version of the MMWAVE-STUDIO tools. Algorithms implemented in Matlab were used to post process the captured raw data.
4.2 Testing and Results

Multiple test scenarios were setup to explore the capabilities of the Cascade Radar RF design in both MIMO and TX beamforming operation.

All data presented here was collected with the Hardware mentioned above.

4.2.1 Test Scenarios

4.2.1.1 MIMO Test Scenarios

- In-lab Angular Resolution
- Side-by-side Car Detection Resolution
- Side-by-side Car Detection Resolution – MUSIC Algorithm
- Car, Pedestrian and Other Targets Close Range Separation
- Car Contour and Orientation
- Car Door Contour
- Bike Contour and Orientation
- Fence Contour Detection
- Curb Contour Detection
- Manhole Contour Detection
- Height Measurement
- Single-Device vs. Cascade Devices

4.2.1.2 TX Beamforming Test Scenarios

- In-lab Beamforming Control Pattern
- Pedestrian Long-Range Detection
- Car Medium-Range detection
- Car Long-Range Detection

4.2.2 MIMO Test Results

4.2.2.1 In-lab Angular Resolution

A basic azimuth separation test was performed within an anechoic chamber radar test range. Two corner reflectors were placed approximately 8 m from the AWR1243P radar sensor. 1.5 degrees of azimuth separation was applied to the reflectors and the resulting azimuth resolution was measured.

In the current configuration, with 86 virtual array elements in the azimuth axis, and lambda/2 spacing of each element, a best-case 1.4 degree angular resolution is possible. This system measured a 1.5 degree separation, which is in close alignment to the expected.
Figure 21. Two Corner Reflectors Separated by 1.5 Degrees in Azimuth

Figure 22. Range-Doppler FFT Plots Showing Detected, Separated Peaks From Both Reflectors
4.2.2.2 Car Angular Resolution Scenario

A test was performed with two cars located at 112 m range from the sensor at varying angular separations, again showing the angular resolution capabilities of the AWR1243P MIMO radar operation.

Figure 23. Two-Car Angular Separation Test Setup

Figure 24. Two-Car Angular Separation Test Results

Distance = 112 m
Separation b/w cars = 2 m
Angular separation = 1.0°

Distance = 112 m
Separation b/w cars = 3.3 m
Angular separation = 1.7°
4.2.2.3 Car, Pedestrian, and Other Targets Close Range Separation

A test was performed showing the AWR1243P MIMO radar is able to separate a person and a bicycle at different lateral distances away from the car. This represents a more challenging scenario due to the pedestrian (or another low RCS object) occupying the same range bin as the high reflecting point from the car (high RCS object).

![Figure 25. Bicycle and Person 1.5 Meters Away From Vehicle](image)

![Figure 26. Bicycle and Person 1.0 Meter (left) and 0.5 Meter (right) Away From Vehicle](image)

4.2.2.4 Car Contour and Orientation

A test was performed showing the AWR1243P MIMO radar detecting the contour and orientation of a car. The azimuth and range resolution allows for the detection of the contour of many vehicle surfaces.
Figure 27. Car at 0 Degrees (Top), 90 Degrees (Middle) and 135 Degrees (Bottom) Orientation and Corresponding Azimuth Range Heatmap
4.2.2.5 **Curb Contour Detection**

A test was performed showing the AWR1243P MIMO radar detecting the contour and orientation of multiple curbs in a parking lot setting. The azimuth and range resolution allows for the detection of the contour of these shorter driving obstacles.

**Figure 28. Parking Lot Curb Scene (Top), and Resulting Azimuth Range Heatmap (Bottom)**
4.2.3 TX Beamforming Results

4.2.3.1 In-lab Beamforming Control Pattern

A basic beam steering test was performed within an anechoic chamber radar test range. A single corner reflector was placed approximately 8 m from the AWR1243P radar sensor. Beamsteering vectors were then programmed into the AWR1243P devices to achieve a 15 degree beam rotation. The resulting target return versus angle graph was then compared against the simulated 15-degree beam rotation pattern. Close alignment between the simulated and measured system was observed.
Figure 29. Simulated (Top) and Measured (Bottom) 15 Degree Beam Steering Test
4.2.3.2 Pedestrian Long-Range Detection

In this test a pedestrian jogging from 30 m to 140 m. The AWR1243P Cascade RF board is running in 9-TX beamforming mode.

The range profile shows the pedestrian at 120 m with 20-dB SNR. The Doppler-Range plot is shown to the right. Slight Doppler displacement can be seen corresponding to the pedestrian velocity.

Figure 30. Range Profile (top left), and Doppler-Range Plot of a Pedestrian Jogging Away From Radar at 120 Meters Range

4.2.3.3 Car Long-Range Detection

Similar to the pedestrian long range detection test, this test observes a car at 350-m range. The AWR1243P Cascade RF board is running in 9-TX beamforming mode.

The range profile shows the vehicle at 350 m with 14-dB SNR.
Figure 31. Range Profile (left), and Doppler-Range Plot of a Vehicle 350 Meters Range
5 Design Files

5.1 Schematics
To download the schematics, see the design files at TIDEP-01012.

5.2 Bill of Materials
To download the bill of materials (BOM), see the design files at TIDEP-01012.

5.3 Altium Project
To download the Altium Designer® project files, see the design files at TIDEP-01012.

5.4 Gerber Files
To download the Gerber files, see the design files at TIDEP-01012.

5.5 Assembly Drawings
To download the assembly drawings, see the design files at TIDEP-01012.

6 Software Files
The cascade board was configured using an updated version of the MMWAVE-STUDIO tools.

7 Related Documentation
1. Texas Instruments, Programming Chirp Parameters in TI Radar Devices Application Report
2. Texas Instruments, AWR1243 76-GHz to 81-GHz high-performance automotive MMIC
3. Texas Instruments, AWR1243 Cascade Application Report
4. Texas Instruments, MMWAVE-STUDIO

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