LM5020, LM5025, LM5030

PWM DC-DC Controllers with Built-In Start-Up Regulators Simplify Switching Power Supply Design

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**Modular Current Sharing Controller**

The LM5080 is a simple and cost-effective load-share controller that provides all of the functions required to balance the currents delivered from multiple power converters operated in parallel. The LM5080 implements an average program method of active load share control which adjusts the output voltage of individual power stages either up or down to deliver nearly equal currents to a common load. The average program method improves stability and reduces the output voltage tolerance when compared to other common load sharing methods. The LM5080 supports two common applications for load share controllers: external control in which the load share circuit balances currents between separate power modules (bricks), and internal control where the load share circuit is integrated into the voltage regulation loop of each power converter module or circuit.

**Features**
- Single-wire star link current share bus
- No precision external resistors necessary
- 3V to 15V bias voltage range
- Adaptable for high or low side current sensing
- Flexible architecture allows 4 modes of operation:
  - Negative remote sense adjustment
  - Positive remote sense adjustment
  - Trim or reference adjustment
  - Feedback divider adjustment

The LM5080 is available in MSOP-8 packaging and is ideal for use in consumer electronics, industrial test equipment, data communications systems, automotive power systems, distributed power systems, and battery-powered applications.


**First 7V to 75V Input, 2.5A Buck Regulator**

The LM5005 high-voltage switching regulator features all of the functions necessary to implement an efficient high-voltage buck regulator using a minimum of external components. This easy-to-use regulator includes a 75V N-Channel buck switch with an output current capability of 2.5A. The regulator control method is based upon current-mode control utilizing an emulated current ramp. Current-mode control provides inherent line feed-forward, cycle-by-cycle current limiting, and ease of loop compensation. The use of an emulated control ramp reduces noise sensitivity of the pulse-width modulation circuit, allowing reliable control of very small duty cycles necessary in high input voltage applications. The operating frequency is programmable from 50 kHz to 500 kHz.

**Features**
- Integrated 75V power MOSFET supports load currents up to 2.5A
- Adjustable output voltage from 1.225V
- Unique, easy-to-use emulated peak current mode control topology enables high frequency operation at VIN up to 75V
- Programmable switching frequency with bi-directional synchronization capability simplifies system design
- Highly integrated, high-speed, full-feature PWM regulator reduces overall solution size

The LM5005 is available in a power enhanced TSSOP-20 package featuring an exposed die attach pad to aid thermal dissipation. It is ideal for use in consumer electronics, telecommunications, data communications systems, automotive power systems, and distributed power applications.

Although PWM DC-DC switching power converters are based on simple topologies, making practical power supplies out of them requires the addition of various functions such as start-up bias, soft-start, switch driving, regulation, short circuit protection, over-voltage protection, over-temperature protection, etc. Today, most of these functions are usually implemented within a compact DC-DC PWM controller integrated circuit. However, the problem of starting the DC-DC converter in telecom and other high-voltage applications (i.e., where the input voltage exceeds about 15V) is often not addressed. The controller requires a bias supply voltage to run from so that it can produce gate drive pulses and other required signals. But at turn-on the only voltage available is the input voltage, which, if it is greater than 15V, is typically too high to be used as the bias and gate drive supply voltage. It is therefore necessary to lower the input voltage to 15V or below to start-up the power supply. Once the supply is running, the output voltage or a voltage off of a transformer or inductor winding can be used to provide the bias supply for the IC.

But most DC-DC controllers are designed without start-up circuitry, and the power supply designer is expected to add a separate start-up circuit and a bias supply to them (Figure 1a). This improves the versatility of the PWM controllers, allowing them to be operated with a wider input voltage range, but the extra start-up circuitry they require increases in the complexity and size of the power supply.

National has solved this problem for the designer in its LM50xx family of 8V to 100V PWM controllers (which includes the LM5020, LM5025, LM5030, and others) by integrating a high-voltage start-up circuit within the IC (Figure 1b). This is achieved by fabricating the controller using a 100V process. The high-input voltage can then be directly applied to the VIN pin of the controller, which is the input to an internal linear voltage regulator. This regulator produces a voltage VCC of about 8V that is used to provide start-up power to the IC.

The VCC voltage of the linear regulator is made externally accessible at the VCC pin, for several reasons. One reason is that the VCC pin is the connection point in the linear regulator for an external output capacitor that keeps the VCC voltage clean. Another reason is that VCC can serve as the power source for other low-voltage ICs in the circuit such as op amps, logic, and gate drivers.

The VCC pin can also be used to reduce the power dissipation in the controller and to increase the efficiency of the power supply. LM50xx controllers will operate indefinitely off of the input voltage and the internally generated VCC.
Voltage (as illustrated in Figure 2a) in a flyback converter. But this leads to increased power dissipation in the IC. This dissipation is

$$P_D = (V_{IN} - V_{CC})I_S$$

where $I_{QG}$ is the supply current of the controller is the sum of the controller quiescent current, and $I_S$ is the frequency-dependent gate drive current. This MOSFET current is given by

$$I_S = Q_{FS}f_S$$

where $Q_{FS}$ is the total gate charge of the MOSFET at a gate voltage of $V_{CC}$ and $f_S$ is the switching frequency.

$P_D$ can be excessively large for the controller at high-input voltages, high-switching frequency, and when the IC is driving a large MOSFET that requires a significant gate-drive current. LM50xx controllers are designed such that this power dissipation can be circumvented. In all switching power supply topologies, it is easy to derive a bias voltage from a transformer or inductor winding once the power supply has started running. In LM50xx controllers, this voltage (once available), can be applied directly to the $V_{CC}$ pin to provide power for the IC, and can also be used to power other parts of the system. In all LM50xx controllers, if this applied voltage is greater than the 8V output of the internal regulator, the regulator shuts down, eliminating the power dissipation just described. This can lead to an efficiency improvement of 1% or more if the bias supply is properly designed. (Figure 2b)

Nevertheless, in lower power systems with lower input voltages it is often advantageous to dispense with the bias supply and to run the supply off of the internal linear regulator. This simplifies the supply and reduces its cost and is often used with LM50xx controllers for output power levels of up to about 30W. In summary, National Semiconductor’s high-voltage PWM controllers with integrated start-up regulators allow the power supply designer to reduce circuit complexity, solution size, component costs, design time, and to increase circuit reliability.

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Current-Mode Controller for Forward Converters with Active-Clamp Reset

The LM5026 PWM controller contains all of the features necessary to implement power converters utilizing the active clamp/reset technique with current-mode control. With the active-clamp technique, higher efficiencies and greater power densities can be realized compared with conventional catch winding or RDC clamp/reset techniques. The device can be configured to control either a P-Channel or N-Channel clamp switch. The main gate driver features a compound configuration, consisting of both MOS and Bipolar devices, providing superior gate drive characteristics.

Additional features include line under-voltage lockout, cycle-by-cycle current limit, PWM slope compensation, soft-start, 1 MHz capable oscillator with synchronization input/output capability, precision reference, and thermal shutdown.

Features
- Wide range (8V to 100V) start-up bias regulator
- Two high-speed power MOSFET drivers: 3A main output driver and 1A clamp driver
- User-programmable maximum duty-cycle and UVLO hysteresis thresholds
- User-programmable gate driver overlap and dead-time
- Versatile dual-mode over-current protection with hiccup mode delay timer

The LM5026 is available in TSSOP-16 or thermally enhanced LLP-16 packaging and is ideal for use in telecommunications power systems, +42V automotive power systems, -48V distributed power systems, industrial power supplies, and multi-output power supplies.


Industry’s First 100V Dual Interleaved Active Clamp Current-Mode Controllers

The LM5032 and LM5034 are flexible controllers that can be configured to control two primary power stages. In the first case, the two PWM channels operate 180 degrees out of phase with one another, or are interleaved, which reduces the input ripple current. In the single-output configuration, the interleaving also reduces ripple current in the output filter capacitor. The LM5032 controller can be used for designing dual-interleaved boost, flyback or standard forward converters. The LM5034 controller is specifically designed for interleaved forward converters with active clamp transformer reset.

Features
- Two independent current-mode controllers
- Interleaved single or dual output operation
- Compound 2.5A main FET gate drivers
- Active clamp FET gate drivers
- Integrated 100V start-up regulator
- Up to 1 MHz switching frequency programmed by a single resistor
- Programmable maximum duty cycle
- Adjustable soft-start and input undervoltage sensing
- Adjustable deadtime between main and active clamp gate drivers

The LM5032/34 are available in TSSOP-16 (LM5032) and TSSOP-20 (LM5034) packaging and are ideal for use in telecom infrastructure, networking, industrial, and automotive power supplies.

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