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PG01: TITLE
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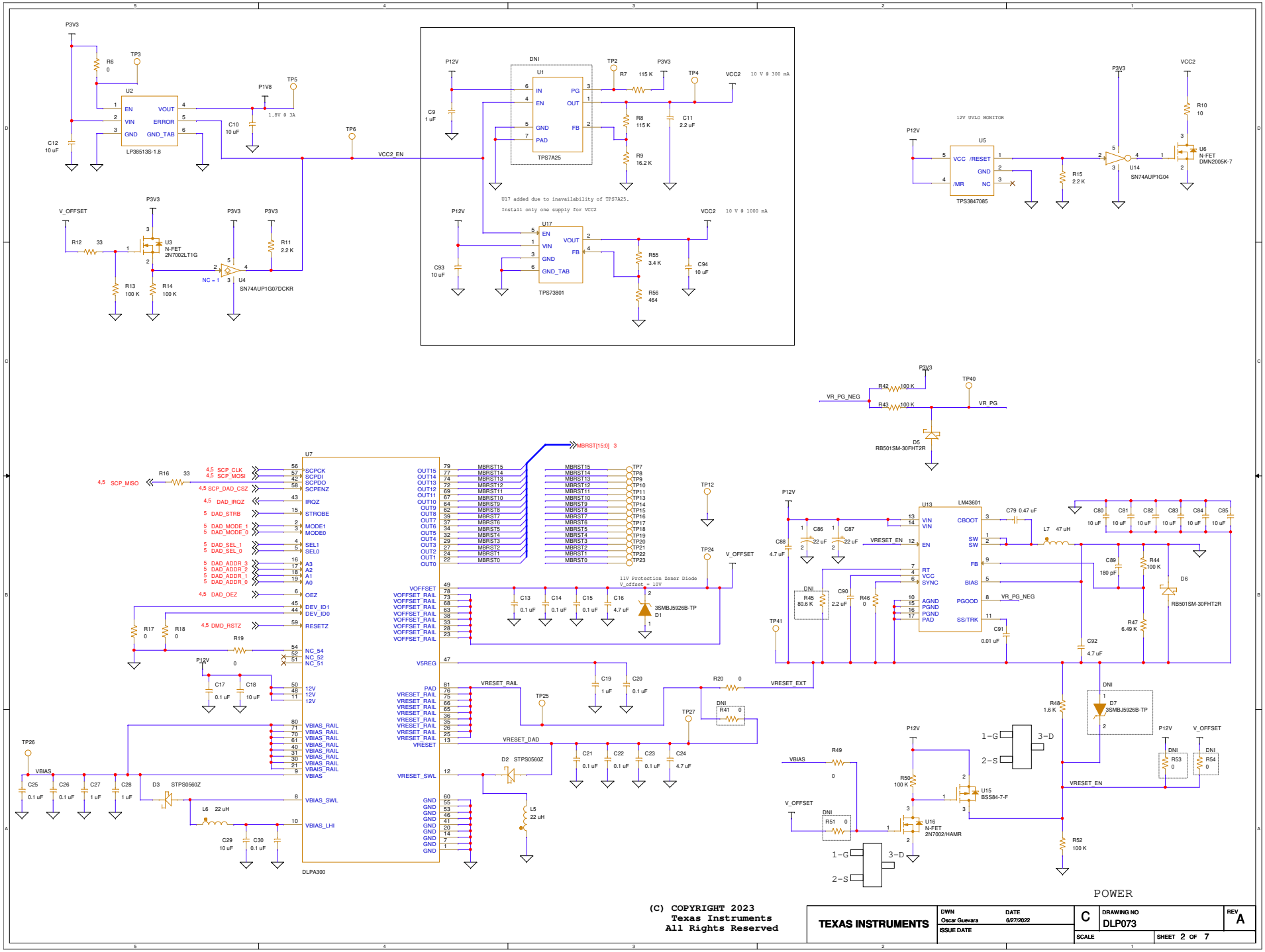
		SYST	TITLE: DLP780NE DMD Board
		PRJ	
		QA	
NEXT ASSY	USED ON		C / DRAWING NO DLP073 / REV C
APPLICATION		SW Cadence Capture V.17.2	SCALE / SHEET 1 of 7


P3V3
R6
0
TP3
U2
EN
VIN
GND
VOUT
ERROR
GND_TAB
LP38513S-1.8
C12
10 uF
C10
10 uF
P1V8
TP5
1.8V @ 3A
TP6
VCC2_EN
P12V
DNI
U1
IN
EN
GND
PAD
PG
OUT
FB
TPS7A25
TP2
R7
115 K
TP4
VCC2
10 V @ 300 mA
C9
1 uF
R8
115 K
R9
16.2 K
C11
2.2 uF
U17 added due to inavailability of TPS7A25.
Install only one supply for VCC2
U17
EN
VIN
GND
GND_TAB
VOUT
FB
TPS73801
C93
10 uF
R55
3.4 K
R56
464
C94
10 uF
10 V @ 1000 mA
V_OFFSET
R12
33
U3
N-FET
2N7002LT1G
R13
100 K
R14
100 K
NC = 1
U4
SN74AUP1G07DCKR
R11
2.2 K
12V UVLO MONITOR
U5
VCC
/RESET
GND
/MR
NC
TPS3847085
R15
2.2 K
U14
SN74AUP1G04
U6
N-FET
DMN2005K-7
R10
10
P2V3
TP40
VR_PG_NEG
R42
100 K
R43
100 K
VR_PG
D5
RB501SM-30FHT2R
U7
SCPCK
SCPDI
SCPDO
SCPENZ
IRQZ
STROBE
MODE1
MODE0
SEL1
SEL0
A3
A2
A1
A0
OEZ
DEV_ID1
DEV_ID0
RESETZ
NC_54
NC_52
NC_51
12V
12V
12V
VBIAS_RAIL
VBIAS_SWL
VBIAS_LHI
VBIAS
DLPA300
SCP_CLK
SCP_MOSI
SCP_MISO
SCP_DAD_CSZ
DAD_IRQZ
DAD_STRB
DAD_MODE_1
DAD_MODE_0
DAD_SEL_1
DAD_SEL_0
DAD_ADDR_3
DAD_ADDR_2
DAD_ADDR_1
DAD_ADDR_0
DAD_OEZ
DMD_RSTZ
R16
33
R17
0
R18
0
R19
0
C17
0.1 uF
C18
10 uF
TP26
C25
0.1 uF
C26
0.1 uF
C27
1 uF
C28
1 uF
D3
STPS0560Z
L6
22 uH
C29
10 uF
C30
0.1 uF
MBRST[15:0]
OUT15
OUT14
OUT13
OUT12
OUT11
OUT10
OUT9
OUT8
OUT7
OUT6
OUT5
OUT4
OUT3
OUT2
OUT1
OUT0
MBRST15
MBRST14
MBRST13
MBRST12
MBRST11
MBRST10
MBRST9
MBRST8
MBRST7
MBRST6
MBRST5
MBRST4
MBRST3
MBRST2
MBRST1
MBRST0
TP7
TP8
TP9
TP10
TP11
TP13
TP14
TP15
TP16
TP17
TP18
TP19
TP20
TP21
TP22
TP23
TP12
TP24
V_OFFSET
VOFFSET
VOFFSET_RAIL
11V Protection Zener Diode
V_offset = 10V
C13
0.1 uF
C14
0.1 uF
C15
0.1 uF
C16
4.7 uF
3SMBJ5926B-TP
D1
V5REG
C19
1 uF
C20
0.1 uF
PAD
VRESET_RAIL
VRESET
TP25
TP27
R20
0
VRESET_EXT
DNI
R41
0
VRESET_DAD
D2
STPS0560Z
C21
0.1 uF
C22
0.1 uF
C23
0.1 uF
C24
4.7 uF
VRESET_SWL
L5
22 uH
GND
P12V
U13
LM43601
VIN
CBOOT
SW
FB
BIAS
PGOOD
SS/TRK
EN
RT
VCC
SYNC
AGND
PGND
PAD
VRESET_EN
C79
0.47 uF
L7
47 uH
C80
10 uF
C81
10 uF
C82
10 uF
C83
10 uF
C84
10 uF
C85
10 uF
C86
22 uF
C87
22 uF
C88
4.7 uF
C89
180 pF
R44
100 K
D6
RB501SM-30FHT2R
R45
80.6 K
C90
2.2 uF
R46
0
VR_PG_NEG
R47
6.49 K
C91
0.01 uF
C92
4.7 uF
TP41
R48
1.6 K
D7
3SMBJ5926B-TP
R49
0
R50
100 K
U15
BSS84-7-F
U16
N-FET
2N7002/HAMR
R51
0
R52
100 K
R53
0
R54
0
1-G
2-S
3-D
POWER
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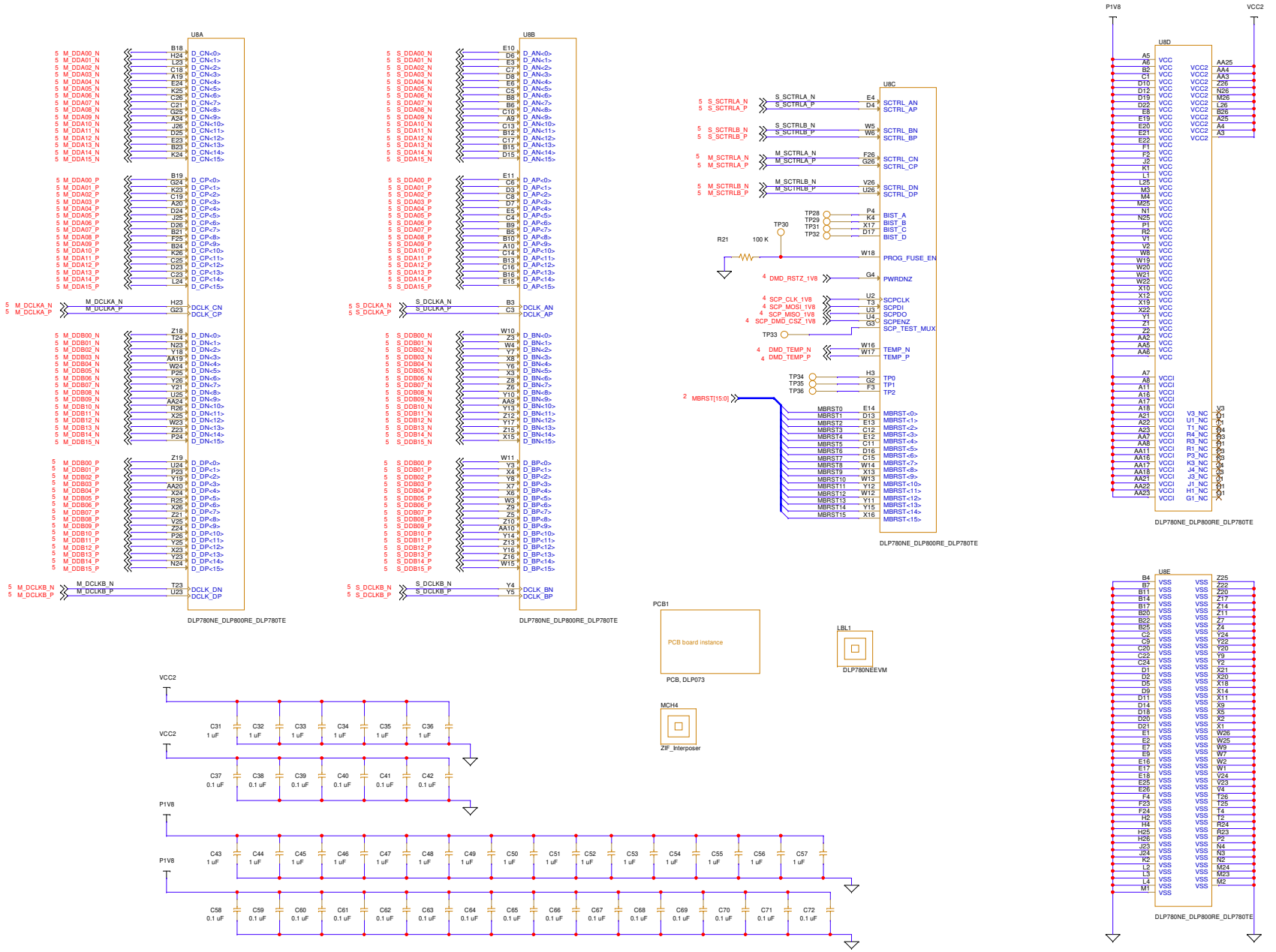

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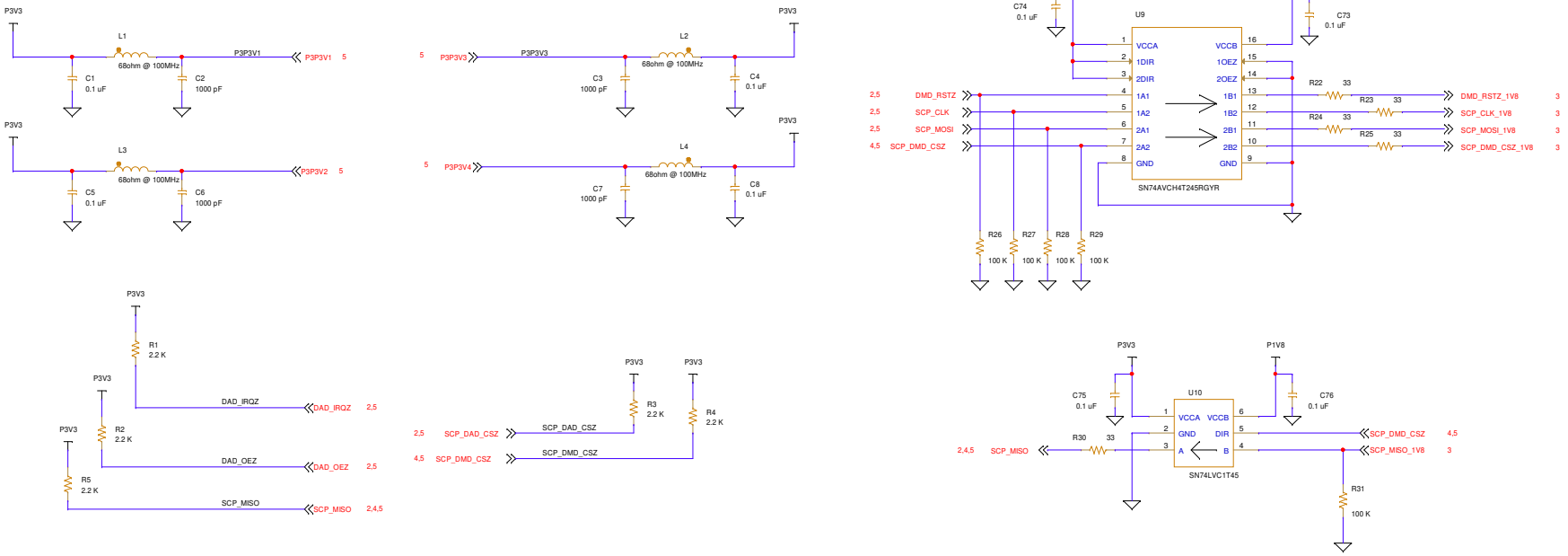
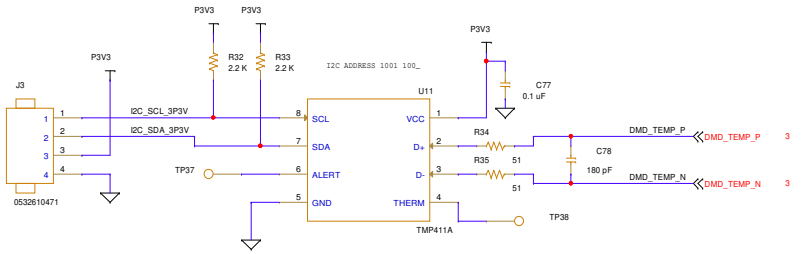


3.3V I2C Connector



DMD

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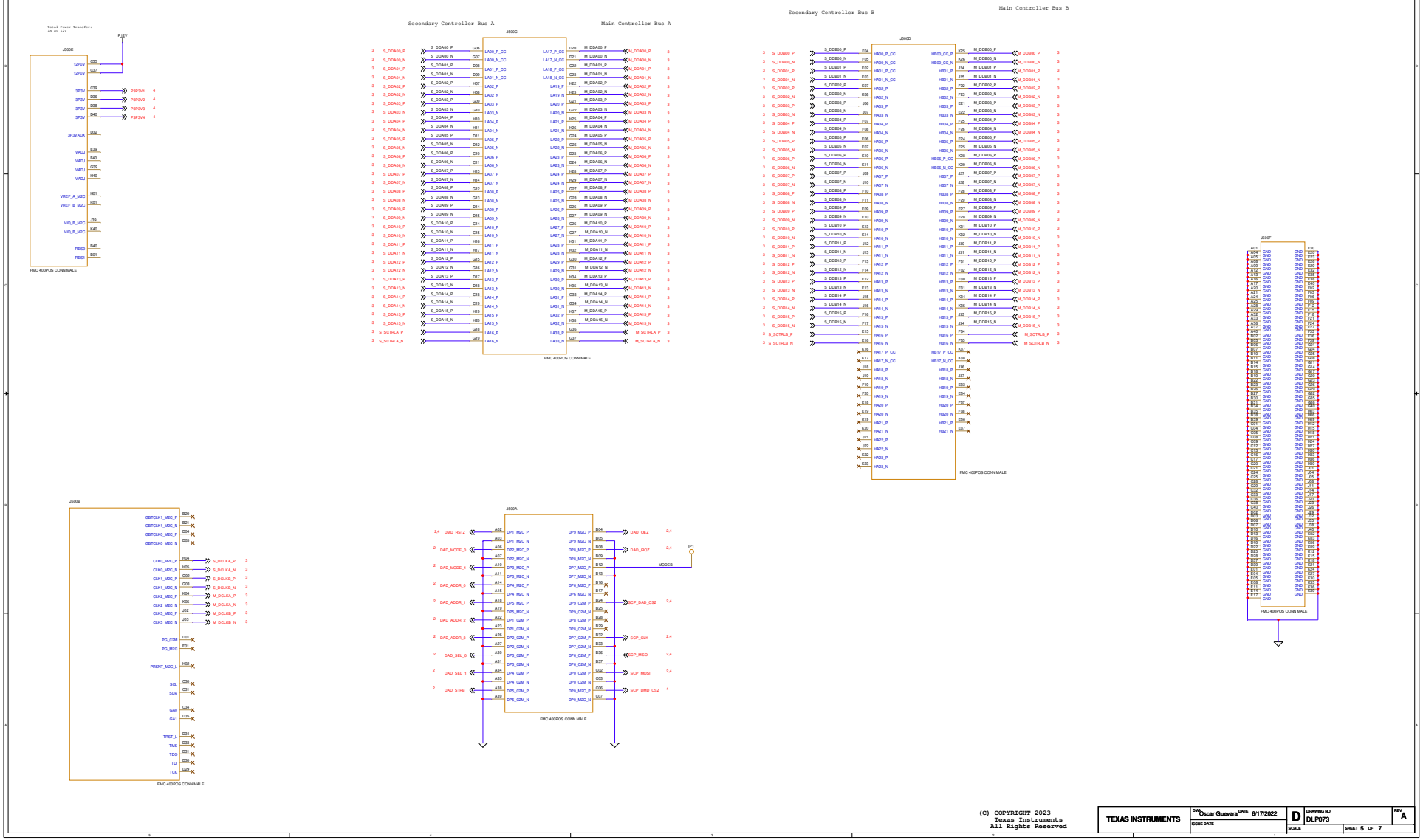

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REV A

REVISIONS:

REV A: INITIAL ENGINEERING RELEASE

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