

ADC08B3000RB – 8 bit Giga-Sample Signal Acquisition Reference Board

1.0 Introduction

The ADC08B3000RB is an ultra high-speed signal acquisition system designed around National's ADC08B3000 analog-to-digital converter. This 8-bit ADC operates at sample rates up to 3.0 Gs/s. National provides this Design Package to customers who wish to use the reference board design as a starting point for a portion of their own product design.

The design package is provided under the licensing agreement in this document (which the customer agrees to by accepting this package) with certain stipulations:

- * that the design package is provided as-is without any warranties concerning its operation as it is designed to be a design tool and not a product;
- * that the customer will not directly productize this design and will only use it as an assistance towards the design of a different product of his own;
- * that while National provides full application support to our customers for the purposes of designing-in our IC products, this design package is provided free of charge as a courtesy and system level design aspects of this package are not supported.

2.0 Contents of the package

1. Reference Board Users' Guide.
2. Board schematics and bill-of-materials (BOM) for ADC08B3000RB.
3. Board layout design.
4. FPGA design source files (necessary source files).

Customer will need to provide the tools required to work with each of these items. These are:

1. PC with Windows XP environment.
2. Allegro or OrCAD design package for schematic entry.
3. Allegro board design package.
4. Xilinx ISE (version 8.1 was used for this design).

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