

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

A

B

C

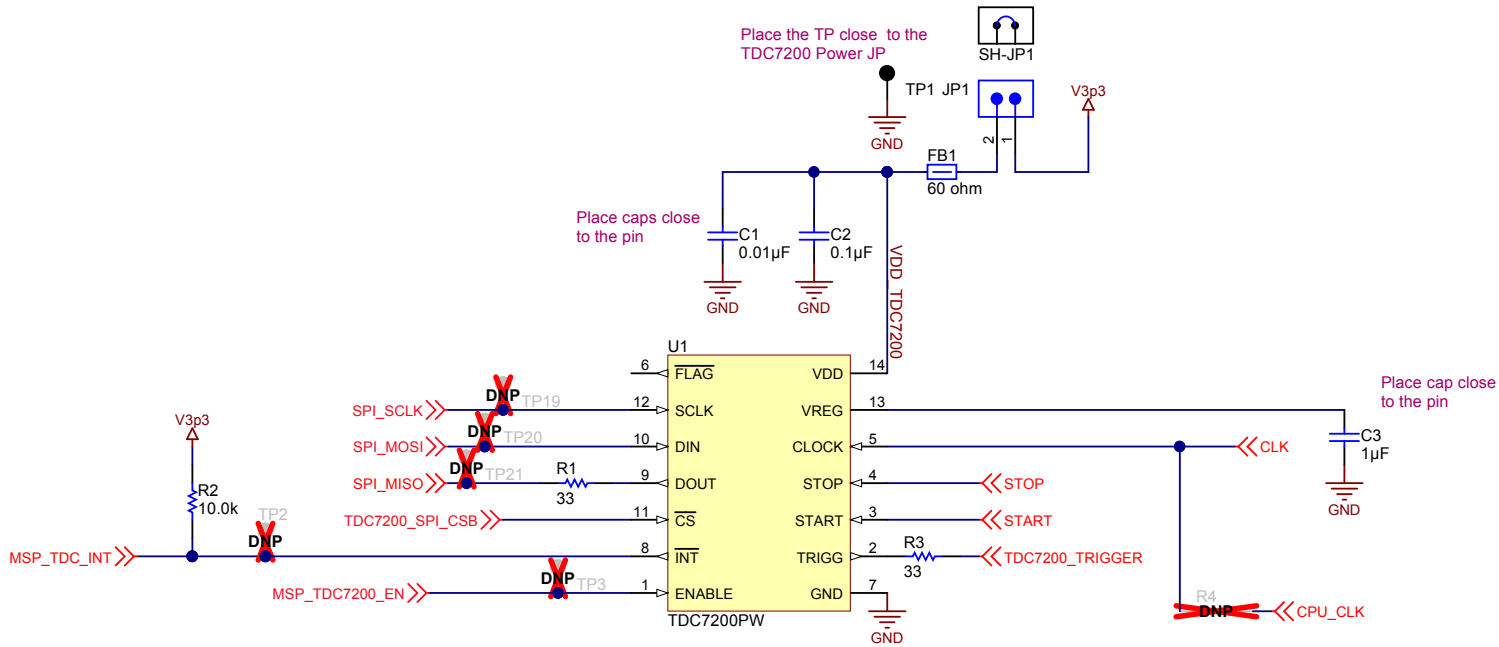
D

A

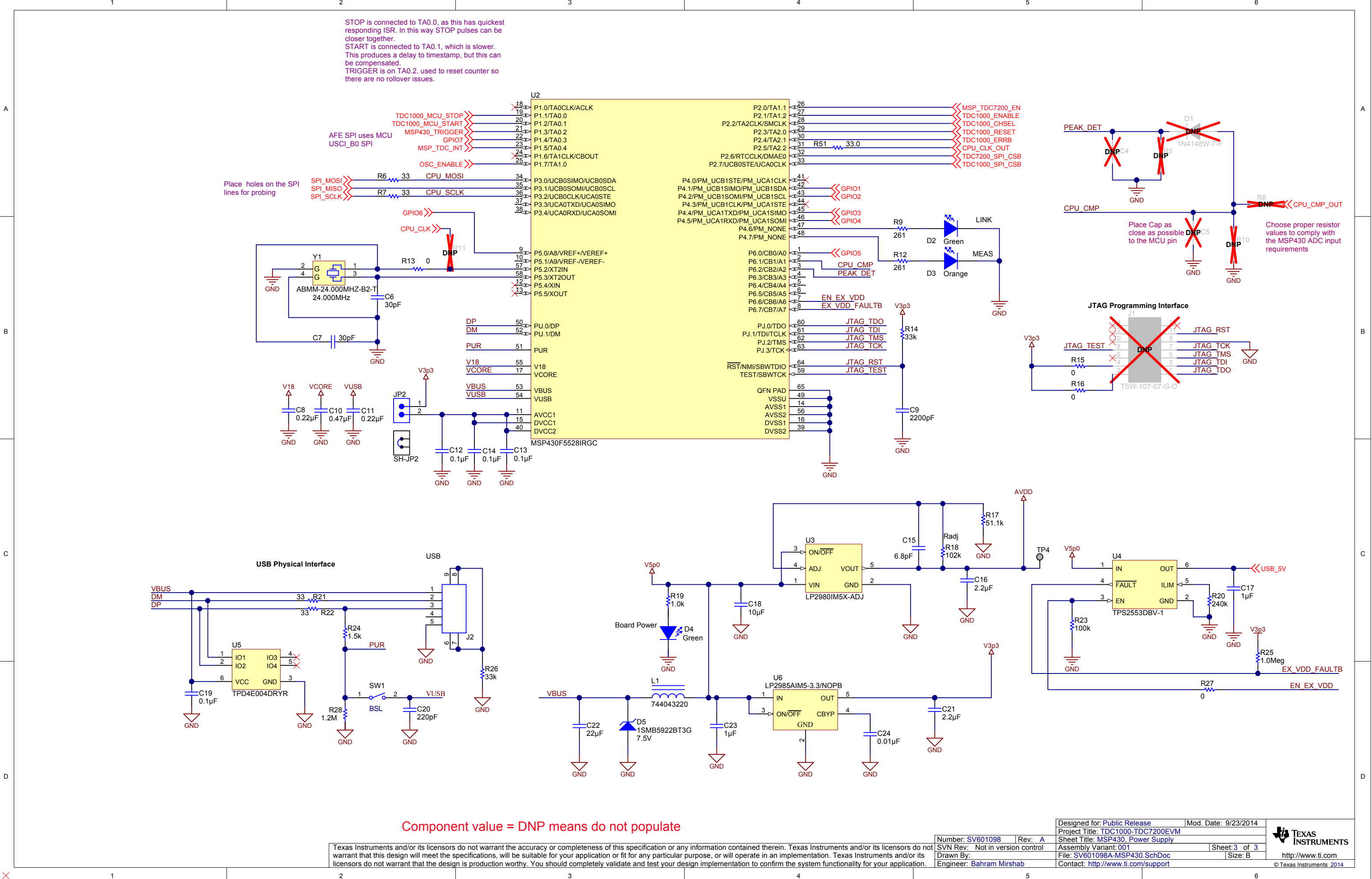
B

C

D



Component value = DNP means do not populate



STOP is connected to TA0.0, as this has quickest responding ISR. In this way STOP pulses can be closer together.
START is connected to TA0.1, which is slower. This produces a delay to timestamp, but this can be compensated.
TRIGGER is on TA0.2, used to reset counter so there are no rollover issues.

Place holes on the SPI lines for probing

Place Cap as close as possible to the MCU pin

Choose proper resistor values to comply with the MSP430 ADC input requirements

Component value = DNP means do not populate

