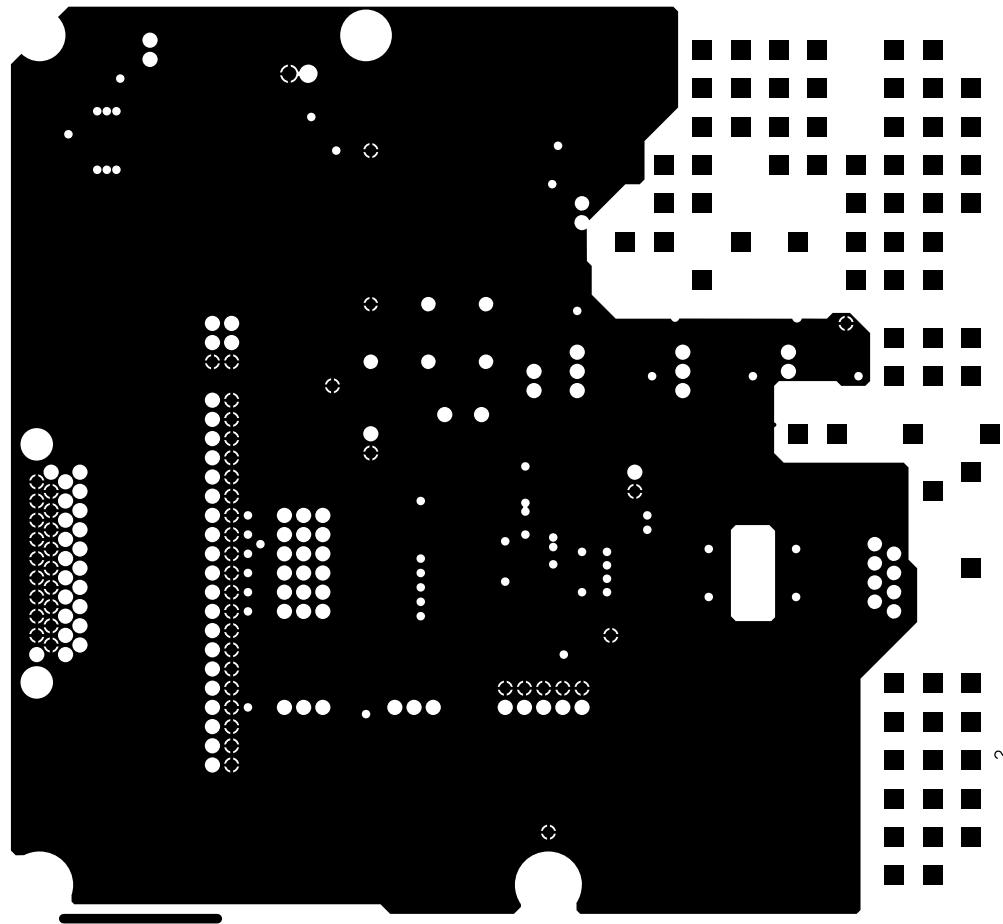


1



National Semiconductor™
COMMUNICATIONS INTERFACE

ALL ARTWORK VIEWED FROM TOP SIDE	DATE:	FEBRUARY 8, 2005
BOARD NAME: DP83848 ASPENPHY DEMO II	BOARD NO:	880012505-001 REV A
LAYER NAME: TOP ETCH	LAYER NO:	1 SHEET 1 of 8

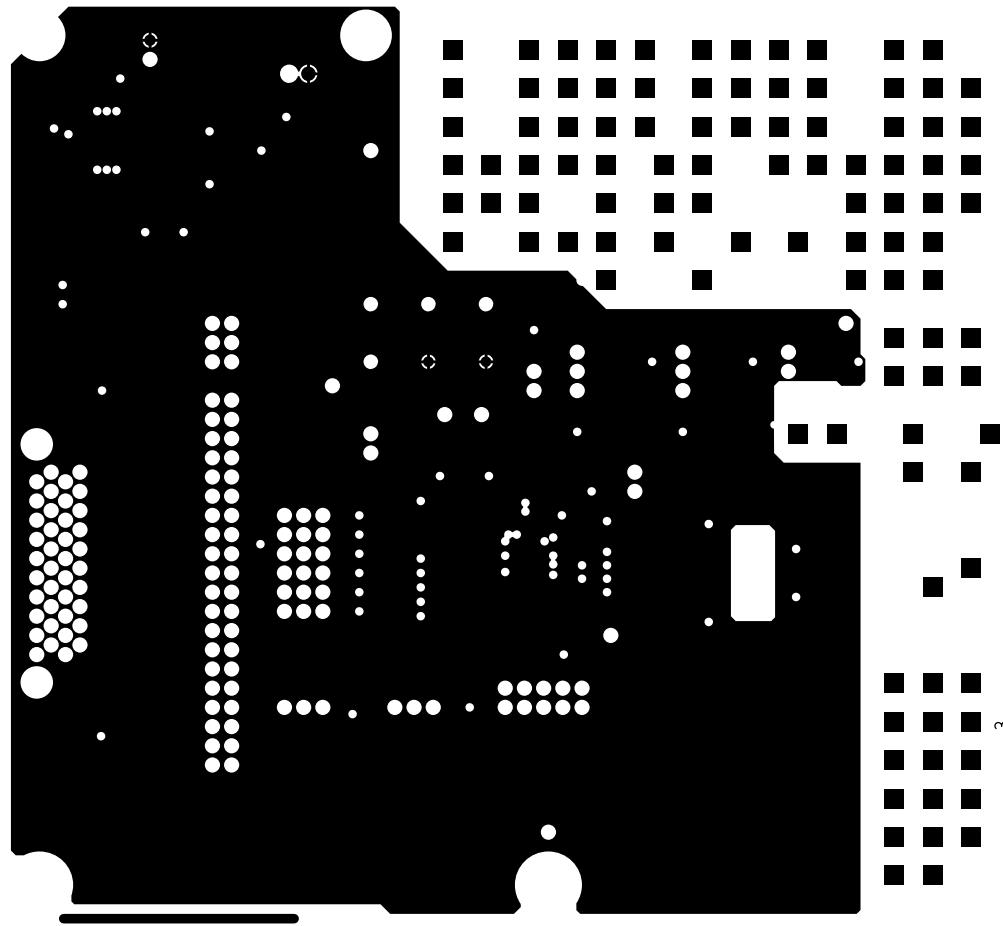


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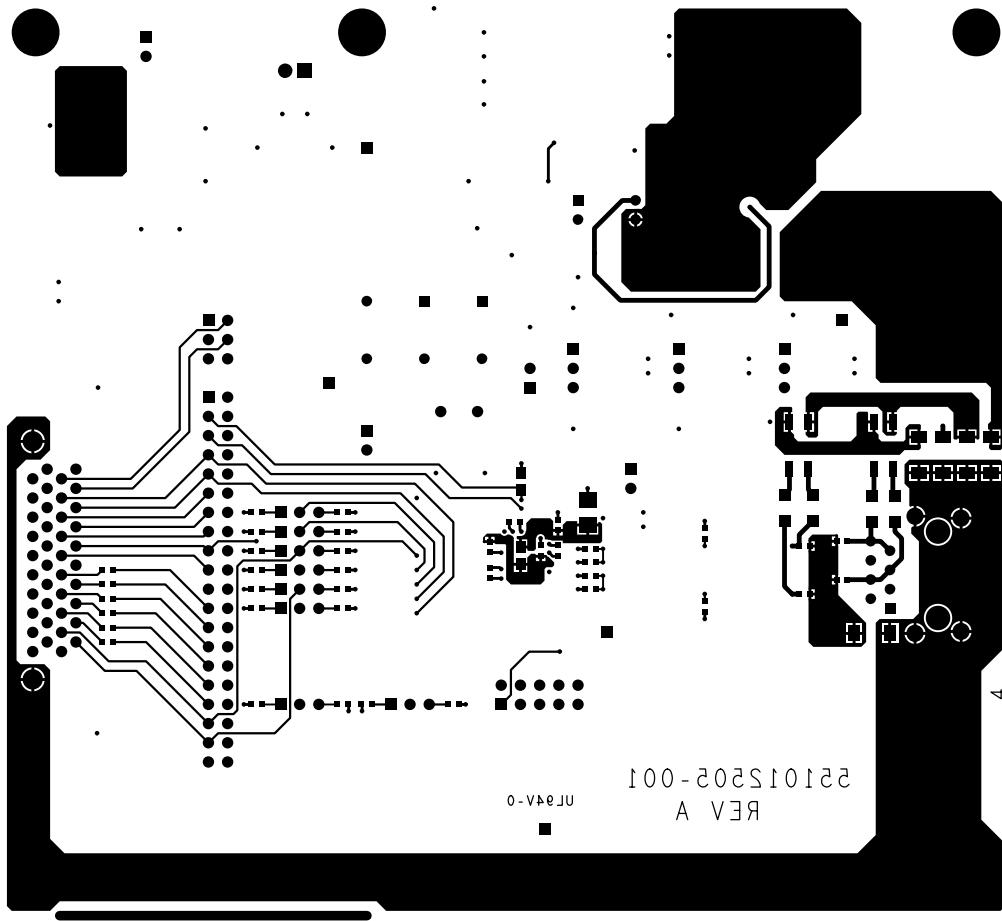
National Semiconductor™
COMMUNICATIONS INTERFACE

ALL ARTWORK VIEWED FROM TOP SIDE	DATE:	FEBRUARY 8, 2005
BOARD NAME: DP83848 ASPENPHY DEMO II	BOARD NO:	880012505-001 REV A
LAYER NAME: GND-1	LAYER NO:	2 SHEET 2 of 8

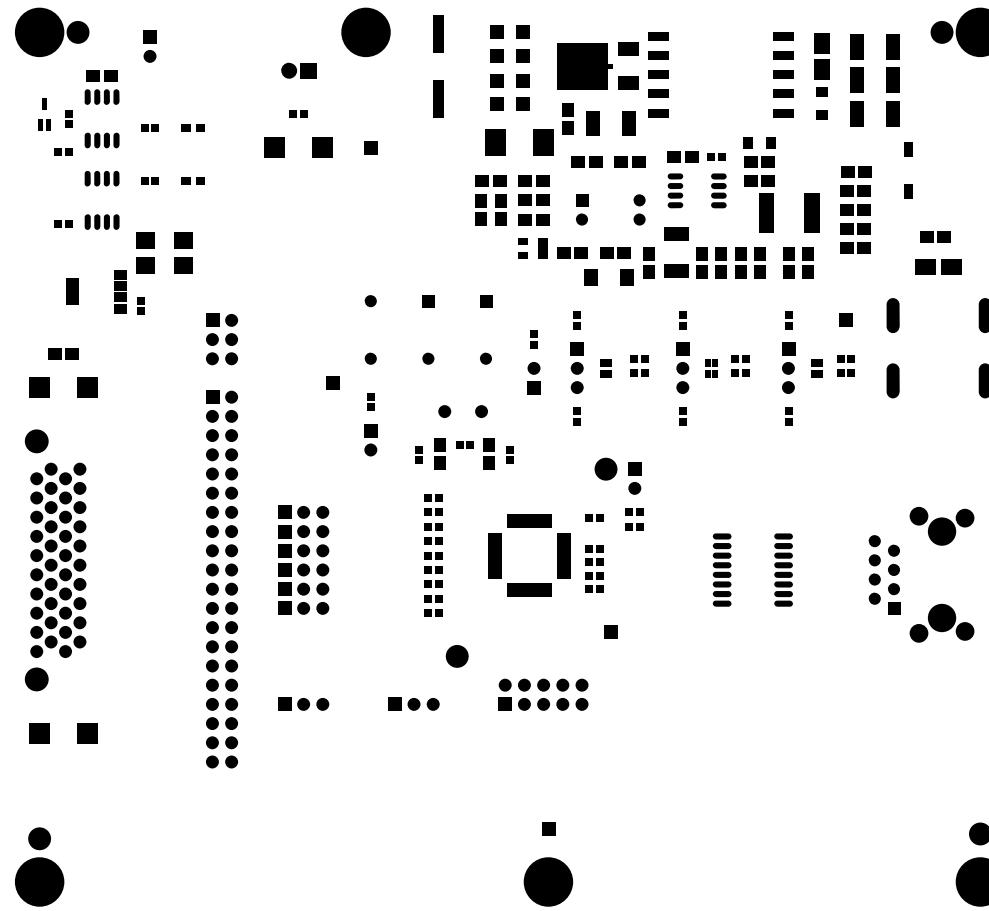


3

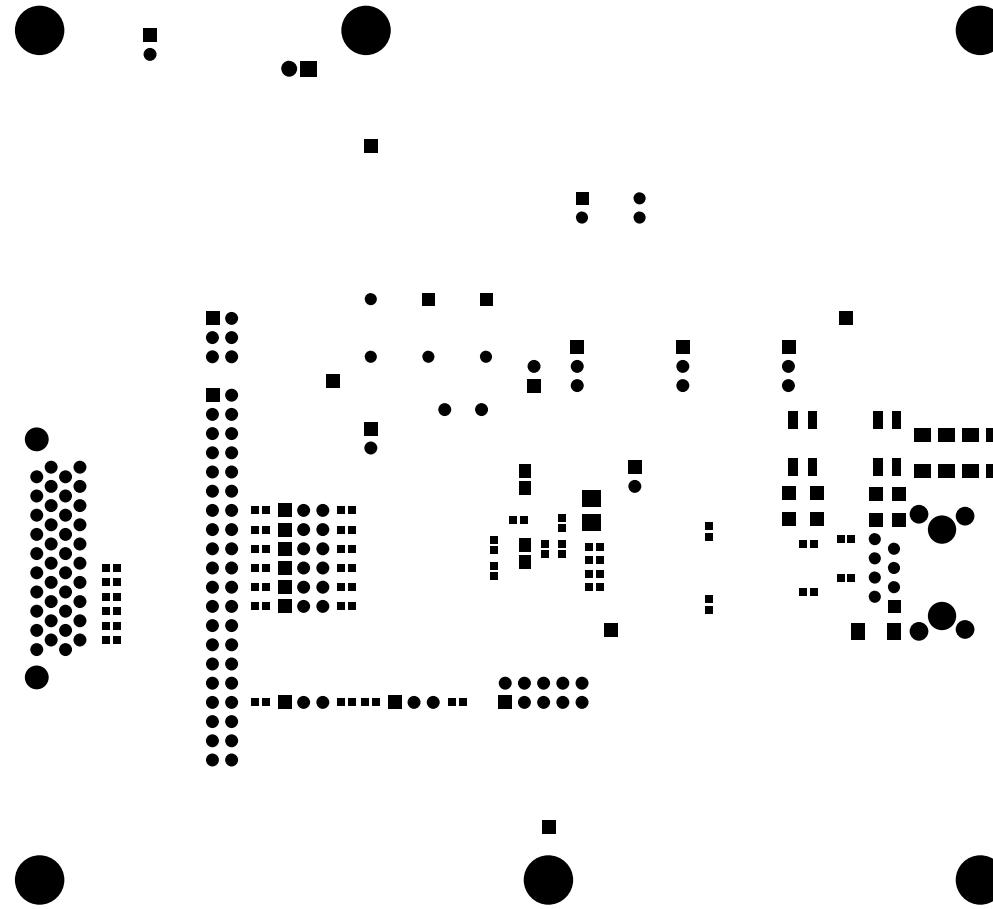
 National Semiconductor™ COMMUNICATIONS INTERFACE	ALL ARTWORK VIEWED FROM TOP SIDE	DATE: FEBRUARY 8, 2005
	BOARD NAME: DP83848 ASPENPHY DEMO II	BOARD NO: 880012505-001 REV A
	LAYER NAME: 3V3	LAYER NO: 3 SHEET 3 of 8



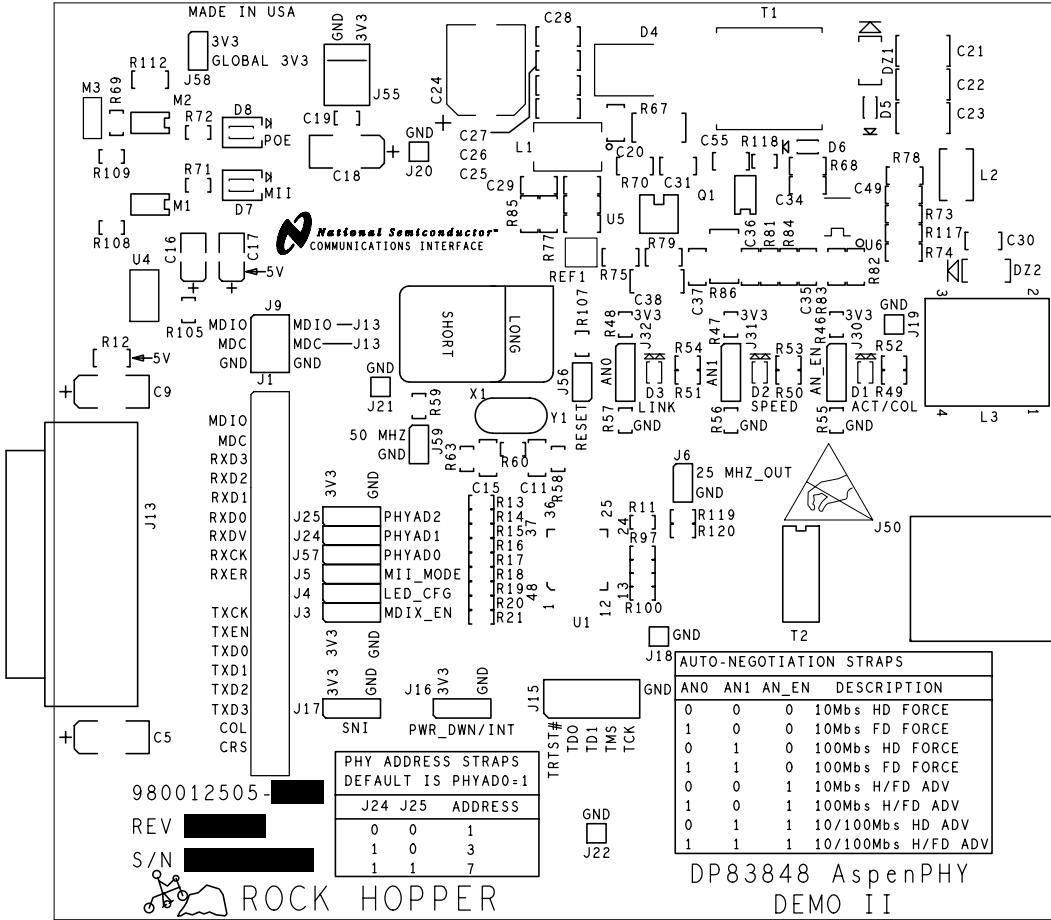
 National Semiconductor™ COMMUNICATIONS INTERFACE	ALL ARTWORK VIEWED FROM TOP SIDE	DATE:	FEBRUARY 8, 2005
	BOARD NAME:DP83848 ASPENPHY DEMO II	BOARD NO:	880012505-001 REV A
	LAYER NAME:BOTTOM ETCH	LAYER NO:	4 SHEET 4 of 8



 National Semiconductor™ COMMUNICATIONS INTERFACE	ALL ARTWORK VIEWED FROM TOP SIDE	DATE:	FEBRUARY 8, 2005
	BOARD NAME:DP83848 ASPENPHY DEMO II	BOARD NO:	880012505-001 REV A
	LAYER NAME:TOP SOLDER MASK	LAYER NO:	SHEET 5 of 8



 National Semiconductor™ COMMUNICATIONS INTERFACE	ALL ARTWORK VIEWED FROM TOP SIDE	DATE:	FEBRUARY 8, 2005
	BOARD NAME:DP83848 ASPENPHY DEMO II	BOARD NO:	880012505-001 REV A
	LAYER NAME:BOTTOM SOLDER MASK	LAYER NO:	SHEET 6 of 8



National Semiconductor™
COMMUNICATIONS INTERFACE

ALL ARTWORK VIEWED FROM TOP SIDE

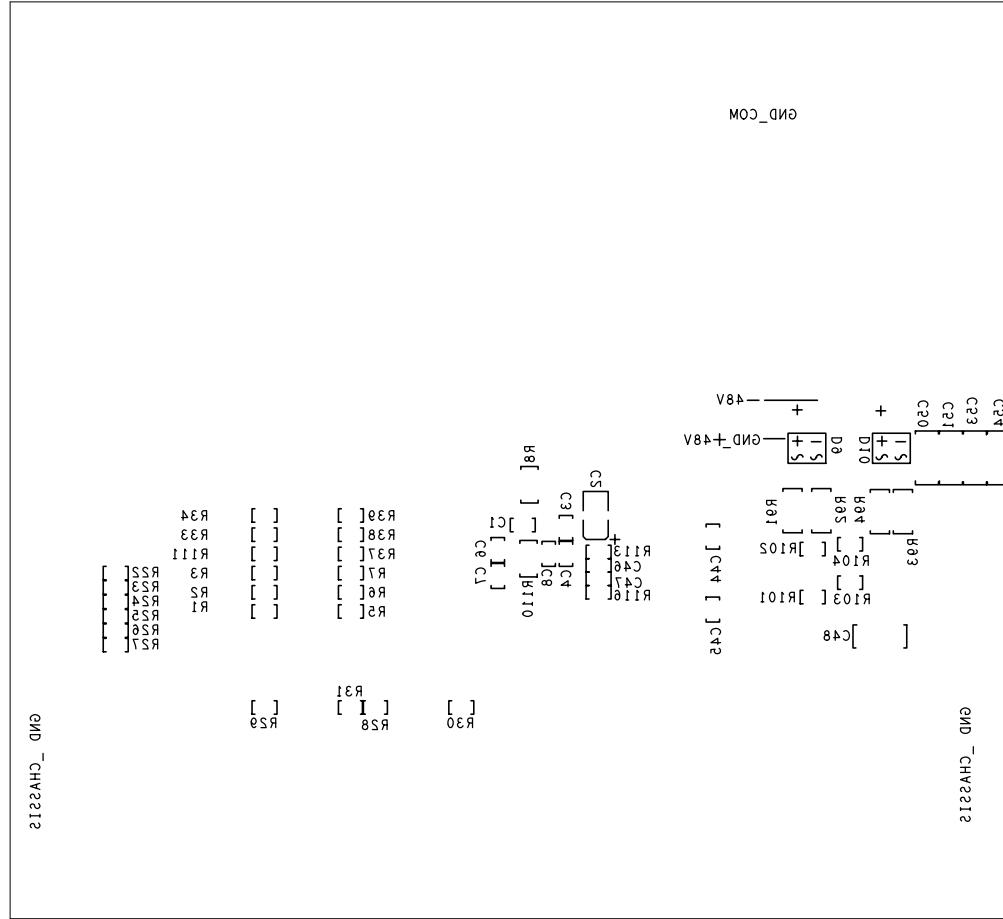
DATE: FEBRUARY 8, 2005

BOARD NAME:DP83848 ASPENPHY DEMO II

BOARD NO: 880012505-001 REV A

LAYER NAME:TOP SILKSHEET

LAYER NO: SHEET 7 of 8



National Semiconductor
COMMUNICATIONS INTERFACE

ALL ARTWORK VIEWED FROM TOP SIDE	DATE:	FEBRUARY 8, 2005	
BOARD NAME:DP83848 ASPENPHY DEMO II	BOARD NO:	880012505-001	REV A
LAYER NAME:BOTTOM SILKSCREEN	LAYER NO:	SHEET 8 of 8	

ncdrill.dat (Board layer 17) Scale=1.00 Tue Sep 13 10:15:20 2005

