Layer Stack Up Detail for: SV601116A.PcbDoc

Layer Name	Gerber Document	Copper Thickness	Dielectric Height	Dielectric Material	Dielectric Constant	Dielectric Type
Top Solder Mask	(.GTS)		0.4mil	Solder Resist	3.50	
Top Layer	(.GTL)	1.4mil				
			59.2mil	FR-4 High Tg	4.80	Core
Bottom Layer	(.GBL)	1.4mil				
Bottom Solder Mask	(.GBS)		0.4mil	Solder Resist	3.50	

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)
600mil X 3095mil

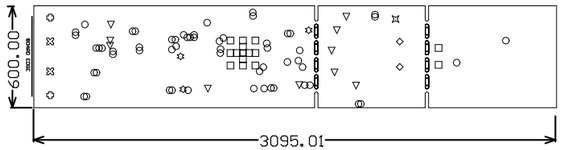
Number of Layers : 2
MIN. TRACK WIDTH: 4 MIL
MIN. CLEARANCE: 8 MIL
MIN. VIA PAD SIZE: 12 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
PER IPC-D-275 CLASS 2 LEVEL C
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

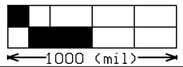
MATERIAL:
[ ] FR-4 [X] FR-4 High Tg [ ] OTHER
THICKNESS: [X] 62 MIL (0.787mm) +/-10% [ ] OTHER
TOLERANCE: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
BOW & TWIST: [X] ANSI IPC-6012 TYPE 3 CLASS 2
[ ] OTHER +/-
COPPER THICKNESS (FINISHED):
OUTER: [X] 1.4MIL (1oz) [ ] 2MIL (1.4oz) [ ] 2.8MIL (2oz)
INNER SIGNAL: [X] 1.4MIL (1oz) [ ] 2.8MIL (2oz)
DRILLING:
REFERENCE: [X] AS SHOWN [X] NC_DRILL FILES
PTH MIN COPPER THICKNESS: [X] 1MIL [ ] OTHER
BOARD FINISH:
SILKSCREEN: [X] TOP [X] BOTTOM
SILKSCREEN COLOR: [X] WHITE [ ] OTHER
SOLDER RESIST COLOR:
[X] GREEN [ ] BLUE [ ] OTHER
SURFACE FINISH: [X] IMMERSION GOLD (ENIG) [ ] Pb-FREE HASL
[ ] OTHER
ARRAY/PANEL: [ ] CUT AND TRIM PER MECH LAYER 1
[ ] N.C. ROUTE [X] V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBS TO MEET OR EXCEED THE REQUIREMENTS OF:
[X] ANSI IPC-A-600F CLASS -> [ ] 1 [X] 2 [ ] 3
[X] UL 94V-0 [X] RoHS [ ] OTHER PER ORDER
ADDITIONAL REQUIREMENTS:
MICROSECTION: [ ] YES
BARE BOARD ELEC. TEST: [ ] NONE [X] REQUIRED [ ] PER ORDER
MANUFACTURER'S ID/LOGO: [ ] RAIL [ ] METAL [X] SILK

Symbol	Hit Count	Tool Size	Physical Length	Rout Path Length	Plated	Hole Type
○	57	6mil (0.152mm)			PTH	Round
□	13	7.874mil (0.2mm)			PTH	Round
☼	3	8mil (0.203mm)			PTH	Round
▽	10	10mil (0.254mm)			PTH	Round
⌑	1	30mil (0.762mm)			PTH	Round
□	2	40mil (1.016mm)			PTH	Round
◇	2	43.307mil (1.1mm)			PTH	Round
⊠	2	43.307mil (1.1mm)			NPTH	Round
⌬	2	30mil (0.762mm)	98.425mil (2.5mm)	68.425mil (1.738mm)	PTH	Slot
	92 Total					

Slot definitions : **Rout Path Length** = Calculated from tool start centre position to tool end centre position.
Physical Length = Rout Path Length + Tool Size = Slot length as defined in the PCB layout

Drill Table





TEXAS INSTRUMENTS

PROJECT TITLE: LDC1000QEVM
DESIGNED FOR: Public Release
FILE NAME: SV601116A.PcbDoc

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #:SV601116	REV: A	SVN REV: Not In VersionControl
LAYER NAME = [illegible]ions			
PLOT NAME = Fabrication Drawing	GENERATED : 8/5/2014 5:36:29 PM	TEXAS INSTRUMENTS	

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ENGINEER: Murali Srinivas	LAYOUT BY: Krypton-Solutions
SCALE: 0.70	ALTIUM DESIGNER VERSION: 10.0.0.27009