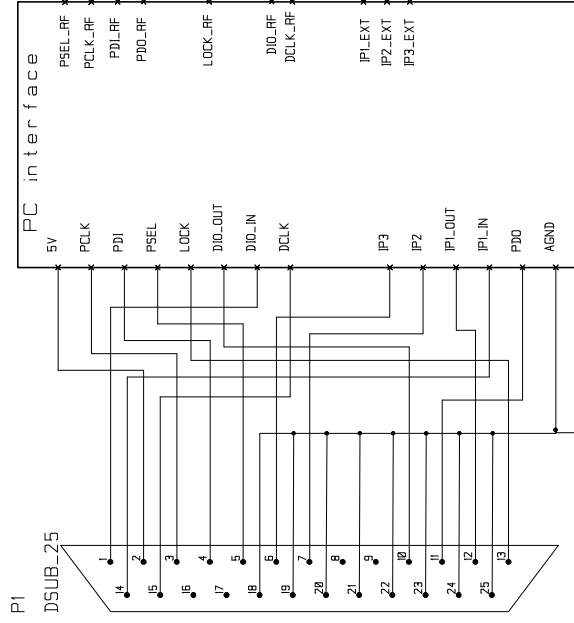
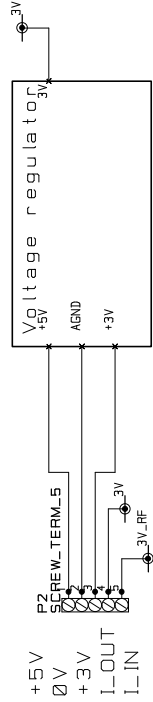
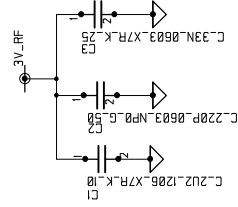


Note: The +5V input is a 4-10V un-regulated supply voltage input

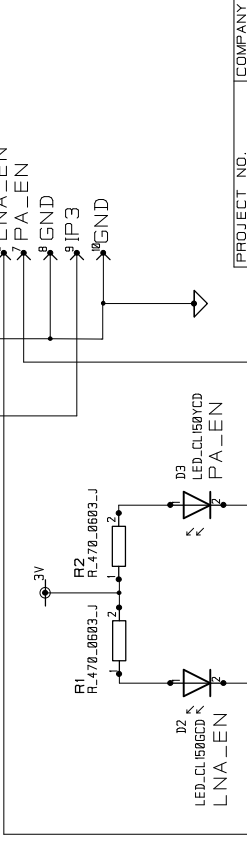
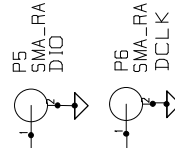
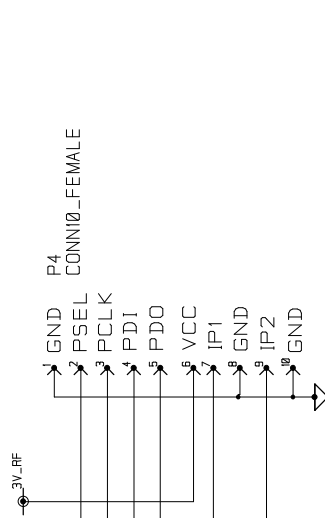
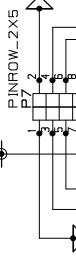


Global power decoupling



Testpins for digital signals

- 1-GND 2-GND 3-PSEL 4-PCLK 5-DCLK 6-PDI 7-DIO 8-PDI0 9-VCC 10-LOCK



PROJECT NO.	02535	COMPANY NAME	CHIPCON AS
APPROVALS		DWG	
DRAWN	TAL	CC1020	Evaluation Board
CHECKED	PME	SIZE	A3
ISSUED		SCALE	
		DWG NO.	2.2
		REV.	2.2
		SHEET	1 (3)