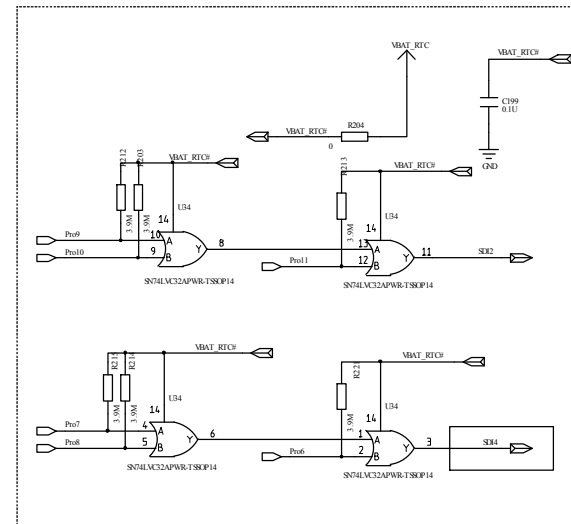
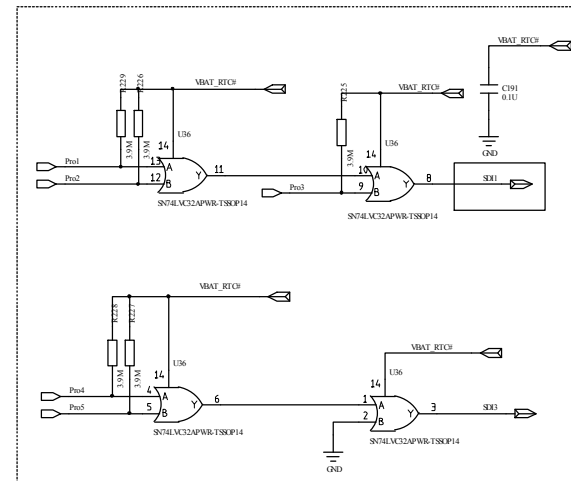
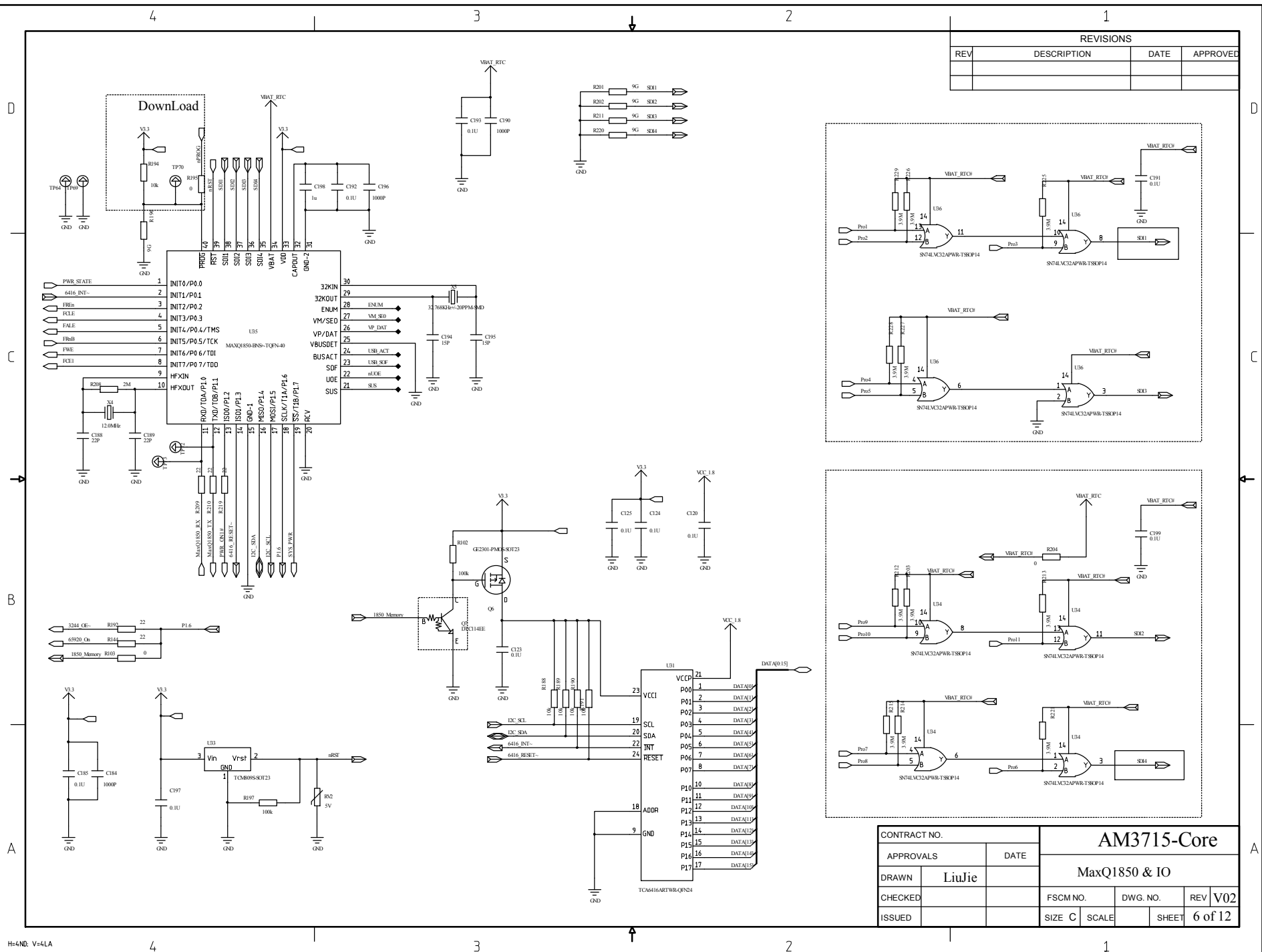


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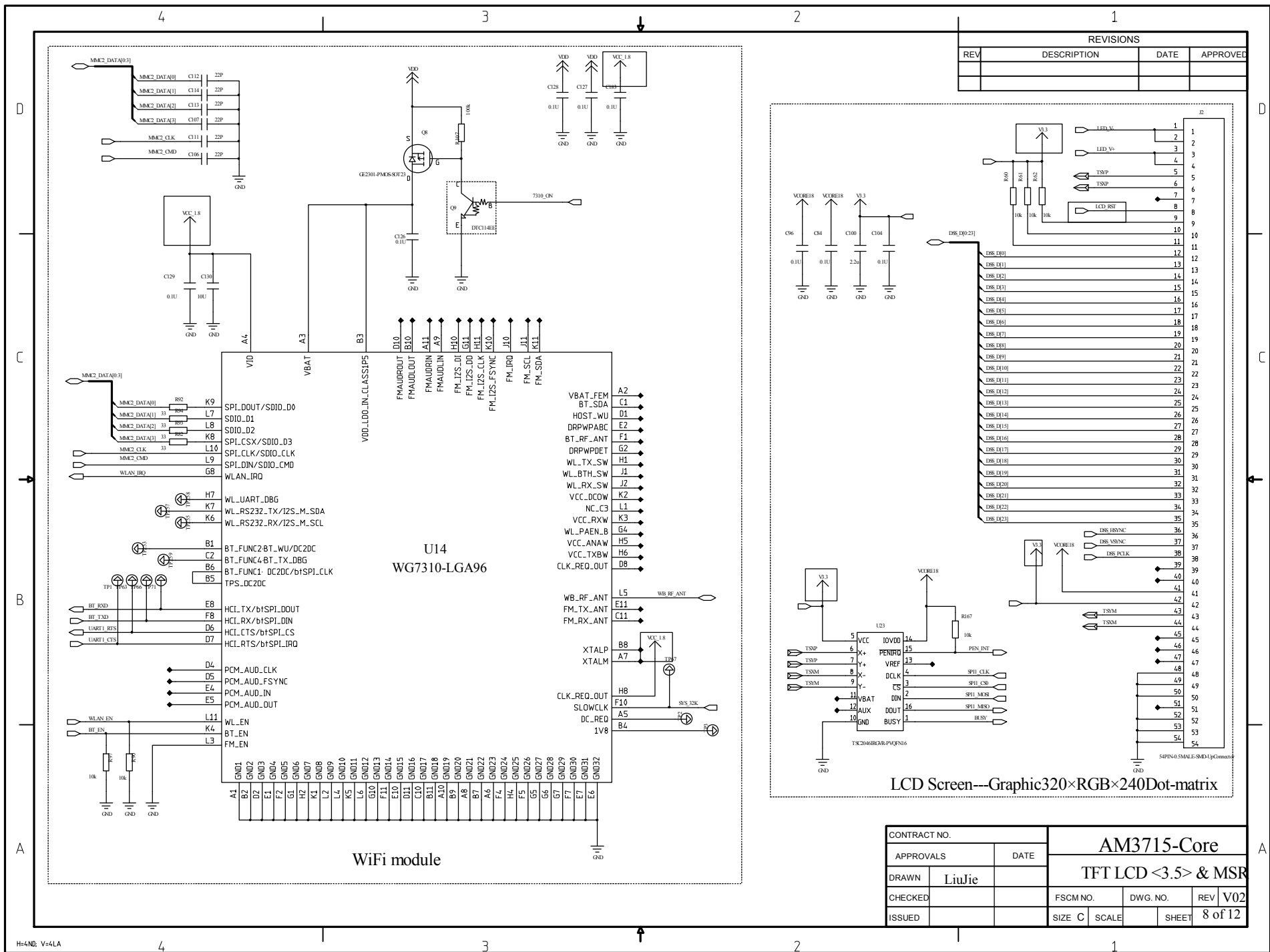
CONTRACT NO.			<div style="text-align: center;"> <h1>AM3715-Core</h1> <h2>GPIO Level Shift</h2> </div>				
APPROVALS		DATE					
DRAWN	LiuJie		FSCM NO.		DWG. NO.	REV	V02
CHECKED			SIZE C		SCALE	SHEET	5 of 12
ISSUED							

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

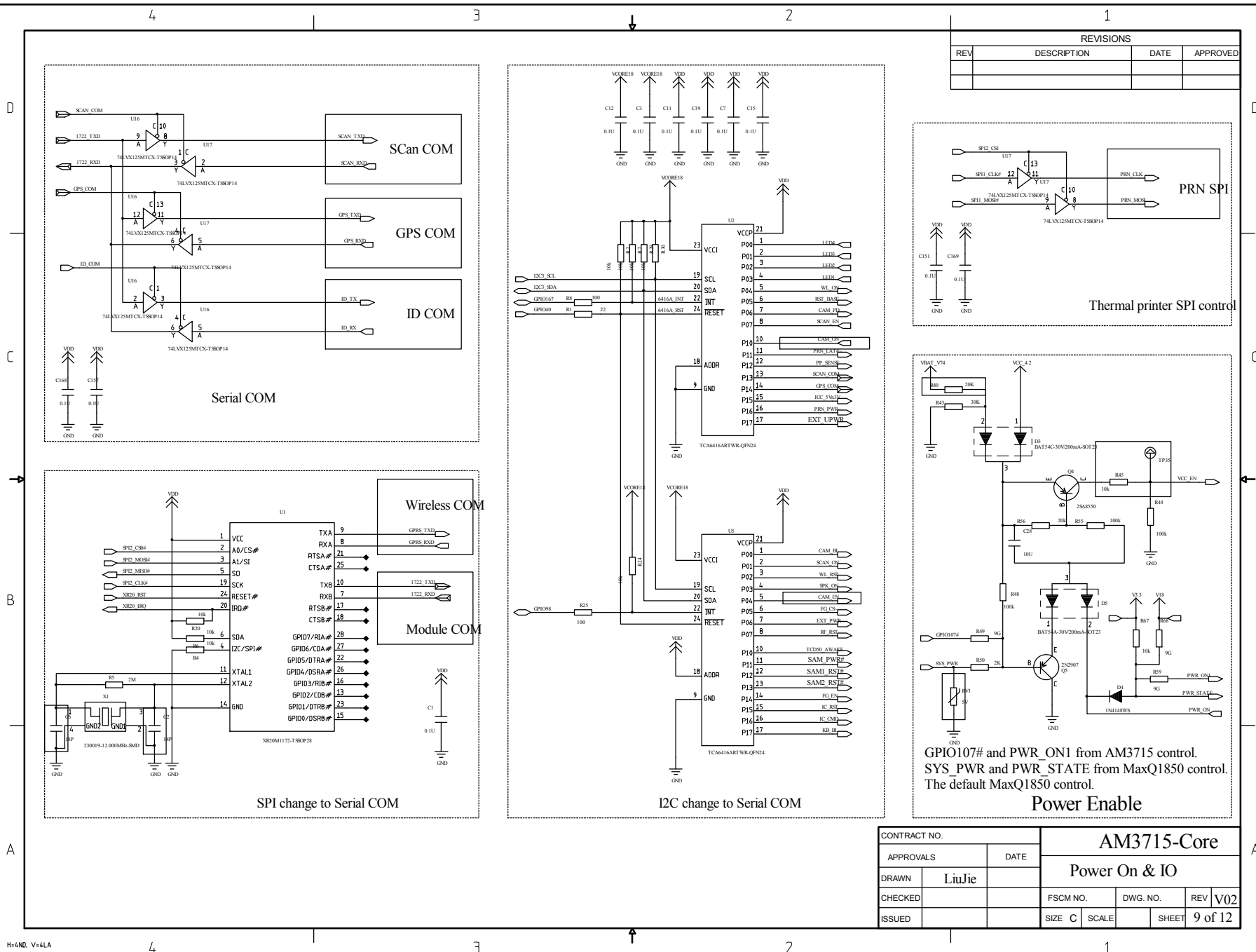


CONTRACT NO.		AM3715-Core			
APPROVALS		DATE		MaxQ1850 & IO	
DRAWN	LiuJie				
CHECKED		FSCM NO.		DWG. NO.	REV V02
ISSUED		SIZE C		SCALE	SHEET 6 of 12





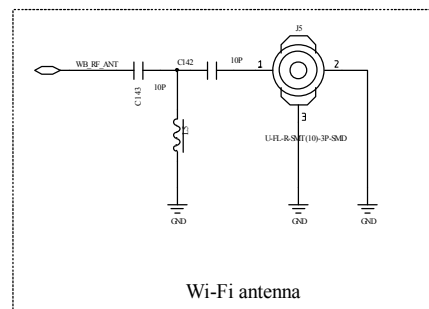




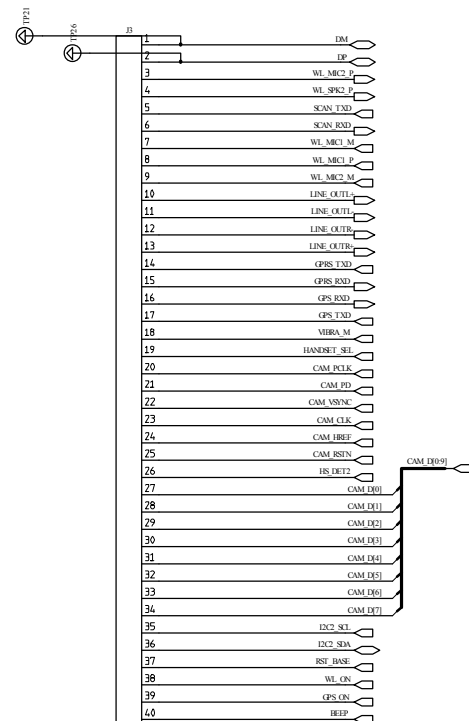
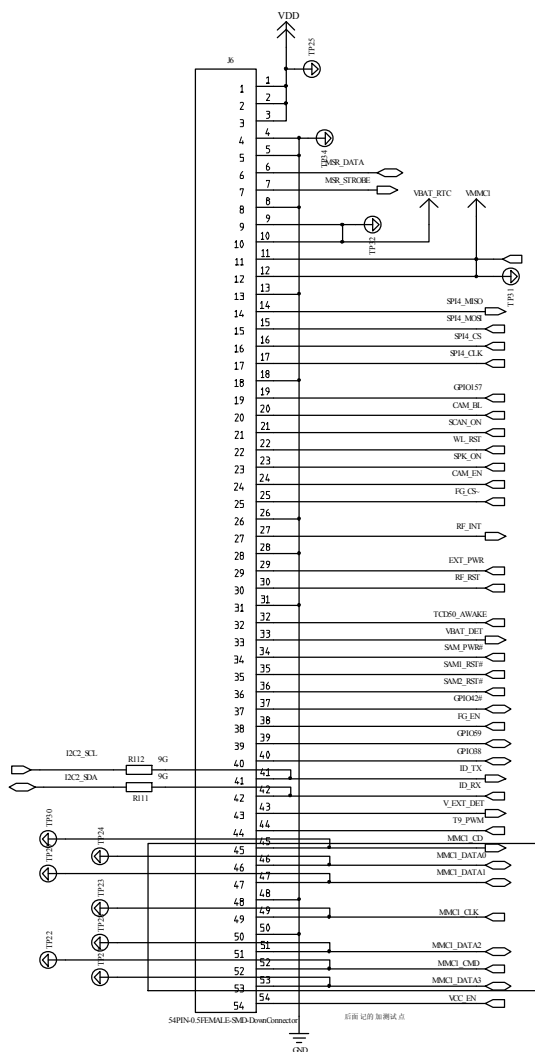




REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



PS.  
VCC=5.0V  
VDD=3.3V  
V3.3=3.3V  
VCC\_1.8=1.8V  
VCORE18=1.8V  
VDD1-CORE=1.2V  
VDD2-CORE=1.2V  
VCC\_4.2=4.2V  
VBAT\_RTC=3.0V  
VPP=4.2V



CONTRACT NO.		VPOS353-Core	
APPROVALS		DATE	
DRAWN		Connectors	
CHECKED		FSCM NO.	DWG. NO.
ISSUED		SIZE C	SCALE
		SHEET	12 of 12