

Layer Name	Order Document	Copper Thickness	Dielectric Material
Top Solder Mask	(.GTS)		Solder Resist
Top Layer	(.GTL)	2.8mil	FR-4
MidLayer1	(.G1)	2.8mil	FR-4
MidLayer2	(.G2)	2.8mil	FR-4
Bottom Layer	(.GBL)	2.8mil	FR-4
Bottom Solder Mask	(.GBS)		Solder Resist

**DESIGN INFORMATION**

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)  
3444MIL X 1140MIL

Number of Layers : 4  
 MIN. TRACK WIDTH: 10 MIL  
 MIN. CLEARANCE: 8 MIL  
 MIN. VIA PAD SIZE: 34 MIL

MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL  
 PER IPC-D-275 CLASS 2 LEVEL C  
 REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:  
 FR-408  FR-4 High Tg  OTHER \_\_\_\_\_

THICKNESS:  62 MIL (1.6mm) +/-10%  OTHER 31MILS +/-10%

TOLERANCE:  ANSI IPC-6012 TYPE 3 CLASS 2  
 OTHER +/- \_\_\_\_\_

BOW & TWIST:  ANSI IPC-6012 TYPE 3 CLASS 2  
 OTHER +/- \_\_\_\_\_

COPPER THICKNESS (FINISHED):  
 OUTER:  1.4MIL (1oz)  2MIL (1.4oz)  2.8MIL (2oz)  
 INNER SIGNAL:  1.4MIL (1oz)  2.8MIL (2oz)  N/A

DRILLING:  
 REFERENCE:  AS SHOWN  NC\_DRILL FILES  
 PTH MIN COPPER THICKNESS:  1MIL  OTHER \_\_\_\_\_

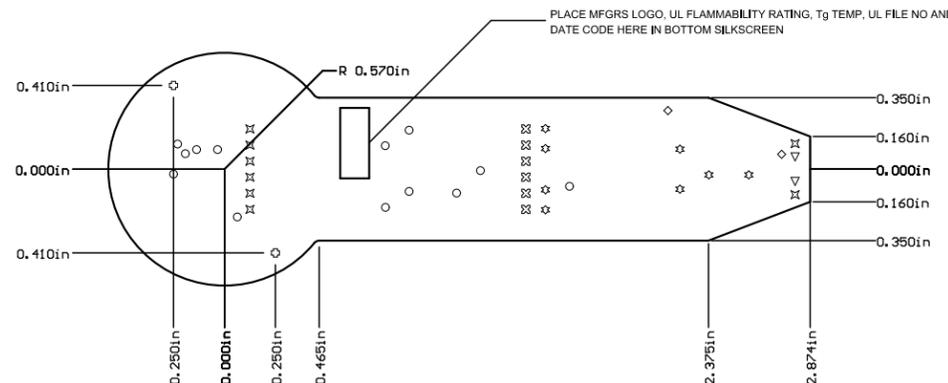
BOARD FINISH:  
 SILKSCREEN:  TOP  BOTTOM  
 SILKSCREEN COLOR:  WHITE  OTHER BLACK  
 SOLDER RESIST COLOR:  
 GREEN  BLUE  OTHER WHITE

SURFACE FINISH:  IMMERSION GOLD (ENIG)  ENEPIG  
 IMM. TIN/SILVER OR EQUIV  OTHER \_\_\_\_\_

ARRAY/PANEL:  CUT AND TRIM PER MECH LAYER 1  
 N.C. ROUTE  V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:  
 ANSI IPC-A-600F CLASS ->  1  2  3  
 UL 94V-0  RoHS  OTHER \_\_\_\_\_

ADDITIONAL REQUIREMENTS:  
 MICROSECTION:  YES  
 BARE BOARD ELEC. TEST:  NONE  REQUIRED  PER ORDER  
 MANUFACTURER'S UL:  RAIL  METAL  SILK



Symbol	Hit Count	Tool Size	Plated	Hole Type
○	13	16mil (0.406mm)	PTH	Round
⊗	8	32mil (0.813mm)	PTH	Round
▽	2	33.465mil (0.85mm)	PTH	Round
⊗	6	35mil (0.889mm)	PTH	Round
☆	8	37mil (0.94mm)	PTH	Round
◇	2	43.307mil (1.1mm)	PTH	Round
⊕	2	93mil (2.362mm)	NPTH	Round
	<b>41 Total</b>			

Drill Table



PROJECT TITLE:  
Two-Switch Dual FR4

DESIGNED FOR:  
Public Release

FILE NAME:  
PMP6025E2.PcbDoc

ENGINEER:  
Irwin Nederbragt

LAYOUT BY:  
Kerry Murphy

SCALE: 1.00

ALTIUM DESIGNER VERSION:  
14.3.14.34663