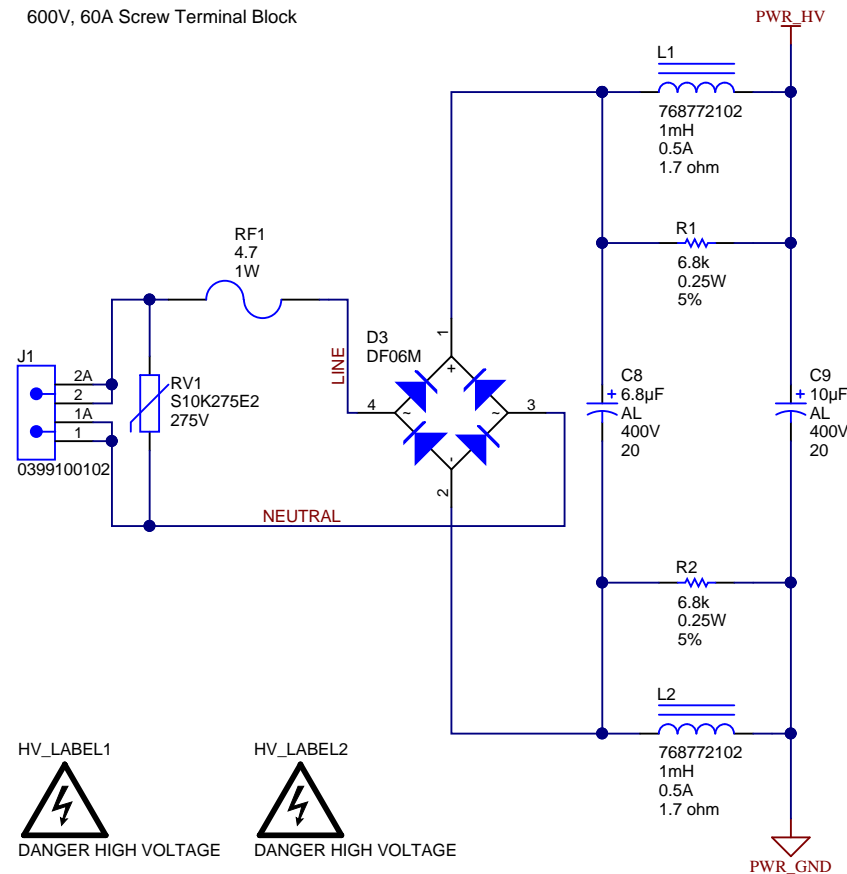


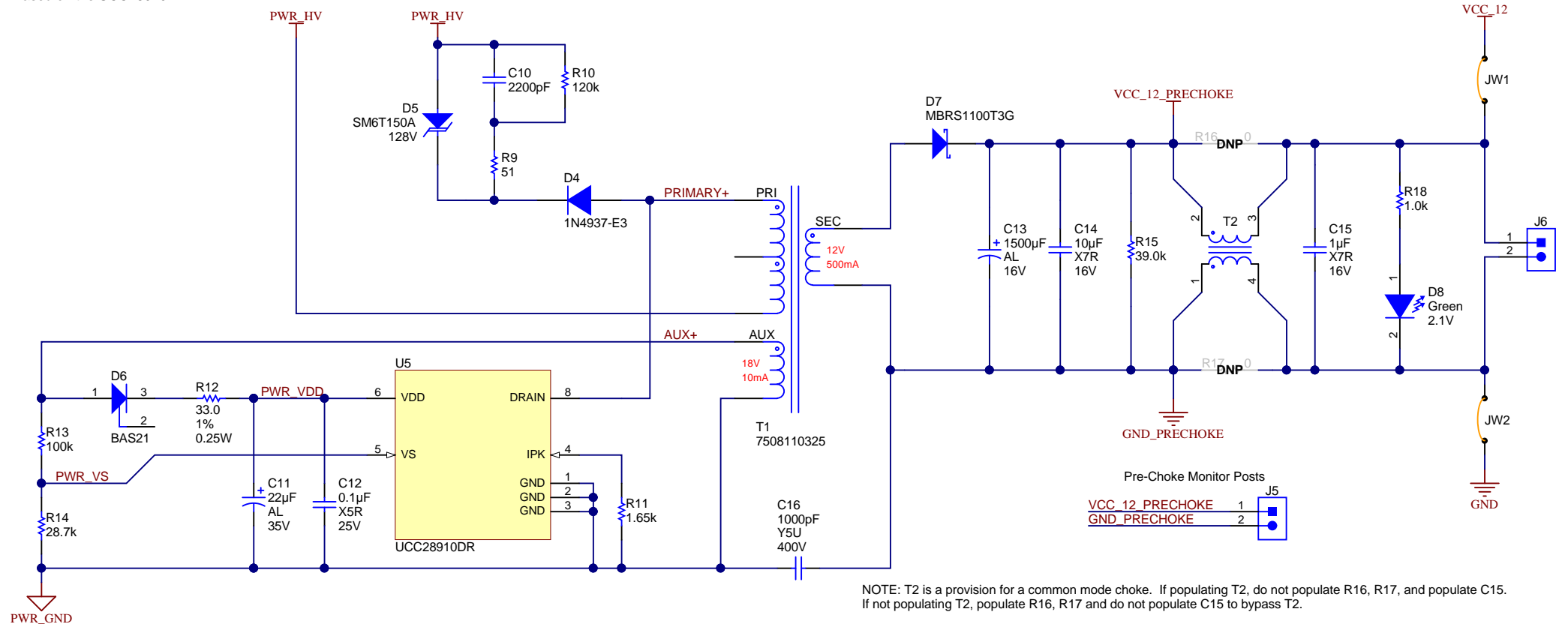
PSM Power Entry Terminal

600V, 60A Screw Terminal Block



12VDC Supply

Universal Offline Flyback Converter with Primary Side Regulation
Based on the UCC28910FBEVM



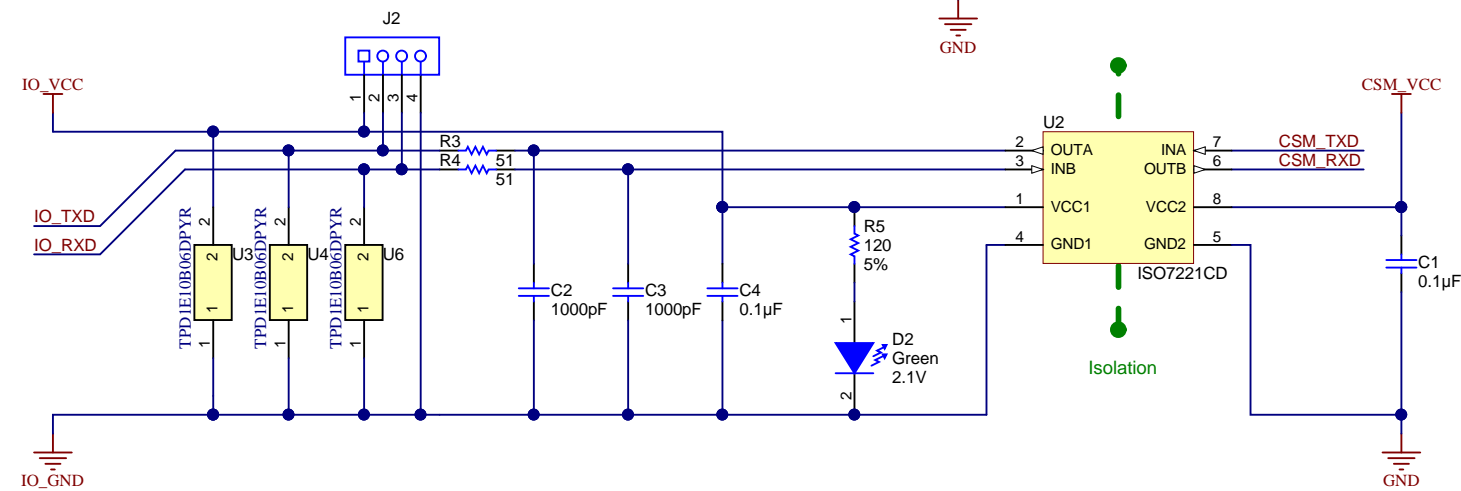
PSM Isolated IO Terminal

This terminal provides access to a UART port to communicate with the CSM.
The UART interface is isolated, and a 2.8V to 5V rail must be provided.

NOTE: TVS protection diodes U3, U4, and U6 make up the primary ESD clamp in the IO domain.

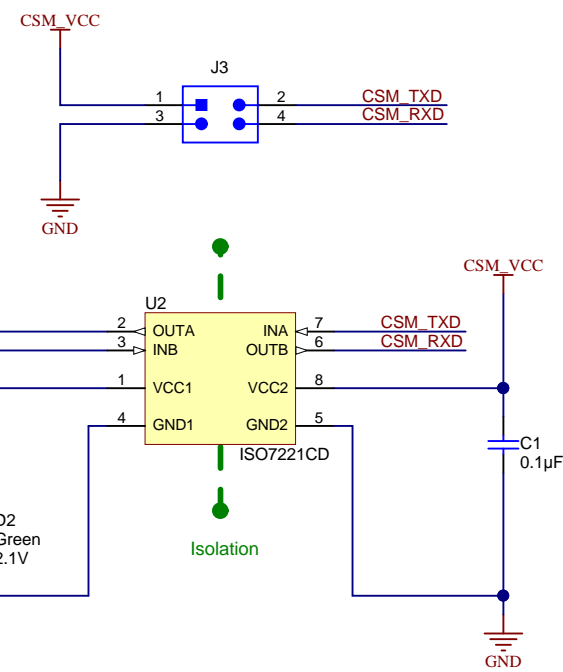
NOTE: Resistors R3/R4 form a low pass filter with capacitors C2/C3, with a cutoff frequency of approximately 3 MHz.

NOTE: LED D2 indicates that the isolated (terminal) side of the IO domain is powered.



CSM Interface Headers

2x2 0.1 Pitch Header Interface to the Capacitive Sensing Module (CSM)



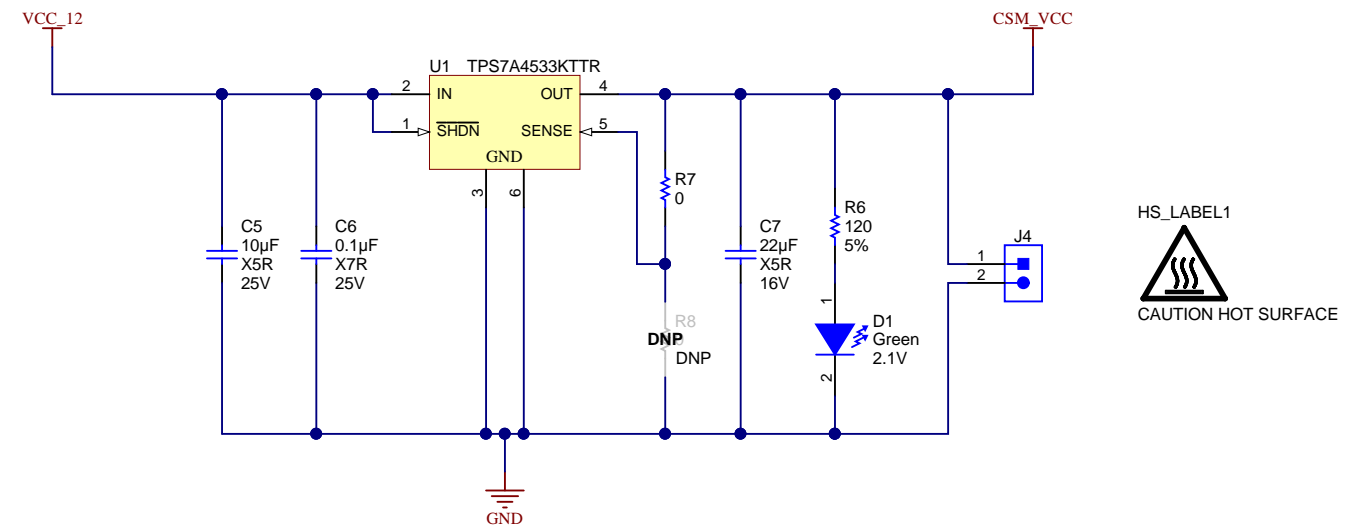
CSM Low Voltage DC Supply

100mA Maximum Drive

Allows for fixed-output or adjustable-output versions of the linear regulator
Supplies CSM and Isolated Communications Block (Internal Side)

NOTE: LED D1 indicates that the CSM is powered.

NOTE: Resistors R7 and R8 form the feedback circuit for the linear regulator U1. For a fixed-ouput regulator, R7 must be a 0-ohm, and R8 must be a DNP. For an adjustable-output regulator, R7 and R8 may be set as required to achieve the desired output voltage.



FCC1
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Orderable: No	Designed for: Public Release	Mod. Date: 5/26/2016
TID #: TIDM-CAPTOUCHEMCREF	Project Title: Noise Tolerant Capacitive Touch HMIs	
Number: PSM-UACTO3P3VDC	Sheet Title: Schematic	
SVN Rev: Version control disabled	Assembly Variant: With Choke	Sheet: 1 of 1
Drawn By: W. Schnoor	File: PSM-UACTO3P3VDC.SchDoc	Size: B
Engineer: Walter Schnoor	Contact:	

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