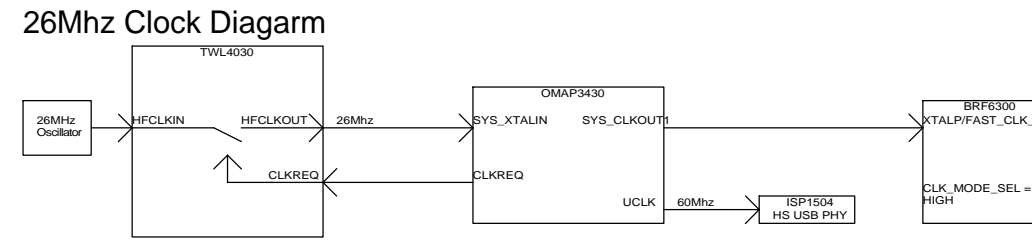


PROJECT: OMAP3430-10	
PART NUMBER: 1007969	
ASSEMBLY NAME: OMAP3430 SOM	
SCHEMATICS: KTL	
TABLE OF CONTENTS	
PAGE	DESCRIPTION
1	BLOCK DIAGRAM
2	EXPANSION BUS
3	USB
4	CPU RAM, LCD, GPMC, MMC
5	FLASH
6	WIRED LAN
7	BLUETOOTH
8	AUDIO, TOUCH
9	POWER
10	CPU PERIPHERALS
11	TEST POINTS
12	ECO LIST



IMPORTANT NOTES ABOUT THIS SCHEMATIC

- DESIGN NOTE: Example text for the design note to show the note inside the colored box.

1) DESIGN NOTES in grey are information notes.
- DESIGN NOTE: Example text for the design note to show the note inside the colored box.

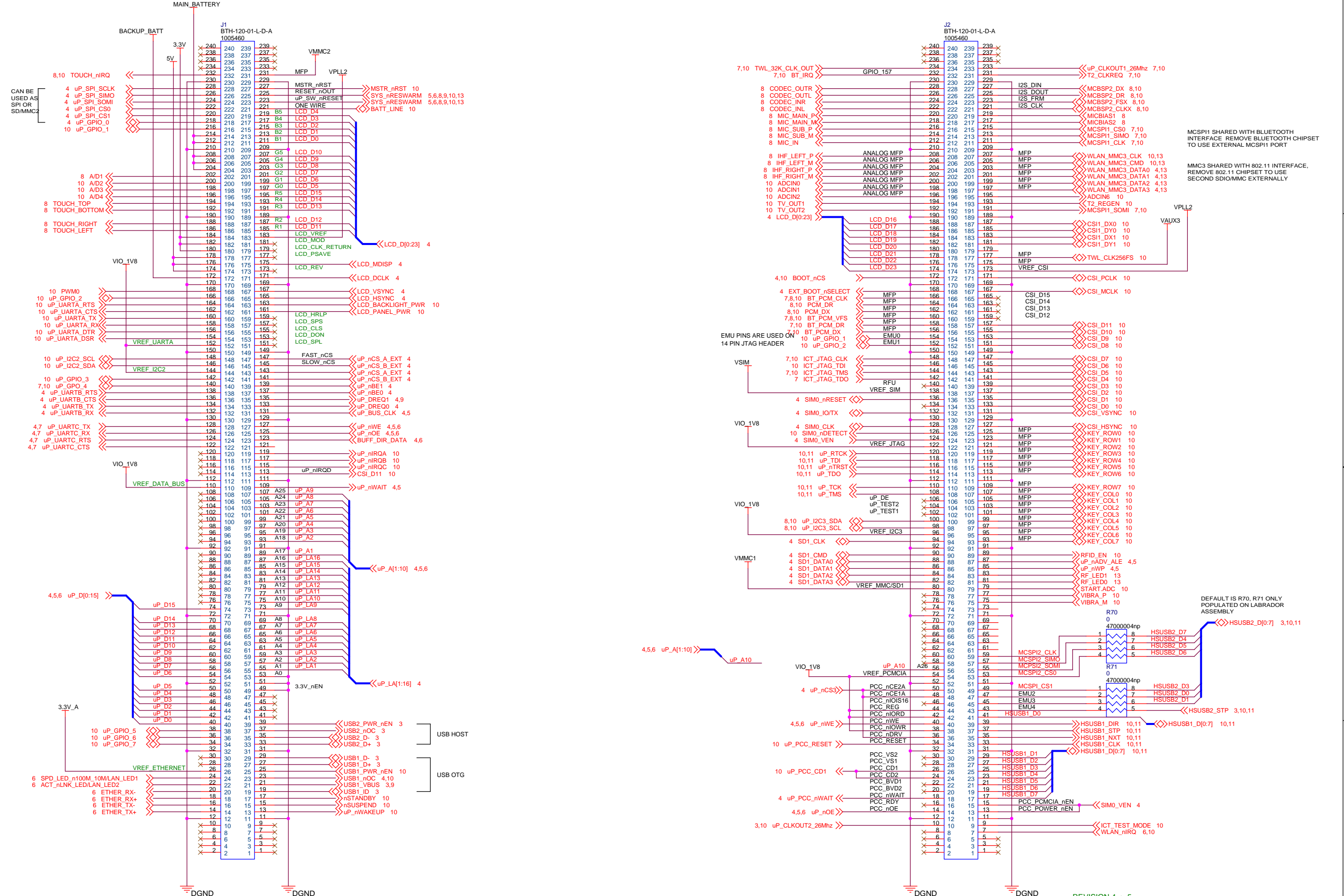
2) DESIGN NOTES in yellow are notes of caution.
- DESIGN NOTE: Example text for the design note to show the note inside the colored box.

3) DESIGN NOTES in red are critical, and must be understood and followed.

IMPORTANT NOTICE:

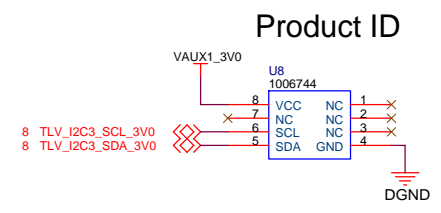
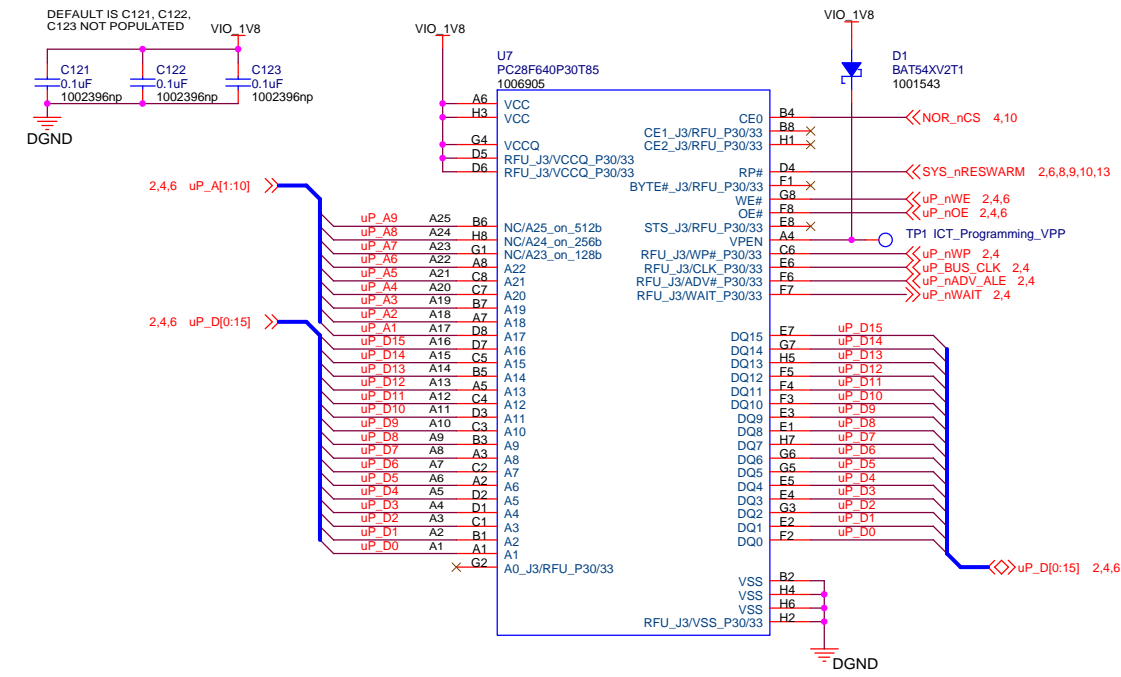
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02 - EXPANSION BUS

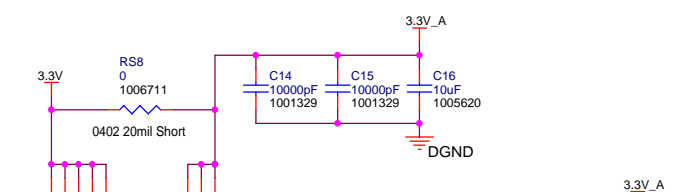
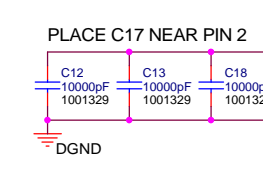
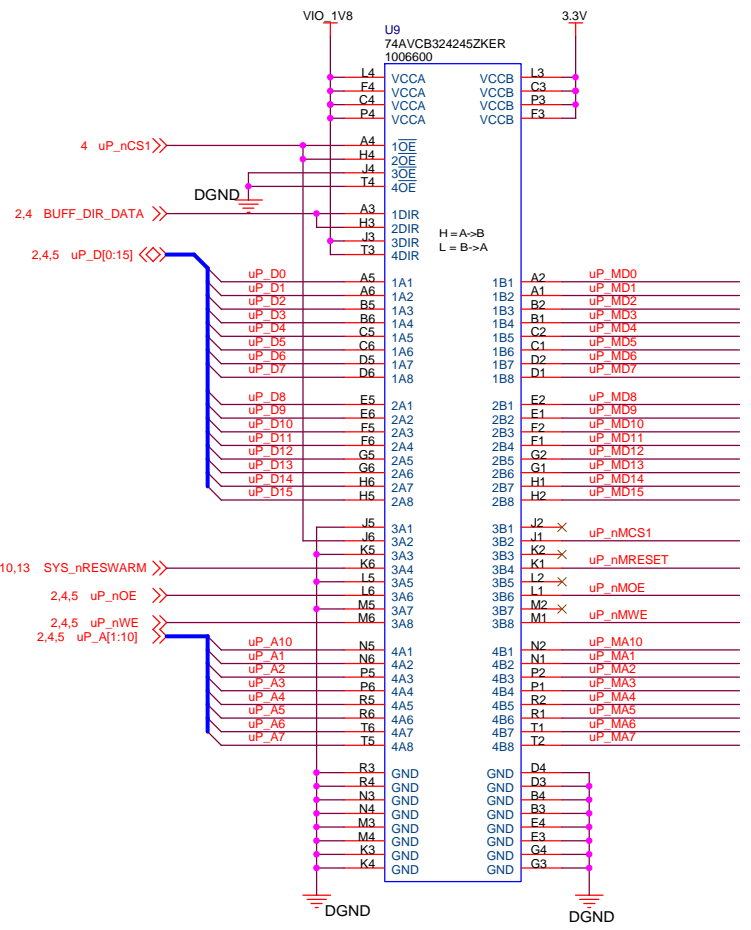


REVISION 4 -> 5
 1) ADDED NETS TWL_CLK256FS, VMMC2, AND AUX3
 2) PCC_PCMCIA_nEN CHANGED TO SIM0_VEN

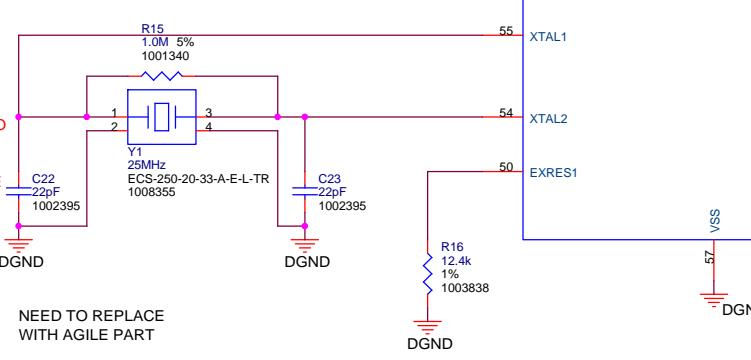
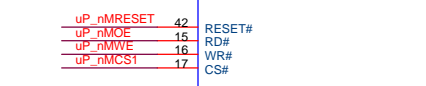
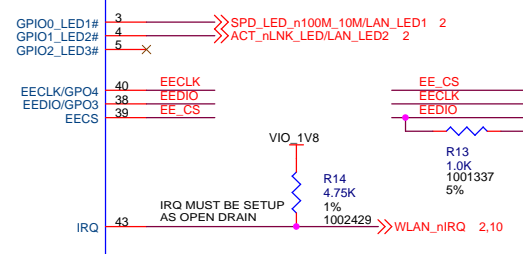
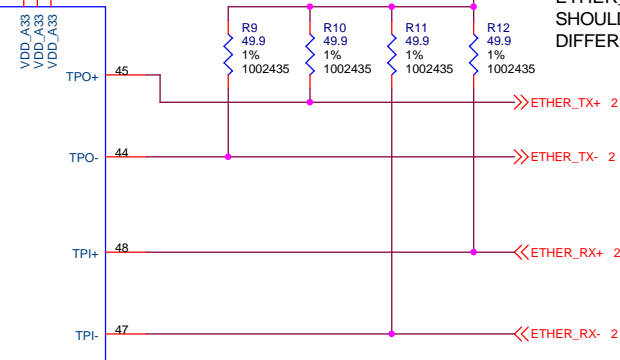
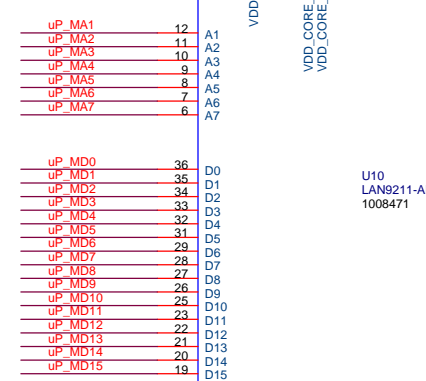
		411 WASHINGTON AVE. N MINNEAPOLIS, MN 55401 PHONE: (612) 672-9495 FAX: (612) 672-9489
		Project OMAP3430-10
Title	OMAP3430 SOM	Sheet 2 Of 12
Number	1007969	Rev 8



06 - WIRED LAN

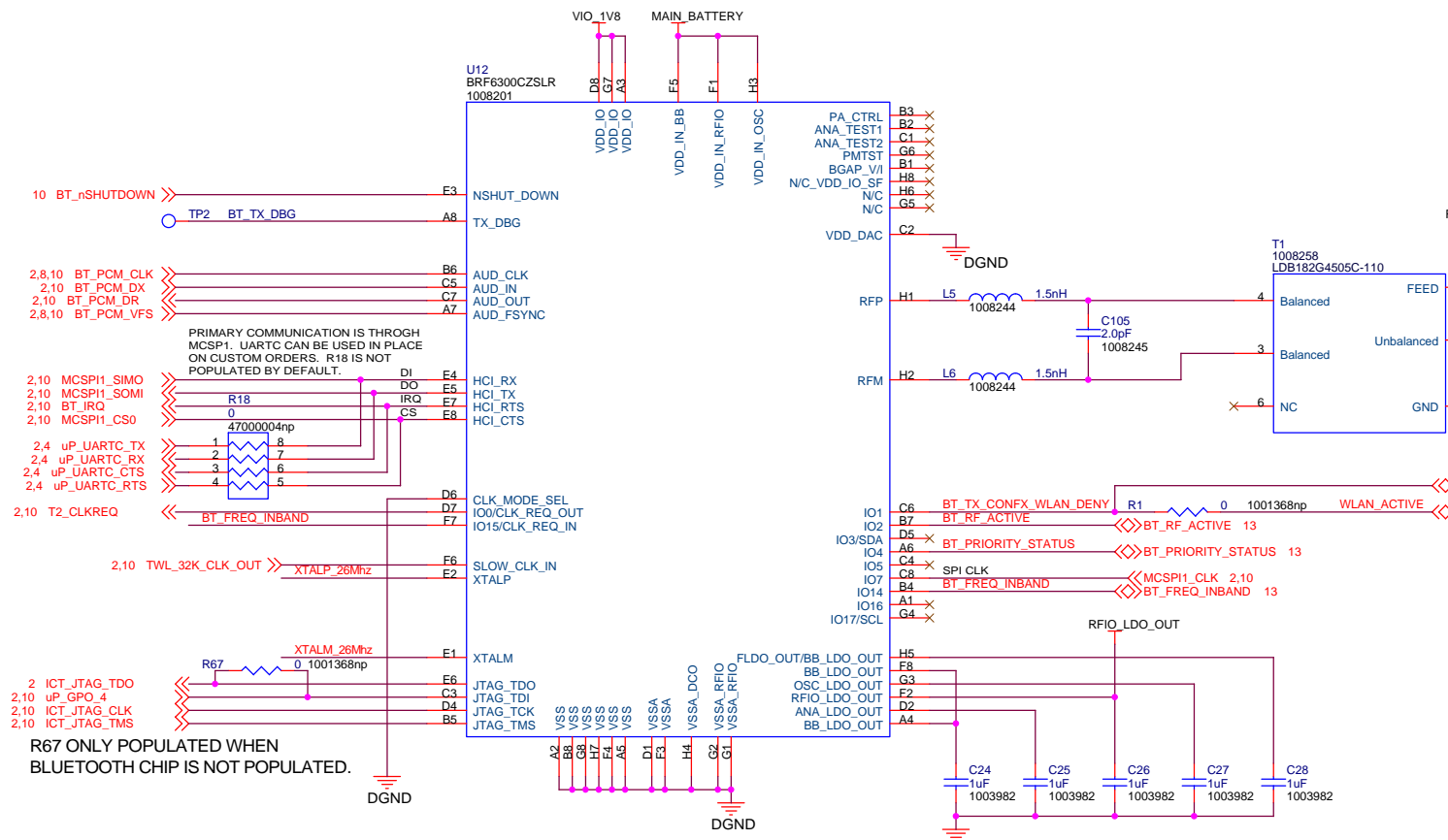
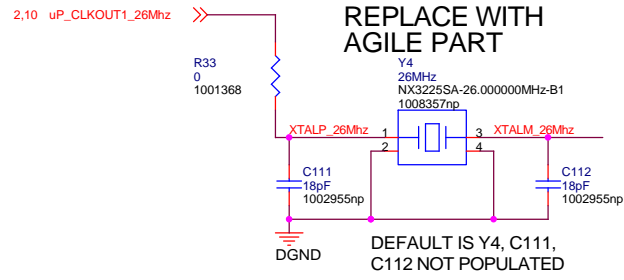


ETHER_TX+, ETHER_TX- AND ETHER_RX+/ETHER_RX- SHOULD BE ROUTED AS DIFFERENTIAL PAIRS.

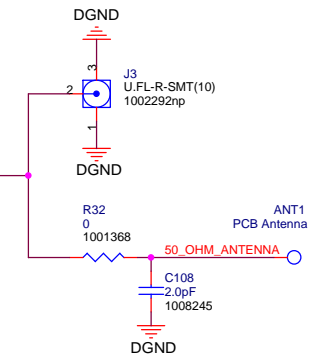


NOTE: Components on this page are not populated by default. See appropriate BOM for exact population for your specific product.

07 - BLUETOOTH



DEFAULT IS J3 NOT POPULATED, R32 PROVIDES CONNECTION TO MEANDER STYLE PCB ANTENNA



TO CHANGE TO FROM 4 TO 2 WIRE CO-EXISTANCE: POPULATE R1 AND TRISTATE PIO4 ON 802.11 CHIPSET

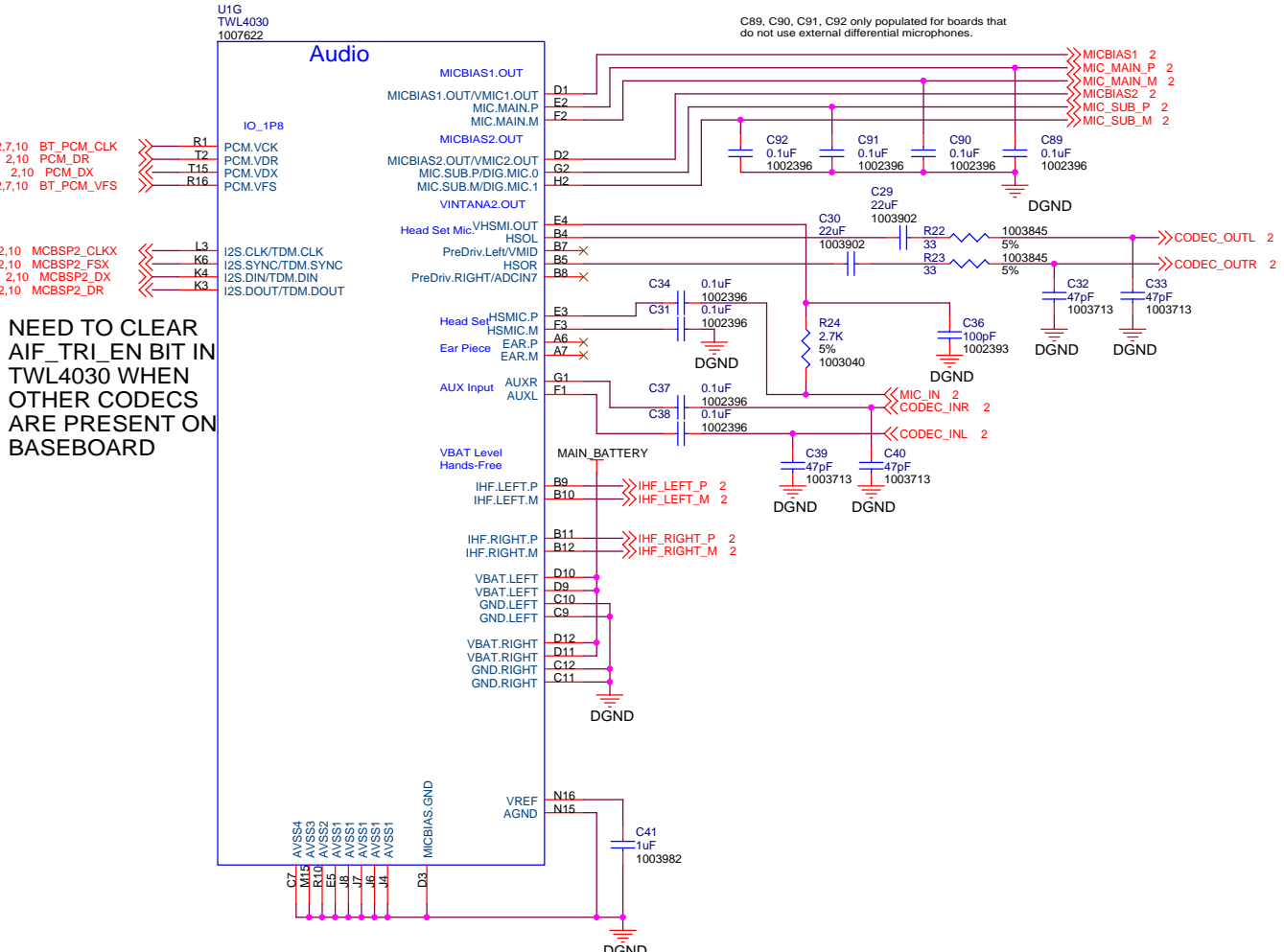
802.11	BT
2 WIRE	PIO3 -> IO1 PIO6 <- IO15
4 WIRE	PIO4 -> IO1 PIO5 <- IO4 PIO6 <- IO14 PIO7 <- IO2

- 10 BT_nSHUTDOWN >> E3 NSHUT_DOWN
- TP2 BT_TX_DBG >> A8 TX_DBG
- 2,8,10 BT_PCM_CLK >> B6 AUD_CLK
- 2,10 BT_PCM_DX >> C5 AUD_IN
- 2,10 BT_PCM_DR >> C7 AUD_OUT
- 2,8,10 BT_PCM_VFS >> A7 AUD_FSYNC
- 2,10 MCSPI1_SIMO >> DI E4 HCI_RX
- 2,10 MCSPI1_SOMI >> DO E5 HCI_TX
- 2,10 BT_IRQ >> IRQ E7 HCI_RTS
- 2,10 MCSPI1_CS0 >> CS E8 HCI_CTS
- 2,4 uP_UARTC_TX >> 1 47000004np
- 2,4 uP_UARTC_RX >> 2 7
- 2,4 uP_UARTC_CTS >> 3 6
- 2,4 uP_UARTC_RTS >> 4 5
- 2,10 T2_CLKREQ << BT_FREQ_INBAND
- 2,10 TWL_32K_CLK_OUT >> XTALP_26MHz
- 2 ICT_JTAG_TDO >> E6 JTAG_TDO
- 2,10 uP_GPO_4 >> C3 JTAG_TDI
- 2,10 ICT_JTAG_CLK >> D4 JTAG_TCK
- 2,10 ICT_JTAG_TMS >> B5 JTAG_TMS

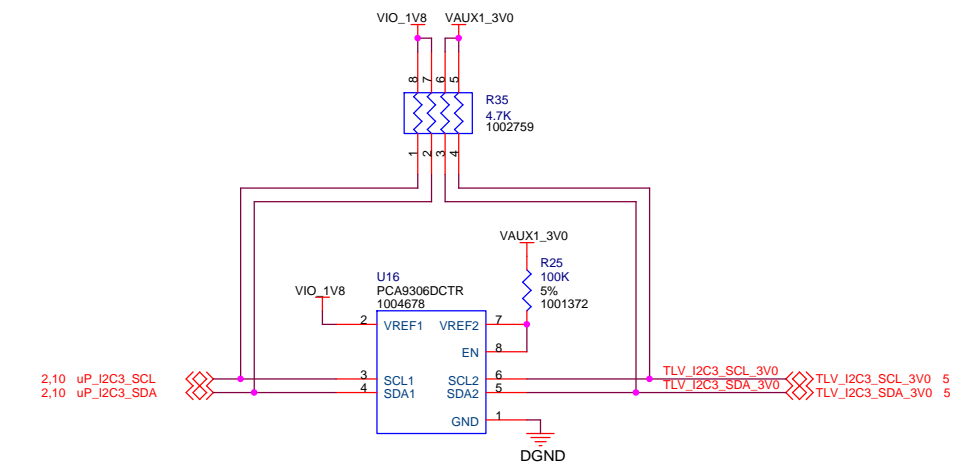
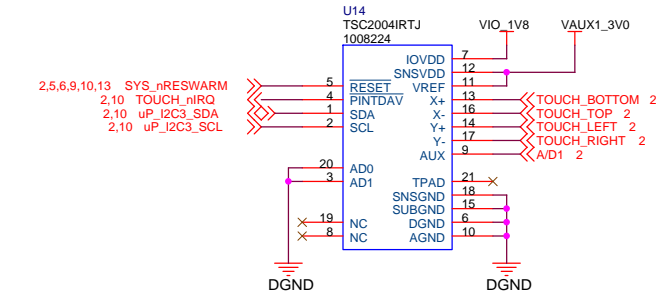
R67 ONLY POPULATED WHEN BLUETOOTH CHIP IS NOT POPULATED.

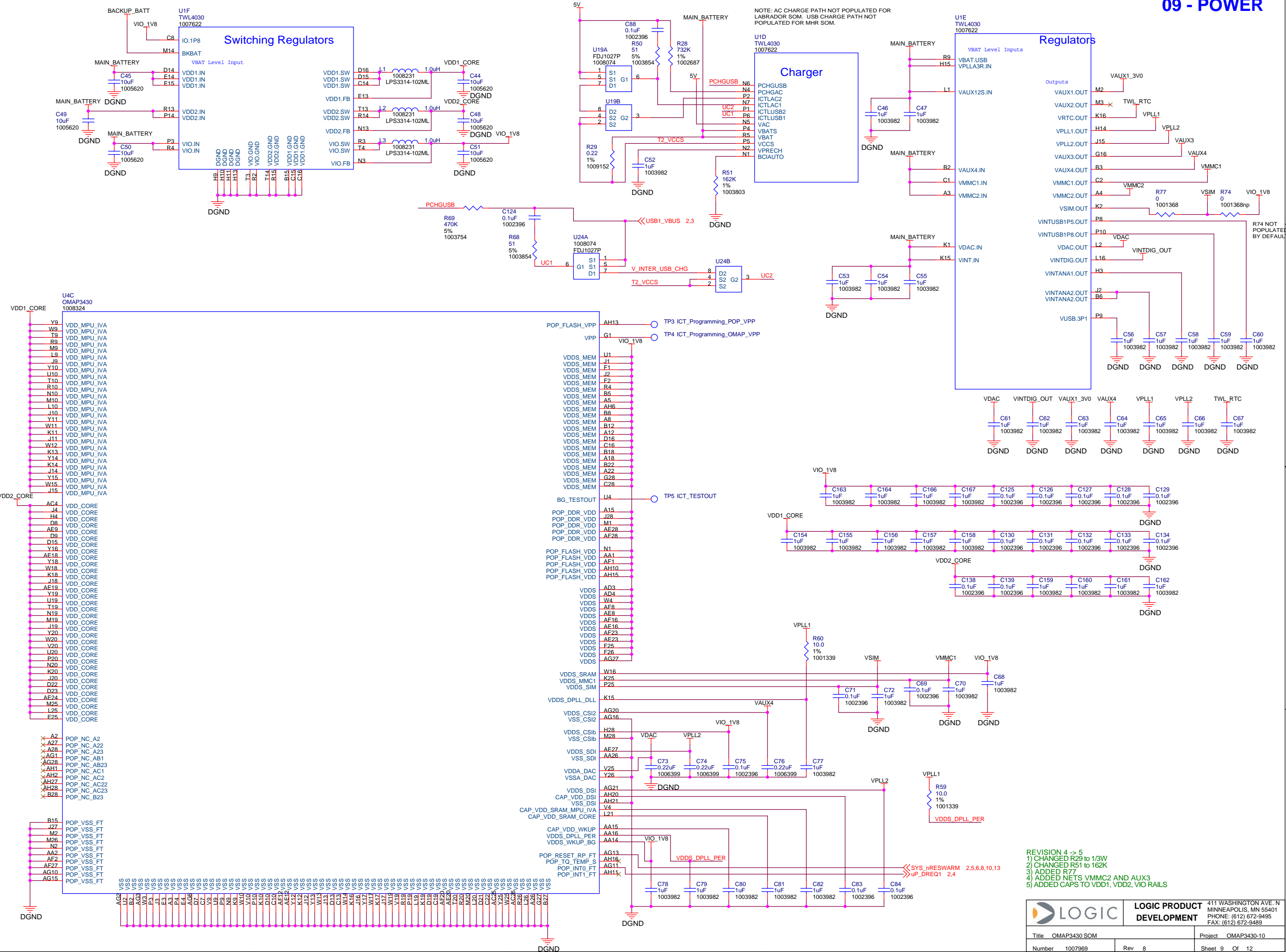
08 - AUDIO, TOUCH

C89, C90, C91, C92 only populated for boards that do not use external differential microphones.



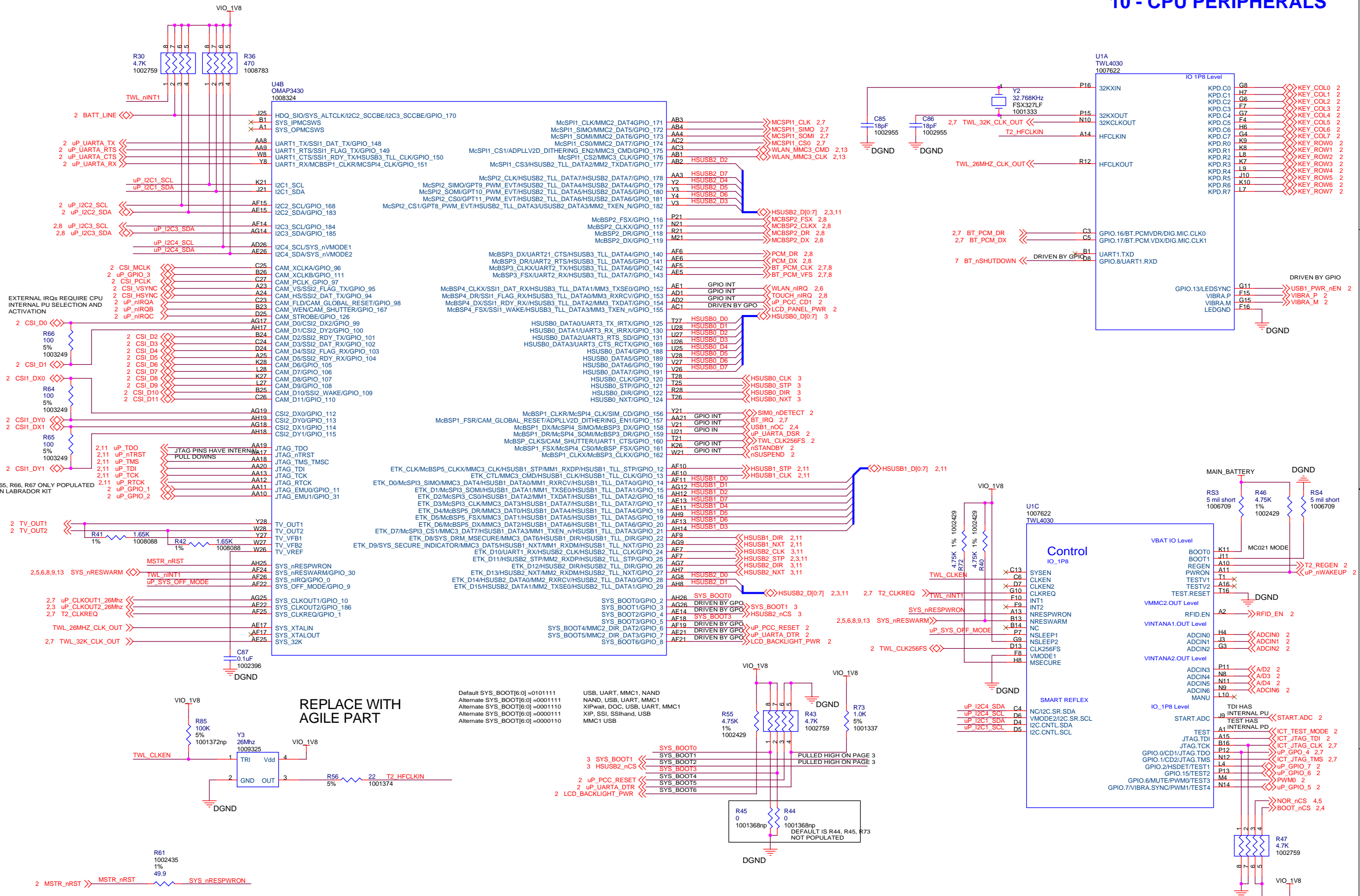
NEED TO CLEAR AIF_TRI_EN BIT IN TWL4030 WHEN OTHER CODECS ARE PRESENT ON BASEBOARD





REVISION 4 -> 5
 1) CHANGED R29 to 1/3W
 2) CHANGED R51 to 162K
 3) ADDED R77
 4) ADDED NETS VMMC2 AND AUX3
 5) ADDED CAPS TO VDD1, VDD2, VIO RAILS

10 - CPU PERIPHERALS



REPLACE WITH AGILE PART

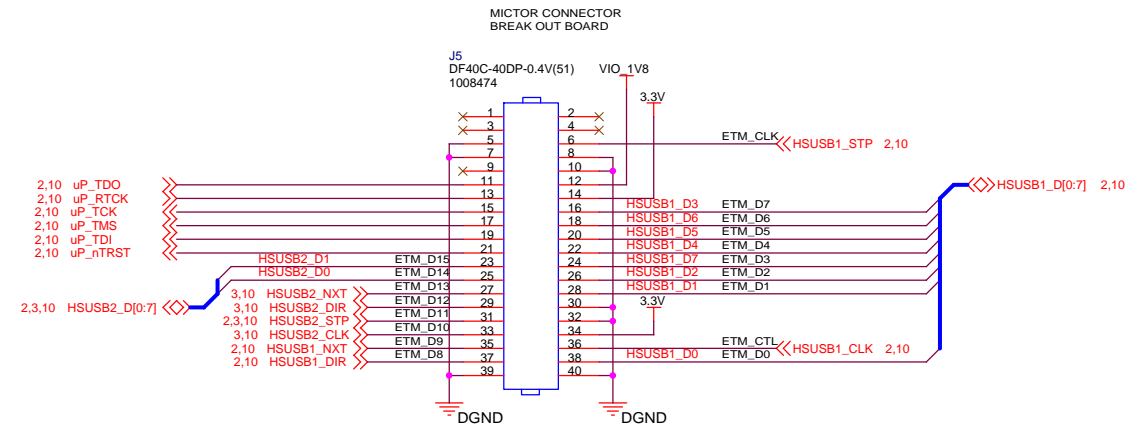
Default SYS_BOOT[6:0]=0101111
 Alternate SYS_BOOT[6:0]=0001111
 Alternate SYS_BOOT[6:0]=0001110
 Alternate SYS_BOOT[6:0]=0000111
 Alternate SYS_BOOT[6:0]=0000110

USB, UART, MMC1, NAND
 NAND, USB, UART, MMC1
 XiPwait, DOC, USB, UART, MMC1
 XiP, SSI, SSIhand, USB
 MMC1 USB

REVISION 6 -> 7
 1) ADDED TWL_CLKEN, R85
 REVISION 5 -> 6
 1) CHANGED FROM CRYSTAL TO OSCILLATOR TO DRIVE T2
 REVISION 4 -> 5
 1) PCC_PMCIA_EN CHANGED TO SYS_BOOT3

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Title	OMAP3430 SOM	Rev	8
Number	1007969	Project	OMAP3430-10
Sheet	10	Of	12

11 - TEST POINTS



Revision Control			
ECO Number	Rev	Description of Change	Date
	1	Initial schematic	06-14-2007
	2	Design review feedback incorporated	09-10-2007
	3	Changes to support Labrador	10-16-2007
C04313	4	TI design review updates	10-25-2007
	5	Updates for Beta	12-31-2007
		ADDED CAPS TO VDD1, VDD2, VIO RAILS ADDED PULLDOWN ON HSUSB0_D5. ADDED PULLDOWN ON U3 PIN F5. CHANGED R29 to 1/3W CHANGED R51 to 162K ADDED R77 ADDED NETS VMMC2 AND AUX3 CONNECTED TWL_CLK256FS TO J2.177 PCC_PCMCIA_nEN CHANGED TO SIMO_VEN control signal ADDED PA, LNA CIRCUIT TO 802.11 INTERFACE	
C04483	6	Additional review updates	01-08-2008
		CHANGED FROM CRYSTAL TO OSCILLATOR TO DRIVE THE OMAP AND T2	
C04597	7	Updates for Beta 2	02-06-2008
		ADDED TP12 ADDED TWL_CLKEN, R85	
C04666	8	Documentation Clean Up	03-06-2008
		MOVED 802.11 TO PAGE 13 UPDATED CIS PARTS (ID 0003793)	