

# **DSP Tools Update**

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## **Three DSP Breakthroughs Announced in 2000**

Impacting Future of Broadband, Emerging & Mass Markets



DSPS Fest 2000

TI C28x<sup>™</sup> DSP Core: The world's first control optimized DSPs

- Up to 400 extended precision MIPs
- Best C/C++ Control Code Efficiency
- Software compatible with C24x<sup>™</sup> DSP



TI C55x<sup>™</sup> DSP Core: The world's lowest mW/MIPS DSPs

- As low as 0.05mW/MIPs for longest battery life
- Best code density
- Software compatible with C54x<sup>™</sup> DSP



TI C64x<sup>™</sup> DSP Core: The world's highest performance DSPs

- Scalable to 1.1GHz
- Best DSP compiler, ease of use
- Software compatible with C62x<sup>™</sup> DSP
- C67x High performance floating point

**TEXAS INSTRUMENTS** 

# **eXpressDSP<sup>™</sup> - DSP Tools**

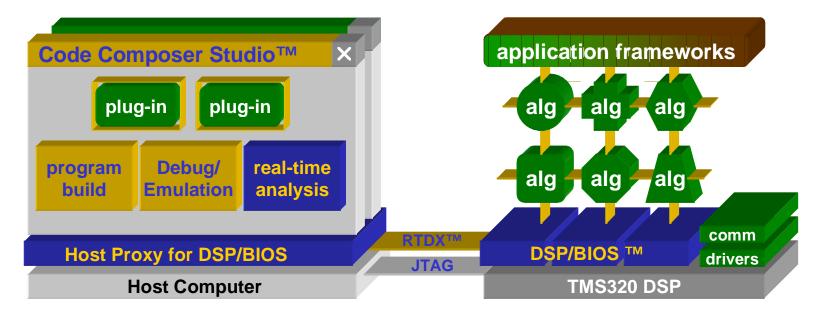
Code Composer Studio IDE

- CCS Plug-ins
- Program Build
- Emulation

DSPS Fest 2000

▶ DSP/BIOS

- DSP Algorithm Standard
- New Development Boards
- Other Highlights

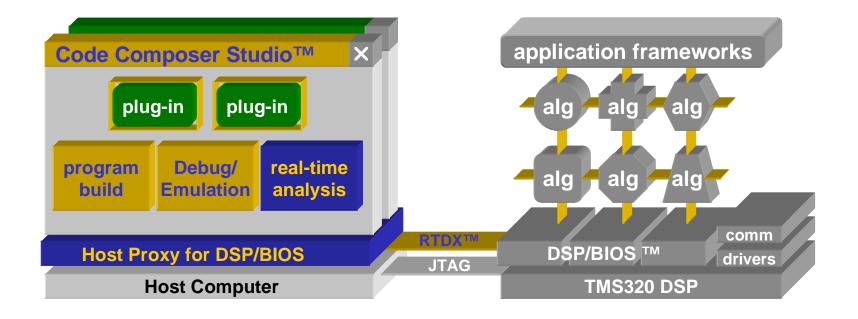


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## **Code Composer Studio IDE**

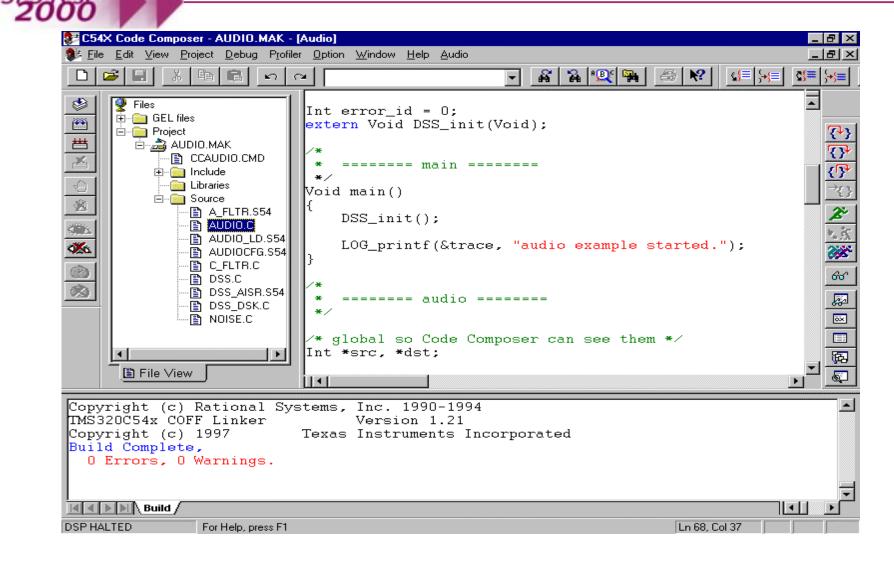


#### Provides edit, debug and build components in a Integrated Development Environment (IDE)



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## Code Composer: Edit, Build, Debug



## **Code Composer: State-of-the-art IDE**

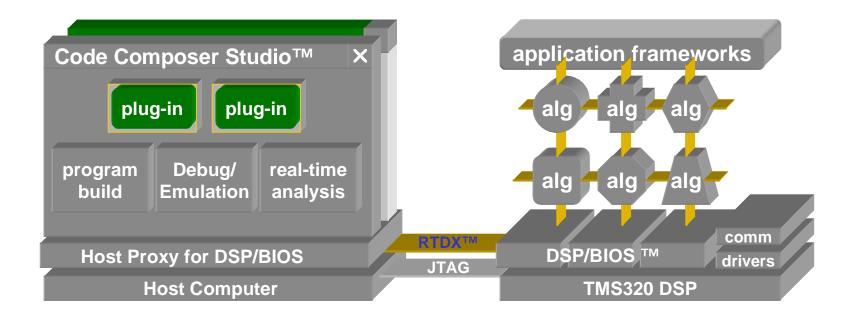


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# **CCS Plug-ins in eXpressDSP**

DSPS Fest

Plug-in: A s/w application that uses the CCS APIs in the Software developer's kit (SDK) & integrates with, customizes or extends the host & target development tool with additional specialized functionality



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# **Types of Plug-ins**

## **Build it Fast!**

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- Application Generation
- UML/SDL Modeling & Design Visualization
- System Simulation & Design
- External Text Editors
- Filter Design Packages
- Hardware Support
- RTOS Support
- Target Content Configuration diagnostics

### Make it Perfect!

- Code Analysis
- Real-time Analysis
- Real-time Instrumentation
- ▶ Run-time Error Analysis
- Signal Generators
- Integrated customized test
   & verification utilities
- Factory/field test &

Hyperception<sup>™</sup> - Visual Application Builder is Compliant

## **Program Build**

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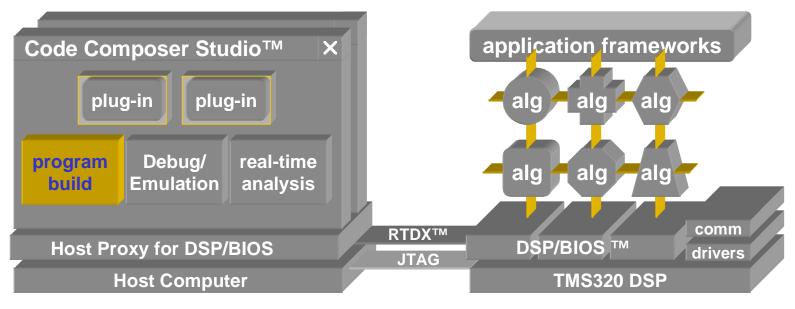
### Best in Class Compiler Performance

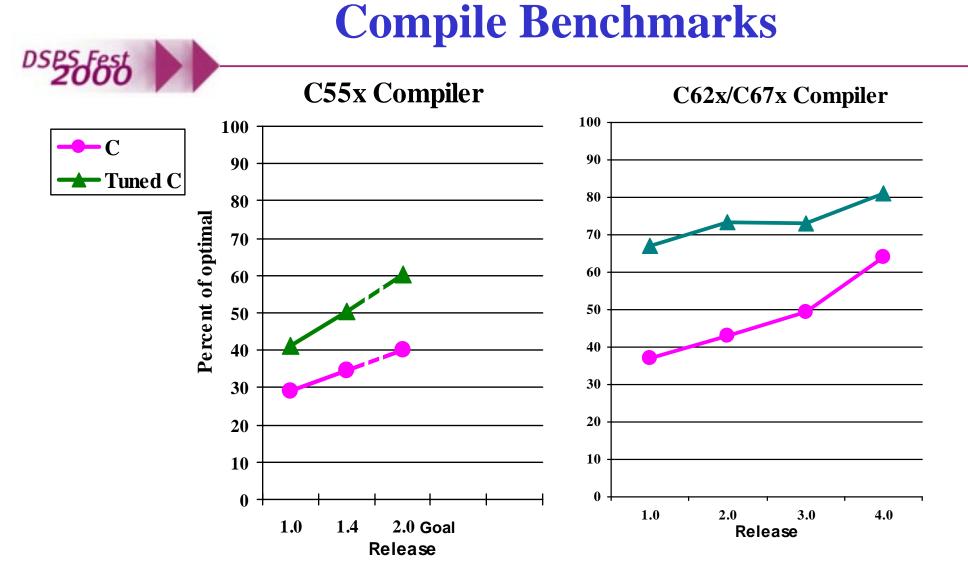
## Profile Based Compiler

Visualize and optimize code size and performance trade-offs

## Visual Linker

Visualize and simplify system memory allocation





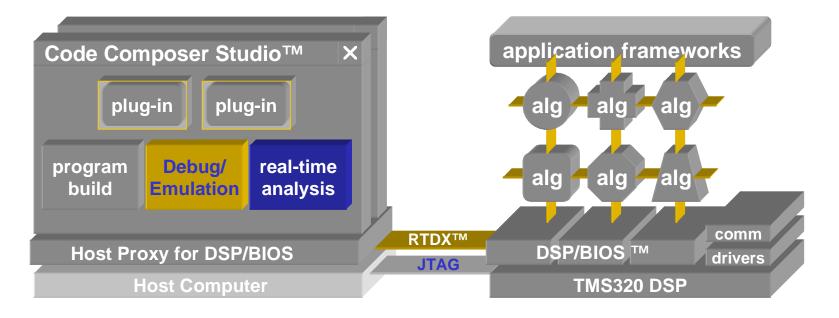
http://www.ti.com/sc/docs/tools/dsp/ccstutor.htm#benchmarks

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## **Emulation Technology**

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- Standard JTAG Emulation
- Real Time Data Exchange (RTDX)
- Advanced Event Triggering
- Real-Time Trace



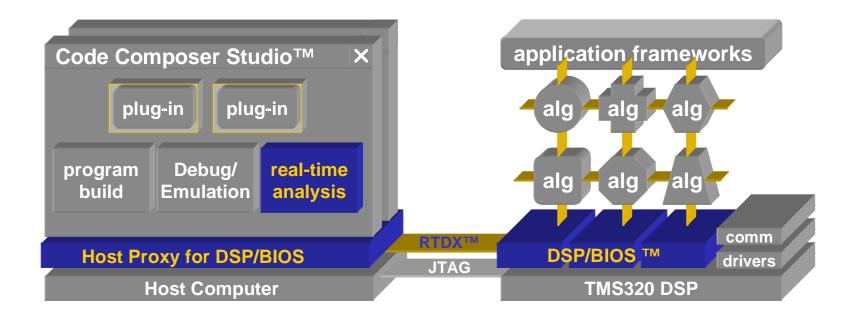
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## **DSP/BIOS**

DSES Fest

scalable, instrumented, real-time kernel

provides a standard software foundation for a growing base of interoperable program components



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## **Mix-and-Match Modules**

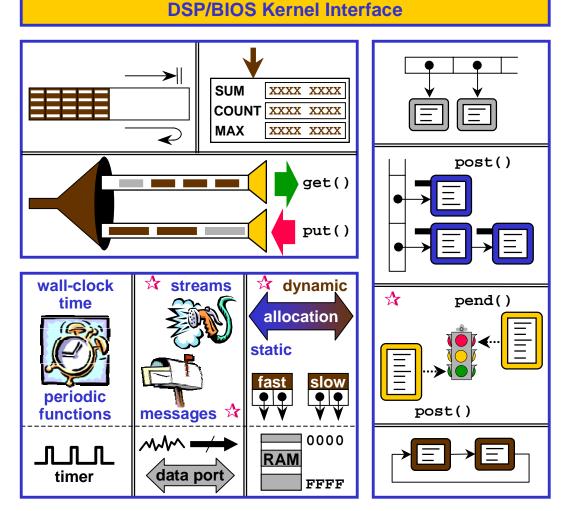
## **REAL-TIME CAPTURE**

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- software event logs
- statistics accumulators
- host cmd/data channels

#### HARDWARE ABSTRACTION

- real-time clock services
   device-independent I/O
   logical memory segments
   MULTIPLE THREADS
- HW+SW interruptssynchronized tasks
- background routines



TARGET TMS320 DSP HARDWARE

☆ = DSP/BIOS II

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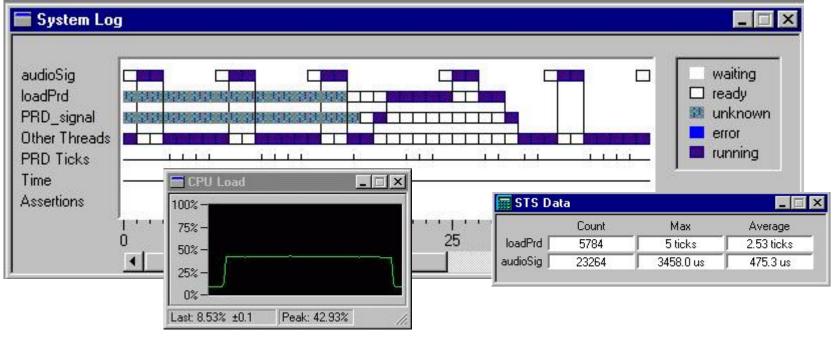
real-time host link

# **DSP/BIOS: Visual real-time analysis**

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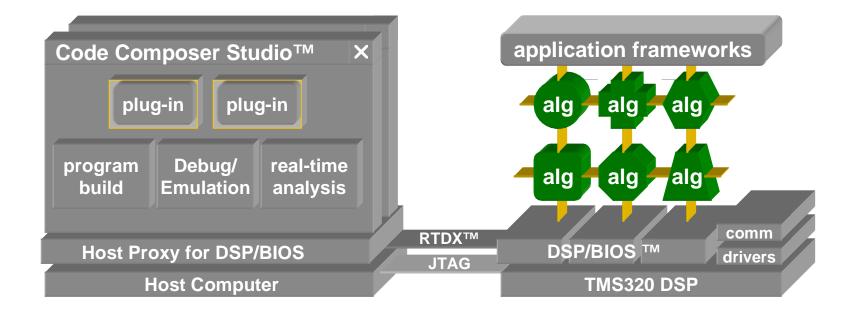
### Implicit diagnostics DSP/BIOS performs *automatically:*

- Real Time Operating System
- Monitor worst-case task execution time
- Provide "software logic analyzer" display of task execution



# TMS320 DSP Algorithm Standard

#### ► A TI published set of programming rules & guidelines for algorithms running on TI DSPs



# **TMS320 DSP Algorithm Standard**

Reduce software integration issues in systems
 Applicable across a multitude of application areas

- >> TTO 3 day workshop 1st class 9/5
- eXpressDSP compliance program
- E-store with Algorithms

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http://www.dspvillage.com

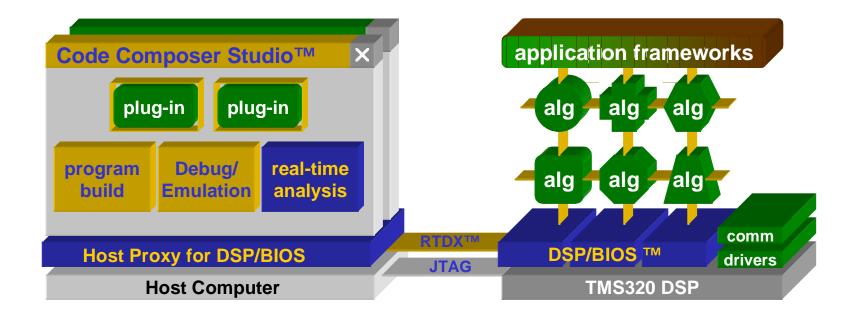


Texas Instruments

## **Development Boards/ Highlights**



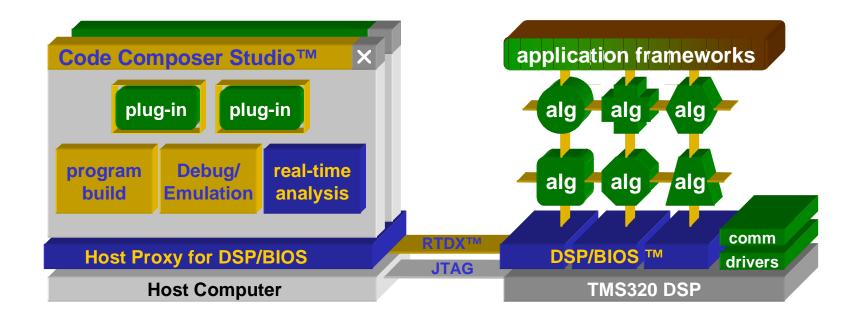
# New Development Boards CCS 2.0 release (1Q01)



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## **eXpressDSP<sup>™</sup> - DSP Tools**





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