

Technology Day Anaheim – March 23, 2010



Time	Session	Signal Chain	Circuit Design and RF	Power	Microcontrollers	OMAP™/DSP	Workshop
8 to 9 a.m.	Registration						
9 to 10:30 a.m.	1	Op Amp Stone Soup: A "Cookbook" Collection of Single-Supply Op Amp Circuits	TI High Reliability, Defense and Aerospace Product Overview	Battery Characteristics, Safety, Cell Balancing and Cell-Based Thermal Sensing	Introduction to Stellaris™ ARM Cortex-M3 MCUs	Video Codecs – What, How and Which	MSP430F5xx Hands-On Workshop (Part 1 of 2)
10:30 to 11 a.m.	Break						
11 a.m. to 12:30 p.m.	2	Inside the Delta-Sigma Converter: Practical Theory and Applications	Low-Power RF Basics	Optimizing High-Frequency Synchronous-Switching Buck Converter Performance	Introduction to Code Composer Studio™ IDE v4.0	Linux Development Tutorial on TI Processors	MSP430F5xx Hands-On Workshop (Part 2 of 2)
12:30 to 2 p.m.	Lunch						
2 to 3:30 p.m.	3	Tackling EMI and RFI at the Board and System Level	High-Speed Layout Considerations	Power-Supply Layout Considerations	Control and Drive Solutions for All Types of Motors	Introducing the Graphics Capabilities of TI SoC's	Code Composer Studio™ v4.0 Hands-On Workshop (Part 1 of 2)
3:30 to 3:45 p.m.	Break						
3:45 to 5:15 p.m.	4	Sensors and the Analog Interface	Circuit Isolation Techniques and Implementations	Choosing the Right Devices to Power your Altera or Xilinx FPGAs	The Future of Low-Power Micros: Energy Harvesting and Ultra-Low-Power Memory (FRAM) <i>by Cymbet</i>	TI's Community Linux Strategy and Partners for DaVinci™, OMAP™ and Sitara™ Processors	Code Composer Studio™ v4.0 Hands-On Workshop (Part 2 of 2)
5:15 to 6:15 p.m.	Reception						

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