



TI Technology Day Milano 2011

Agenda



	Track 1	Track 2	Track 3	Track 4	Track 5
	Motor control	Smart Grid	Power	MCU & Processors	Data acquisition
8:30-9:15	Registration & Exhibition				
9:15-9:30	Opening				
Session 1 9:30-10:15	Motor topologies and an introduction to H bridges	Smart Grid overview	Harvesting photons energy from uW to MW & wireless power	MSP430™ FRAM Technology Overview	High precision signal chain: design criteria
Session 2 10:15-11:00	The H bridge and the DC motor	Metering	Battery characteristics, charger, safety and cell balancing	Concerto: Implementing real-time control and communications with a dual-subsystem microcontroller	Steps from specification to synthesis of anti aliasing filters with Texas Instruments SW tools
11:00-11:30	Break & Exhibition				
Session 3 11:30-12:15	The stepper motor and microstepping	Home automation	Understanding different fuel gauging approaches: voltage based, coulomb counting, Impedance Track	ARM® solutions in TI: STELLARIS® Cortex™-M family	Comprehensive signal chain simulation
Session 4 12:15-13:00	The brushless DC motor basics	Embedded Processing solutions for Smart Grid	Why use NexFET™ MOSFET technology	ARM® solutions in TI: SITARA™ ARM9™, Cortex™ A8 family + Integra & DaVinci™ overview	Verification of simulated signal chain models using the ADC development kits from Texas Instruments
13:00-14:00	Break & Exhibition				
Session 5 14:00-14:45	Design and layout considerations	Low Power Wireless	Powering A/D converter and noise reduction technique	Android™ DevKit for TI platform (Aptasys)	Analysis and test of high speed signal generation systems using Texas Instruments SW/HW tools
Session 6 14:45-15:30	DRV8x offering	Power line communications	Powering processor and FPGA	Software and HW tools overview, Wi-Fi™ and Bluetooth® solutions	Analysis and test of high speed signal acquisition systems using Texas Instruments SW/HW tools
Session 7 15:30-16:15		Real case study: Smart Grid implementation example (Sitek)	Design Spark PCB (RS Components)		
16:15 -16:45	Lottery, Exhibition & Conclusion				