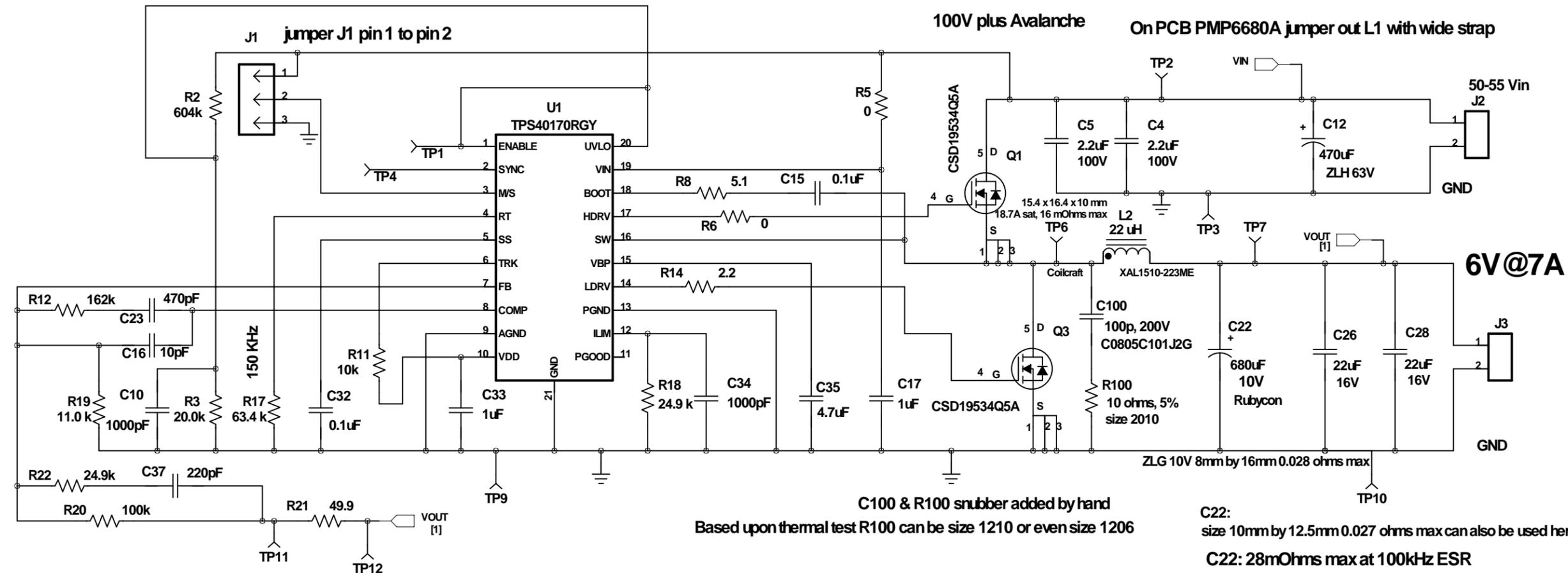


Build on PCB PMP6680 rev A
modified from PMP10828

UVLO: Low inductance / surface mount FETs must be used here, due to the high speed switching
 Do not use TO-220 type FETs
 turn on at 28V_{in} TPS40170 control is Voltage Mode with FF with PWM gain of V_{in}/V_{ramp} about 15
 turn off at 25V_{in} On PCB PMP6680A jumper TP1 to junction of R2/R3
 FET ratings
 14.1 mOhms



V_{ref} for FB is 600mV
 Voltage divider: R19 with R20 shown for 6V output
 TP9, TP12 and TP11 are for Bode plot of control loop
 connect TP11 (white) last
 with values shown loop crossover at 12.5kHz with 75 degrees phase margin
 Loop can be sped up further if needed for dynamic response.

Tested January 15, 2015: See Test Report

C100 & R100 snubber added by hand
 Based upon thermal test R100 can be size 1210 or even size 1206
 C22: size 10mm by 12.5mm 0.027 ohms max can also be used here
 C22: 28mOhms max at 100kHz ESR
 max overall output Z at 17kHz: less than 40mOhms
 L2 Z at 15kHz 2 ohms
 min filter attenuation at 15kHz is 50

Texas Instruments		
Title 50-55Vin 6V 7A TPS40170		
Size B	Number PMP10898	Rev A
Date 1/8/2016	Drawn by Josh Mandelcorn	
Engineer Josh Mandelcorn	Filename PMP10898_REVA_6V.sch	Sheet 1 of 1

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