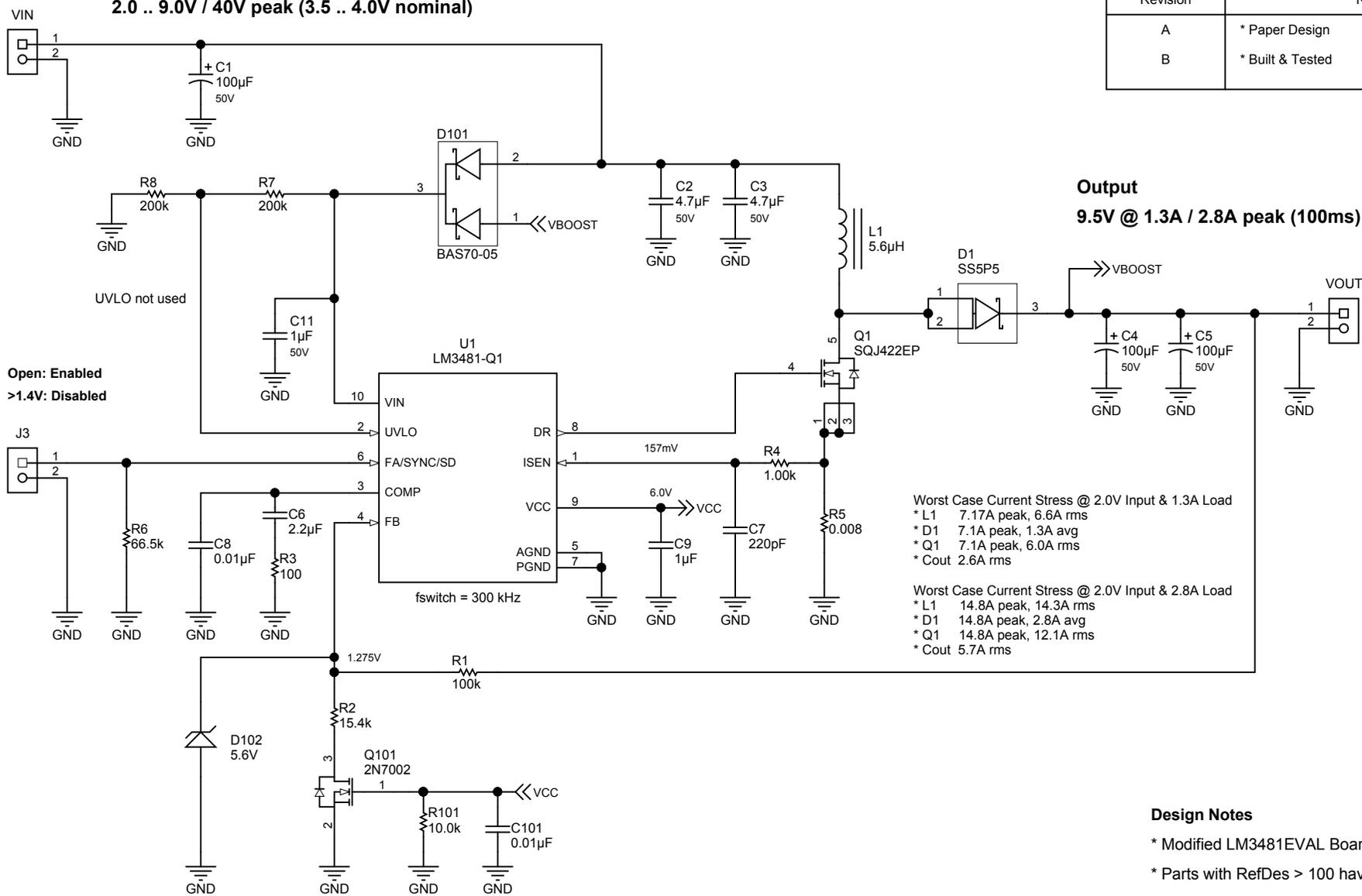


Input
2.0 .. 9.0V / 40V peak (3.5 .. 4.0V nominal)

Revision History	
Revision	Notes
A	* Paper Design
B	* Built & Tested



Output
9.5V @ 1.3A / 2.8A peak (100ms)

Worst Case Current Stress @ 2.0V Input & 1.3A Load
 * L1 7.17A peak, 6.6A rms
 * D1 7.1A peak, 1.3A avg
 * Q1 7.1A peak, 6.0A rms
 * Cout 2.6A rms

Worst Case Current Stress @ 2.0V Input & 2.8A Load
 * L1 14.8A peak, 14.3A rms
 * D1 14.8A peak, 2.8A avg
 * Q1 14.8A peak, 12.1A rms
 * Cout 5.7A rms

Design Notes

- * Modified LM3481EVAL Board
- * Parts with RefDes > 100 have no footprint on PCB

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Designed for: Public Release	Mod. Date: 1/12/2016
Project Title: Automotive Boost	
Number: PMP11784	Rev: B
SVN Rev: Version control disabled	Assembly Variant: [No Variations]
Drawn By:	File: PMP11784RevB.SchDoc
Engineer: Matthias Ulmann	Contact: http://www.ti.com/support
Sheet: 1 of 1	Size: A4



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