Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Orderable:	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-Si	Project Title: TIDA-010210-Si	
Number: TIDA-010210-SiRev: E2	Sheet Title: Coversheet	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 6
Drawn By: Avinash N	File: Cover Sheet-Pg1.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: http://www.ti.com/support	

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Orderable:	Designed for: Public Release	Mod. Date: 3/17/2022	
TID #: TIDA-010210-Si	Project Title: TIDA-010210-Si		
Number: TIDA-010210-SiRev: E2	Sheet Title: Block Diagram		
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 6	
Drawn By: Avinash N	File: Block Diagram-Pg2.SchDoc	Size: B	
Engineer: Riccardo Ruffo	Contact: http://www.ti.com/support		

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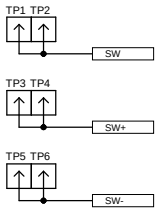
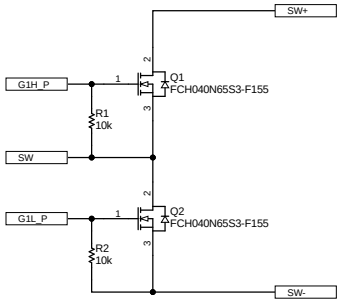
Power Stage





Heat Sink1
[MECH]
SKV585811-AL

Thermal Pad1
[MECH]
SF100-404005

Orderable:	Designed for: Public Release	Mod. Date: 3/17/2022	
TID #: TIDA-010210-Si	Project Title: TIDA-010210-Si		
Number: TIDA-010210-Si	Rev: E2	Sheet Title: Hardware	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 3 of 6	
Drawn By: Avinash N	File: Power Stage-Pg3.SchDoc	Size: B	
Engineer: Riccardo Ruffo	Contact: http://www.ti.com/support		

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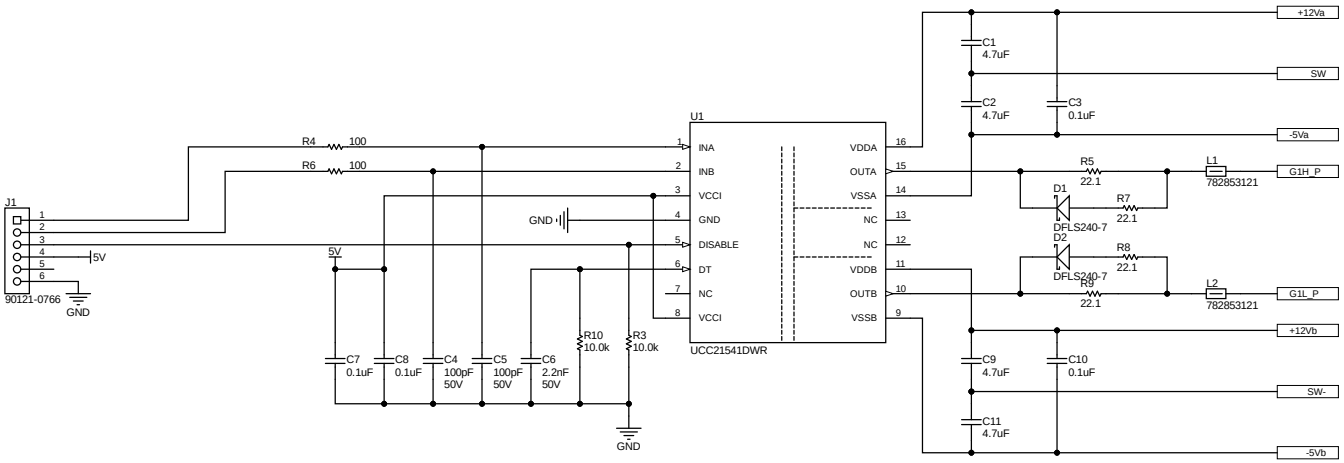
Isolated Gate Driver circuit



Orderable:	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-Si	Project Title: TIDA-010210-Si	
Number: TIDA-010210-SiRev: E2	Sheet Title: Hardware	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 4 of 6
Drawn By: Avinash N	File: Gate Driver Circuit-Pg4.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: http://www.ti.com/support	

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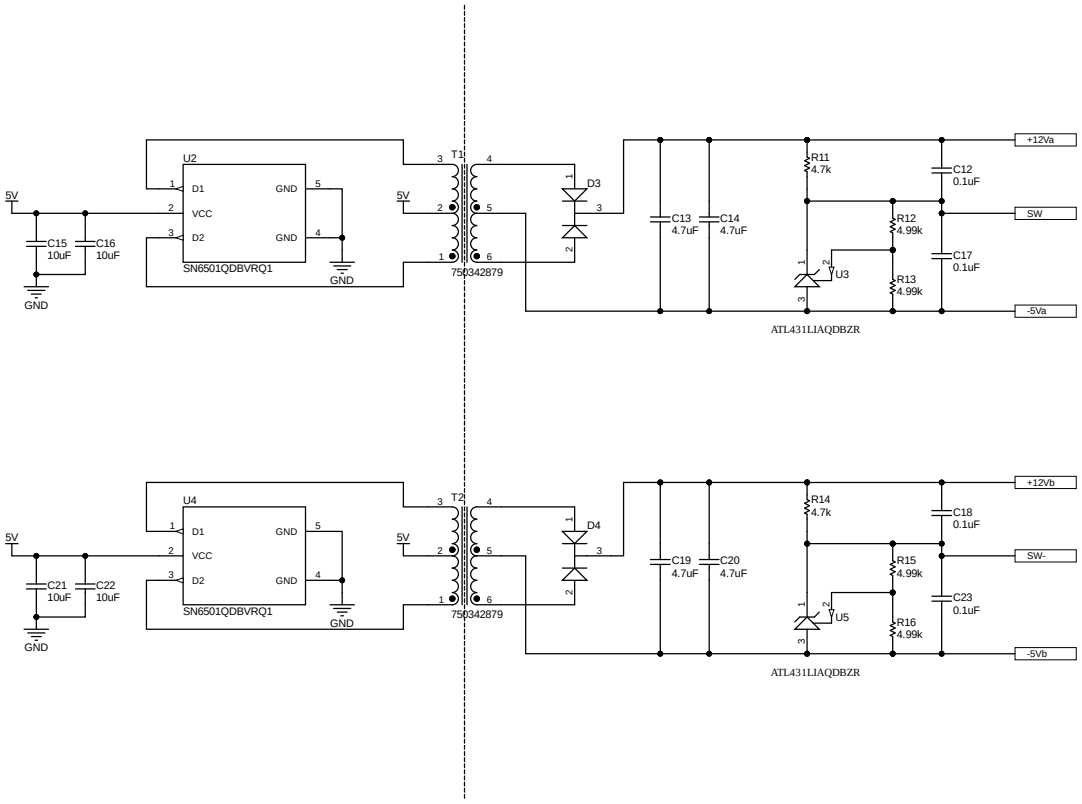
Bias power supply for UCC21530



Orderable:	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-Si	Project Title: TIDA-010210-Si	
Number: TIDA-010210-SiRev: E2	Sheet Title: Hardware	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 5 of 6
Drawn By: Avinash N	File: Bias Power Supply-Pg5.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: http://www.ti.com/support	

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CAUTION HOT SURFACE


DANGER HIGH VOLTAGE

PCB Number: TIDA-010210-Si
PCB Rev: E2


CE Mark

PCB LOGO
FCC disclaimer

PCB LOGO
WEEE logo

Label Table	
Variant	Label Text

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
This Assembly Note will show in the PcbDoc and associated outputs

ZZ3
Assembly Note
This Assembly Note will show in the PcbDoc and associated outputs

ZZ4
Assembly Note
This Assembly Note will show in the PcbDoc and associated outputs

FID4 Fiducial
FID5 Fiducial
FID6 Fiducial
FID7 Fiducial
FID8 Fiducial
FID9 Fiducial

Orderable:	Designed for: Public Release	Mod. Date: 3/17/2022
TID #: TIDA-010210-Si	Project Title: TIDA-010210-Si	
Number: TIDA-010210-SiRev: E2	Sheet Title: Hardware	
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 6 of 6
Drawn By: Avinash N	File: EVM Hardware-Pg6.SchDoc	Size: B
Engineer: Riccardo Ruffo	Contact: http://www.ti.com/support	

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