Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

TPS54202

VIN

3 VIN

Cin

1 GND

5 EN

EN

6 BOOT

Cboot

Lo

2 SW

4 FB

Rfb1

Rfb2

Co

VOUT

Orderable: N/A	Designed for: Public Release	Mod. Date: 6/17/2016
TID #: TIDA-00949	Project Title: 3.3V 1A DC DC module with single layer and TO-24	
Number: TIDA-00949 Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 3
Drawn By:	File: CoverSheet.SchDoc	Size: B
Engineer: Jason Tao	Contact: http://www.ti.com/support	http://www.ti.com

TEXAS INSTRUMENTS
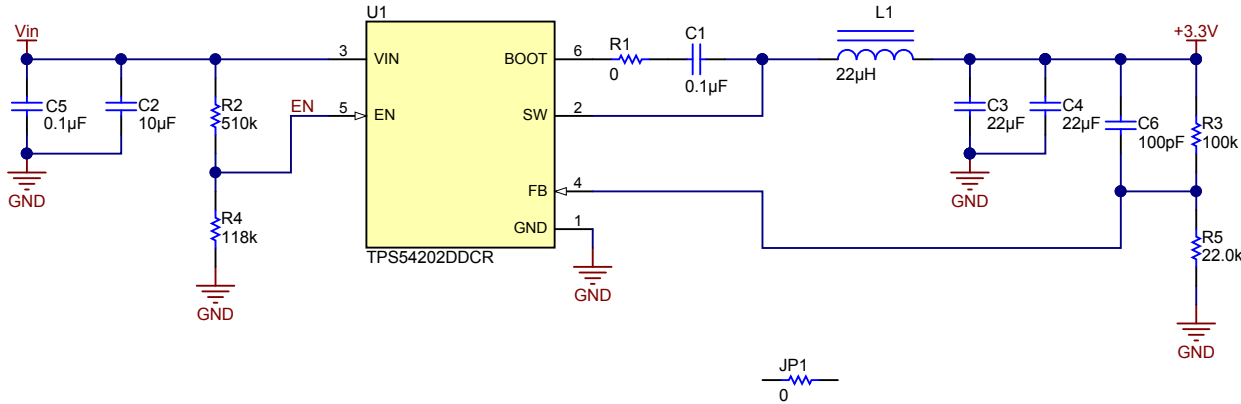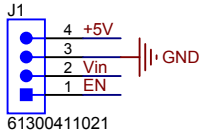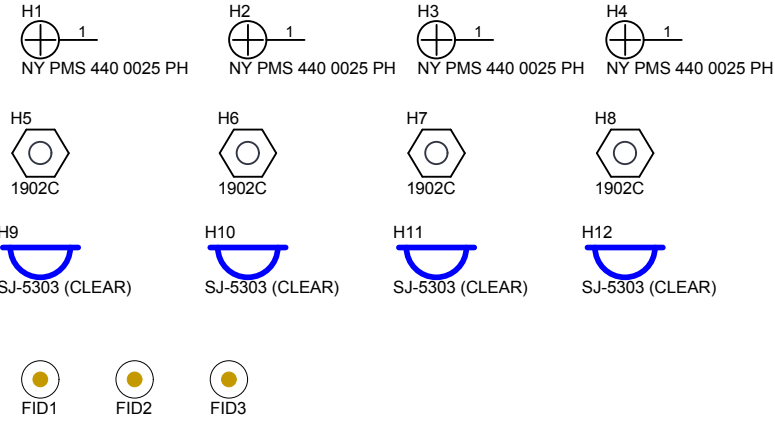
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NOTES:

INPUT VOLTAGE: 7-V to 20-VDC, 12-VDC IS RECOMMENDED
PEAK INPUT CURRENT: 1A

OUTPUT VOLTAGE: 3.3VDC   Nominal
OUTPUT CURRENT: 1A   Nominal
OCP THRESHOLD: 2A
OTP THRESHOLD: 160 degree

H1
1
NY PMS 440 0025 PH

H2
1
NY PMS 440 0025 PH

H3
1
NY PMS 440 0025 PH

H4
1
NY PMS 440 0025 PH

H5
1902C

H6
1902C

H7
1902C

H8
1902C

H9
SJ-5303 (CLEAR)

H10
SJ-5303 (CLEAR)

H11
SJ-5303 (CLEAR)

H12
SJ-5303 (CLEAR)

FID1

FID2

FID3

PCB Number: TIDA-00949
PCB Rev: E1

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Number: TIDA-00949   Rev: E1
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Engineer: Jason Tao

Designed for: Public Release   Mod. Date: 6/17/2016
Project Title: 3.3V 1A DC DC module with single layer and TO-24
Assembly Variant: [No Variations]
Sheet Title:
File: TID_Hardware.SchDoc   Sheet: 3 of 3   Size: B
Contact: http://www.ti.com/support

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