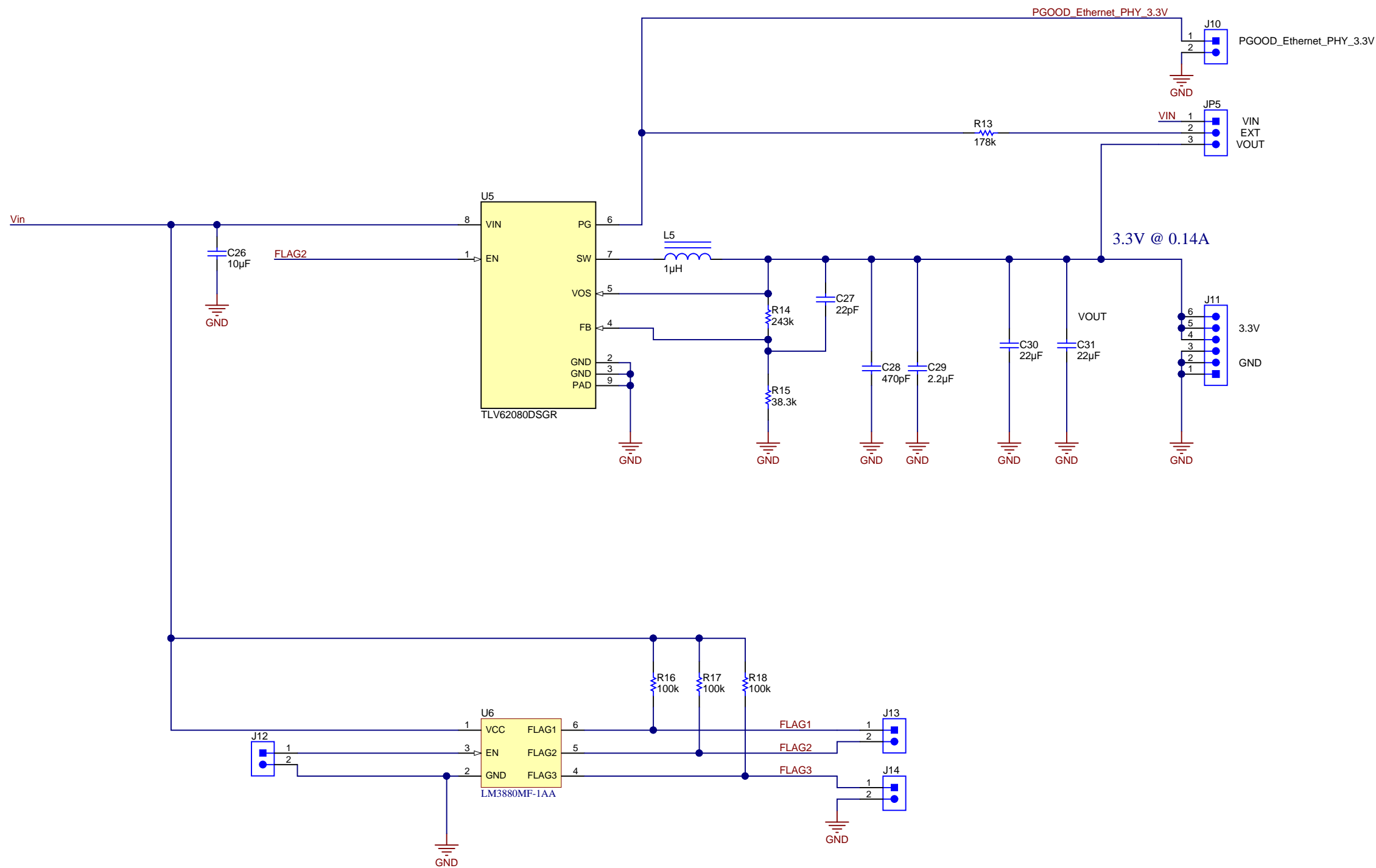


Voltage rail: Ethernet PHY 3.3V



Orderable: <a href="#">ChangeMe!</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 1/10/2018	 <b>TEXAS INSTRUMENTS</b>
TID #: <a href="#">TIDA-01416</a>	Project Title: <a href="#">PMPx_iMX7</a>		
Number: <a href="#">TIDA-01416</a>	Rev: <a href="#">A</a>	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">2</a> of <a href="#">3</a>	
Drawn By: <a href="#">Yann Ducommun</a>	File: <a href="#">TIDA-01416_PMPx_iMX7_A_S3.SchDoc</a>	Size: B	
Engineer: <a href="#">Yann Ducommun</a> Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>			<a href="http://www.ti.com">http://www.ti.com</a> © Texas Instruments 2018

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A

B

C

D

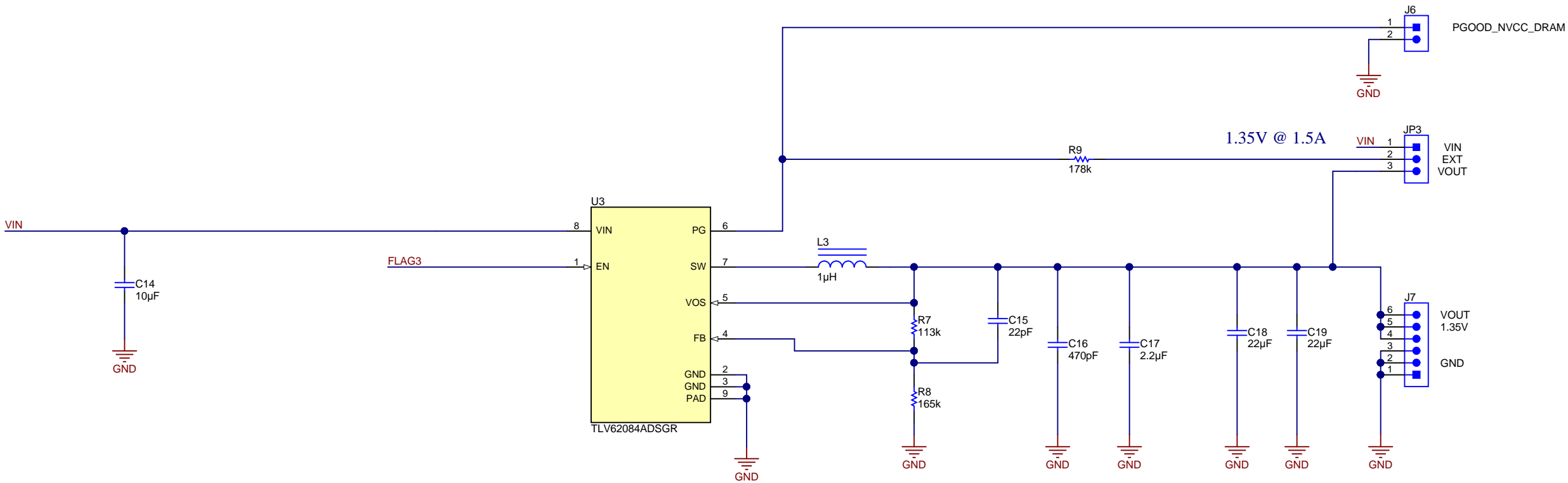
A

B

C

D

Voltage rail: NVCC\_DRAM\_CKE: DDR3L



Voltage rail: VCC\_SD\_IO, NVCC\_3P3 and Ethernet\_PHY 3.3V

